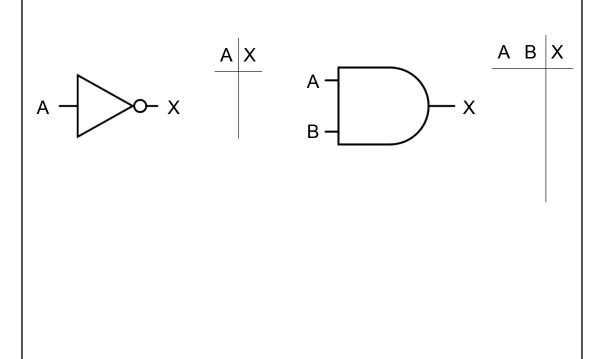
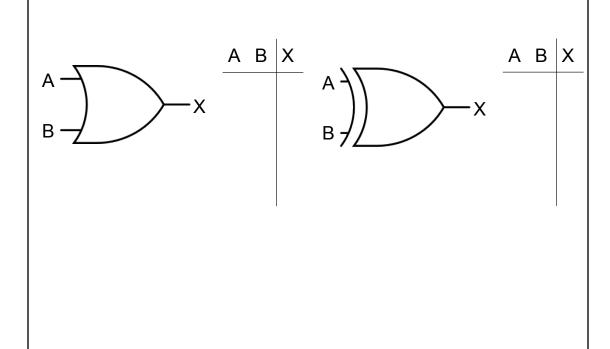


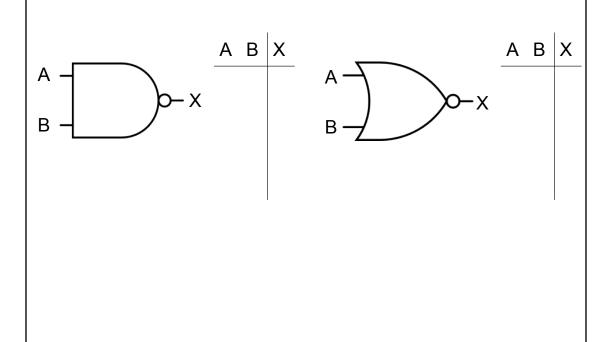
Logic Gates



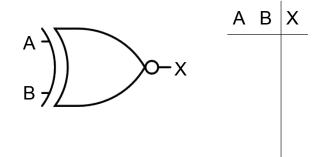
Logic Gates



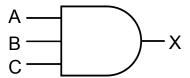
Logic Gates



Logic Gates



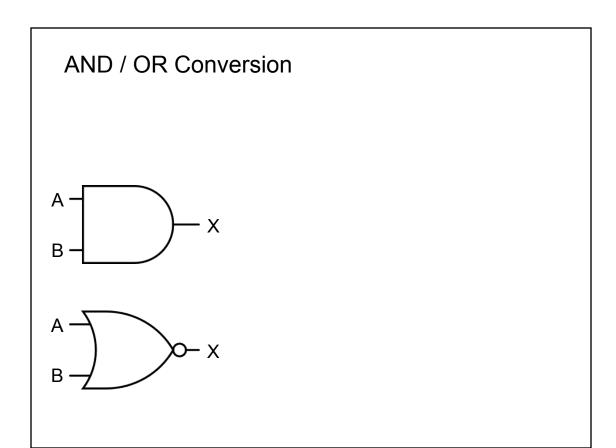
Logic Gates

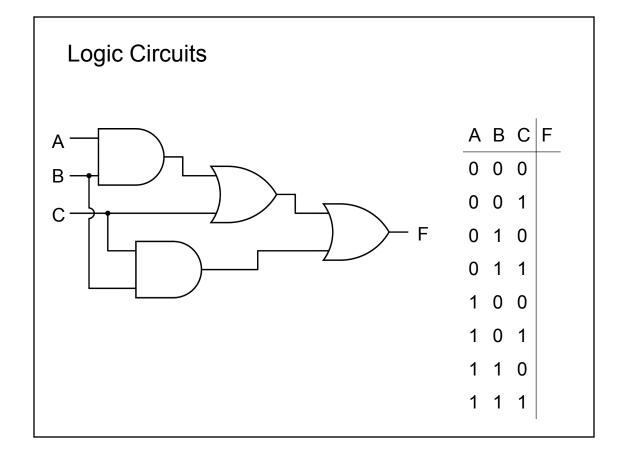


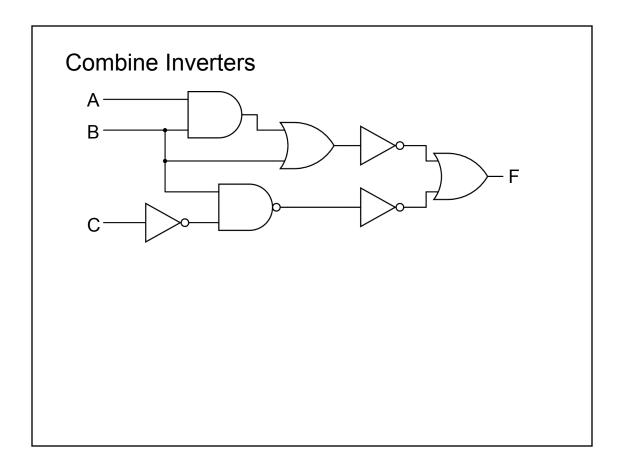
Α	В	С	X
0	0	0	
0	0	1	
0	1	0	
0	1	1	
1	0	0	
1	0	1	
1	1	0	
1	1	1	
			•

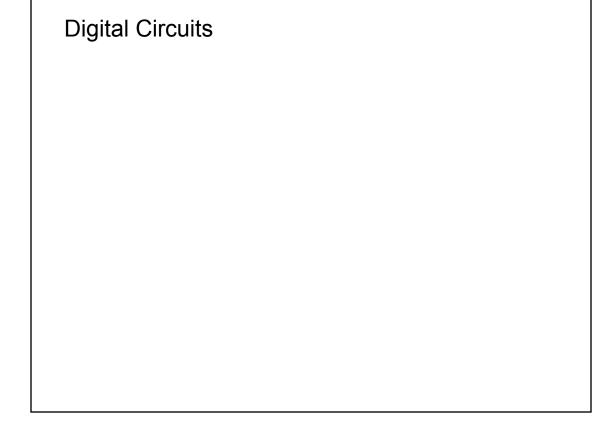
Transistor Count

Gate	Transistors
NOT	
AND	
OR	
XOR	
NAND	
NOR	
XNOR	





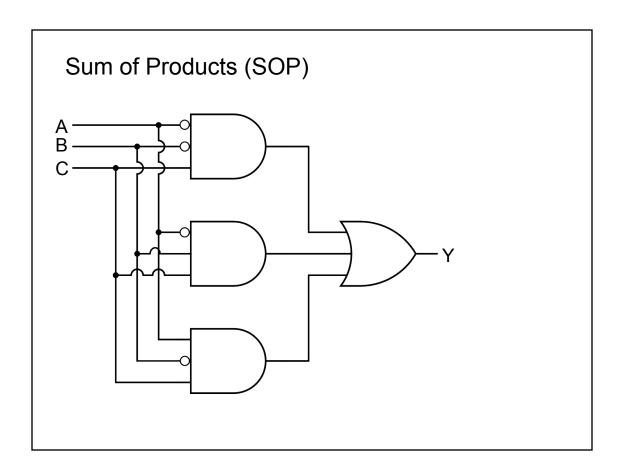




Combinational Circuits				
Digital Expressions				

Sum of Products (SOP)

Sum of Products (SOP)



Product of Sums (POS)

Product of Sums (POS)

- A B C Y
 0 0 0 0
- 0 0 1 1
- 0 1 0 0
- 0 1 1 1
- 1 0 0 0
- 1 0 1 1
- 1 1 0 0
- 1 1 1 0

Product of Sums (POS)

Mintarma	1	Maxterms
wiiileiiis	/	Maxiellis

Term	ABC	Minterm	Maxterm	F
0	000			0
1	001			1
2	010			0
3	011			1
4	100			0
5	101			1
6	110			0
7	111			0

SOP / POS Terms