















Unit mapping of predefined I/O signals DSQC652 do Start Cnv 1 X1:1 di 1-1 X3:1 di 2-1 X3:2 do Start Cnv 2 X1:2 di 3-1 X3:3 do Start Cnv 3 X1:3 do Start Cnv 4 X1:4 di 4-1 X3:4 do Trig Vis 1 di 5-1 X1:5 X3:5 do Trig Vis 2 di 6-1 X1:6 X3:6 do Trig Vis 3 X3:7 X1:7 di 7-1 do Trig Vis 4 X1:8 di 8-1 X3:8 0V 0V X3:9 X1:9 We reserve all rights in this document and in the information contained therein. Reproduction, use or disclosure to third parties without express authority is strictly forbidden. © Copyright 2003 ABB 24V X1:10 24V X3:10 do Man Sync 1 X2:1 di 9-1 X4:1 do Man Sync 2 X2:2 X4:2 di 10-1 do Man Sync 3 X2:3 di 11-1 X4:3 di 12-1 do Man Sync 4 X2:4 X4:4 do Vacuum 1 X2:5 di 13-1 X4:5 X4:6 do Blow 1 X2:6 di 14-1 do Vacuum 2 X2:7 di 15-1 X4:7 do Blow 2 X2:8 di 16-1 X4:8 0V X2:9 0V X4:9 24V X2:10 24V X4:10 Lab/Office: DMRO SE/ 2010-12-16 Plant: Latest revision: Status: ABB Location: + **APPROVED** Unit mapping of predefined I/O signals Sublocation:+ Document no. Rev. Ind Page 9
Next 10
Total 3HAC024480-008 Prepared by, date: Approved by, date:

