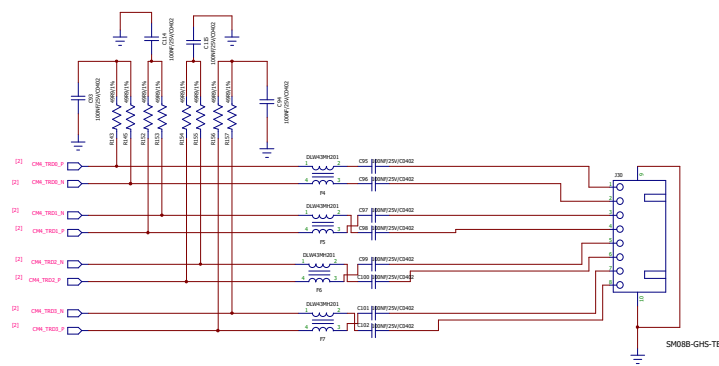


|          |                |
|----------|----------------|
| COMPANY: | Holybro        |
| TITLE:   | CM4-FMUVX-BASE |

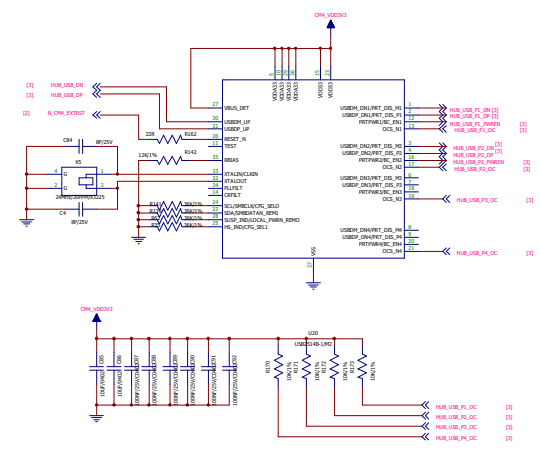
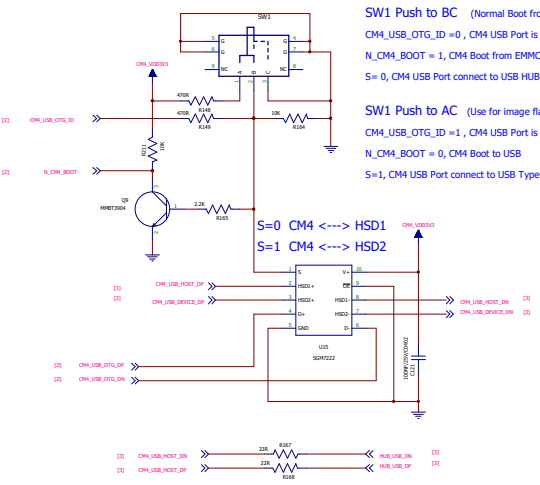
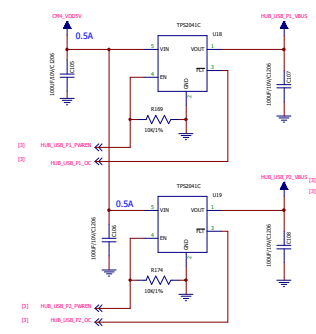
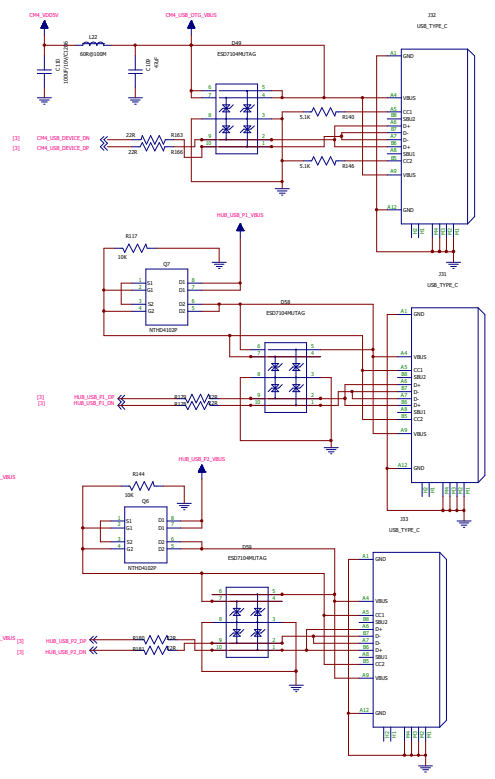


CM4 Connector

| REVISION RECORD |         |          |      |
|-----------------|---------|----------|------|
| LTR             | ECD NO. | APPROVED | DATE |
|                 |         |          |      |
|                 |         |          |      |
|                 |         |          |      |
|                 |         |          |      |

|                          |                   |                       |           |
|--------------------------|-------------------|-----------------------|-----------|
| COMPANY: Holybro         |                   |                       |           |
| TITLE: CM4-FMUVX-BASE    |                   |                       |           |
| DRAWN: Jamming           | DATED: 2021/11/09 | CODE: 027             | SIZE: A1  |
| CHECKED: Jamming         | DATED: 2021/11/15 | DRAWING NO: REVISIONS | REV: RC03 |
| QUALITY CONTROL: Jamming | DATED: 2021/12/20 | SCALE: 1:1            | SHEET: 3  |
| RELEASED: Jamming        | DATED: 2021/12/20 |                       |           |

USB\_HUB



SW1 Push to BC (Normal Boot from Emmc)  
CM4\_USB\_OTG\_ID = 0, CM4 USB Port is Host  
N\_CM4\_BOOT = 1, CM4 Boot from EMMC  
S=0, CM4 USB Port connect to USB HUB

SW1 Push to AC (Use for image flash)  
CM4\_USB\_OTG\_ID = 1, CM4 USB Port is Device  
N\_CM4\_BOOT = 0, CM4 Boot to USB  
S=1, CM4 USB Port connect to USB TypeC Connector

S=0 CM4 <---> HSD1  
S=1 CM4 <---> HSD2