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A	<div><div>Hi3516ADMEB VER.C</div><div>The digital media evaluate board for Hi3516A</div></div>																																										
B																																											
C																																											
D	<div><div>The type and specification of the components refer to the BOM</div><table><tr><td></td><td></td><td></td><td></td><td>NA</td><td></td></tr><tr><td></td><td></td><td></td><td></td><td>ECA NO</td><td>DATE</td></tr><tr><td>DESIGNED</td><td>WANGYONGHAO 206908</td><td colspan="2" rowspan="2">HI3516ADMEB_VC</td><td colspan="2" rowspan="2">00001234</td></tr><tr><td>REVIEWED</td><td>LISI XXXXX</td></tr><tr><td></td><td></td><td>VER</td><td>PART_NUMBER</td><td colspan="2">SHEET 1 OF 15</td></tr><tr><td></td><td></td><td>A</td><td>03030001</td><td colspan="2">HUAWEI TECH CO.,LTD.</td></tr><tr><td></td><td></td><td></td><td></td><td></td><td></td></tr></table></div>									NA						ECA NO	DATE	DESIGNED	WANGYONGHAO 206908	HI3516ADMEB_VC		00001234		REVIEWED	LISI XXXXX			VER	PART_NUMBER	SHEET 1 OF 15				A	03030001	HUAWEI TECH CO.,LTD.							
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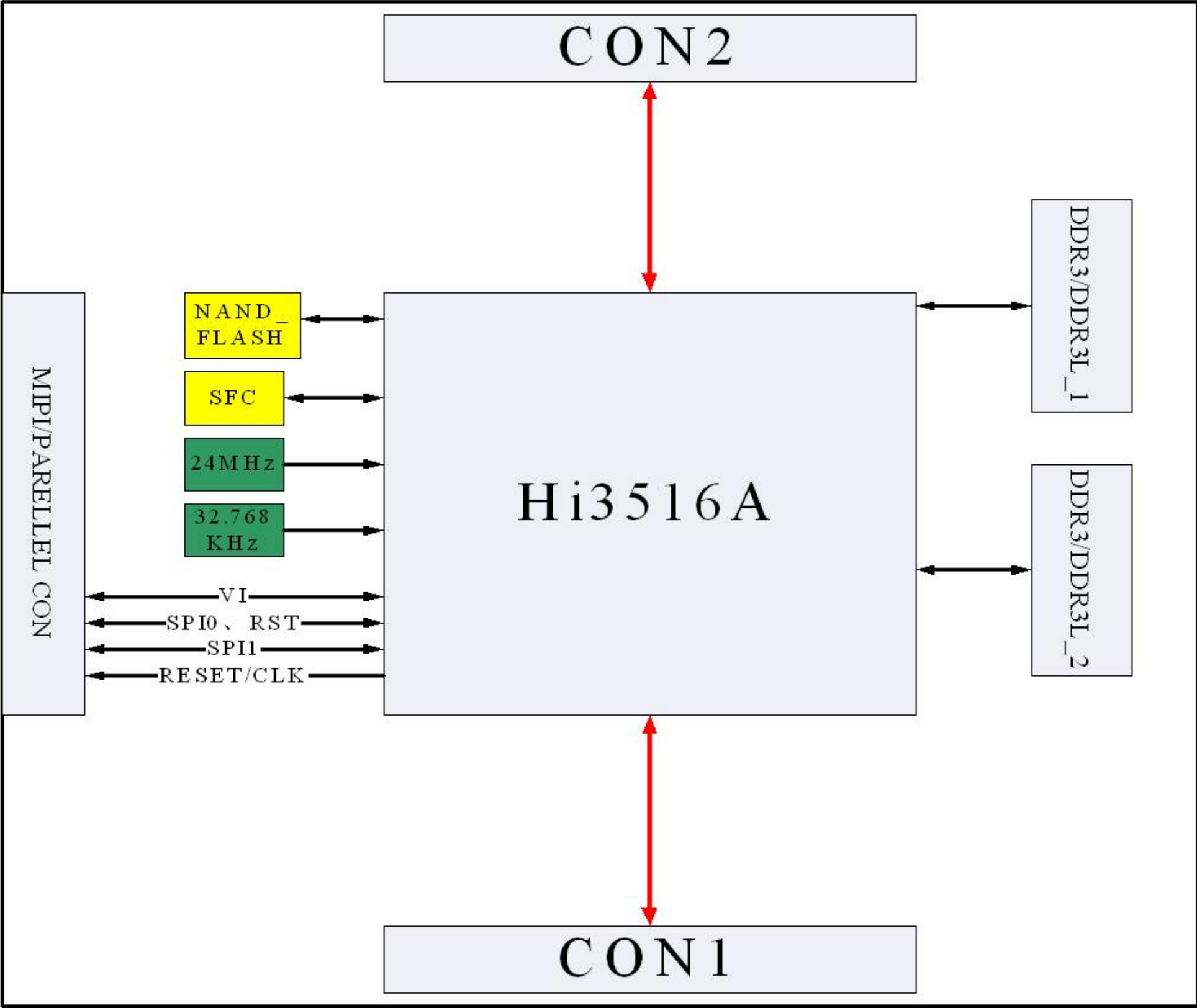
# Changing List

Ver.	Who	Date	Description
Ver.A	WangYonghao	2014-07-24	Initial
Ver.B	WangYonghao	2014-08-18	1.Change the package of J1. 2.Add 33ohm resistor(R33) on MCLK.
Ver.C	WangYonghao	2014-10-6	1.Change R44 from 1.13K to 1K. 2.Change C1 and C2 from 20pF to 22pF. 3.Add more power pin on connector.
		2015-05-21	Change R1, R3, R4 from 220Kohm to 180Kohm.
		2016-07-30	1.Change R99,R71,R94,R95 from 0 to 150Kohm. 2.Change R93,R90,R91,R92 from NC to 499ohm. 3.Change R103,R100,R101,R102 from 576kohm to 12.7Kohm. 4.Change R39,R36,R37,R38 from 470kohm to 12.7Kohm.

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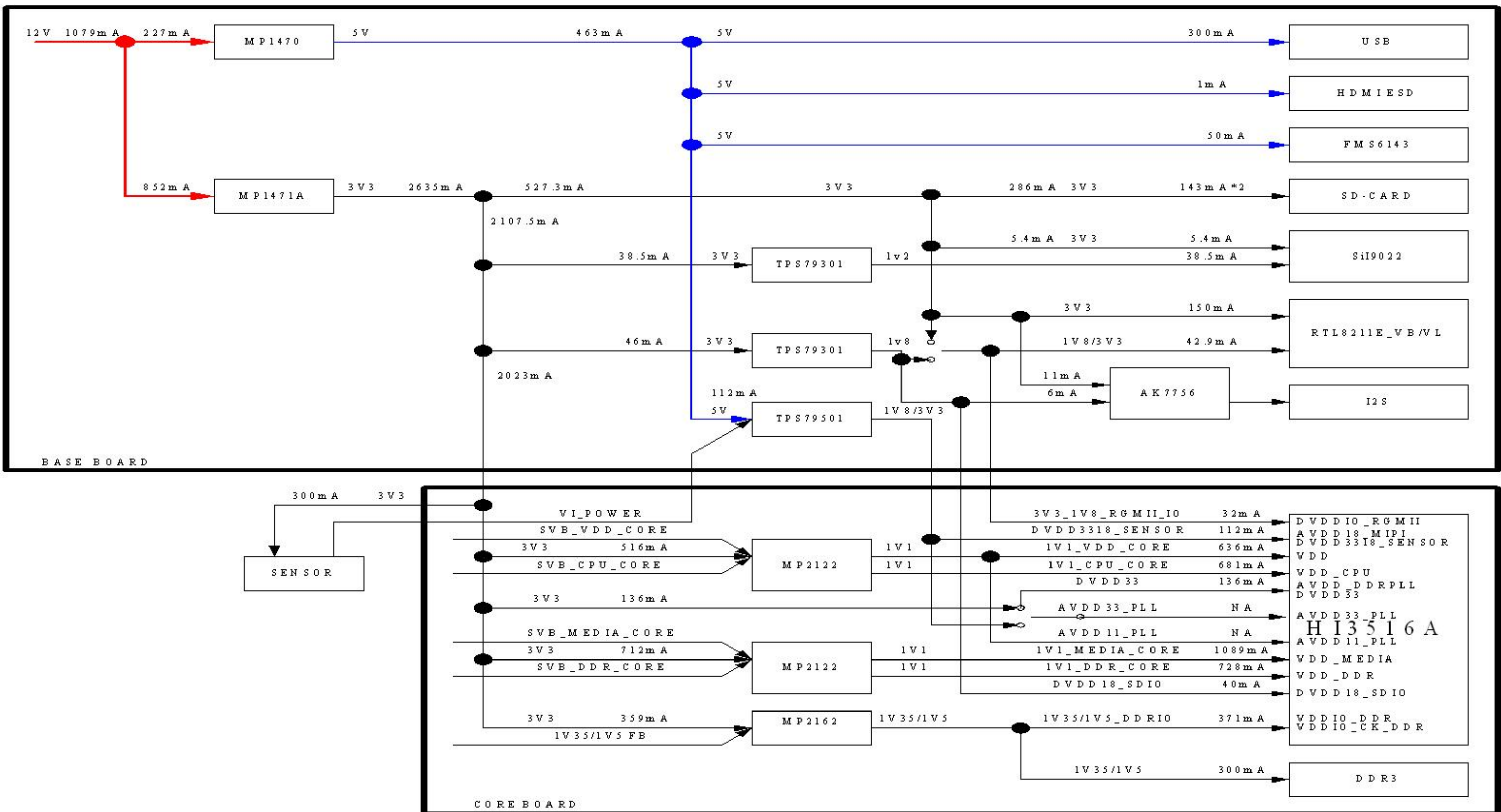
BLOCK DIAGRAM



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# POWER TREE



The currents are only for reference.

For details about the actual currents, see the related documents released by HiSilicon.

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## A



DC/DC 3V3->1V1\_MEDIA\_CORE 2Amax

$$T_{\text{delay}} = 0.452RC = 0.452 * 180k * 1u = 81.36ms$$

DC/DC 3V3->1V1\_MEDIA\_CORE 2Amax

$$T_{\text{delay}} = 0.452RC = 0.452 * 243k * 1u = 99.44ms$$



## DC/DC 3V3->1V1\_CPU\_CORE 2Amax

$$T_{\text{delay}} = 0.452RC = 0.452 * 180k * 1u = 81.36ms$$

## DC/DC 3V3->1V1\_DDR\_CORE 2Amax

$$T_{\text{delay}} = 0.452RC = 0.452 * 180k * 1u = 81.36ms$$



DC/DC 3V3->1V5\_1V35\_DDRIO 2Amax

$$T_{\text{delay}} = 0.452RC = 0.452 * 100k * 1\mu = 45.20ms$$

$$V_{out1} = 0.6 * [(150k+120k)/120k] = 1.35V$$

$$V_{out2} = 0.6 * [(150k+100k)/100k] = 1.5V$$

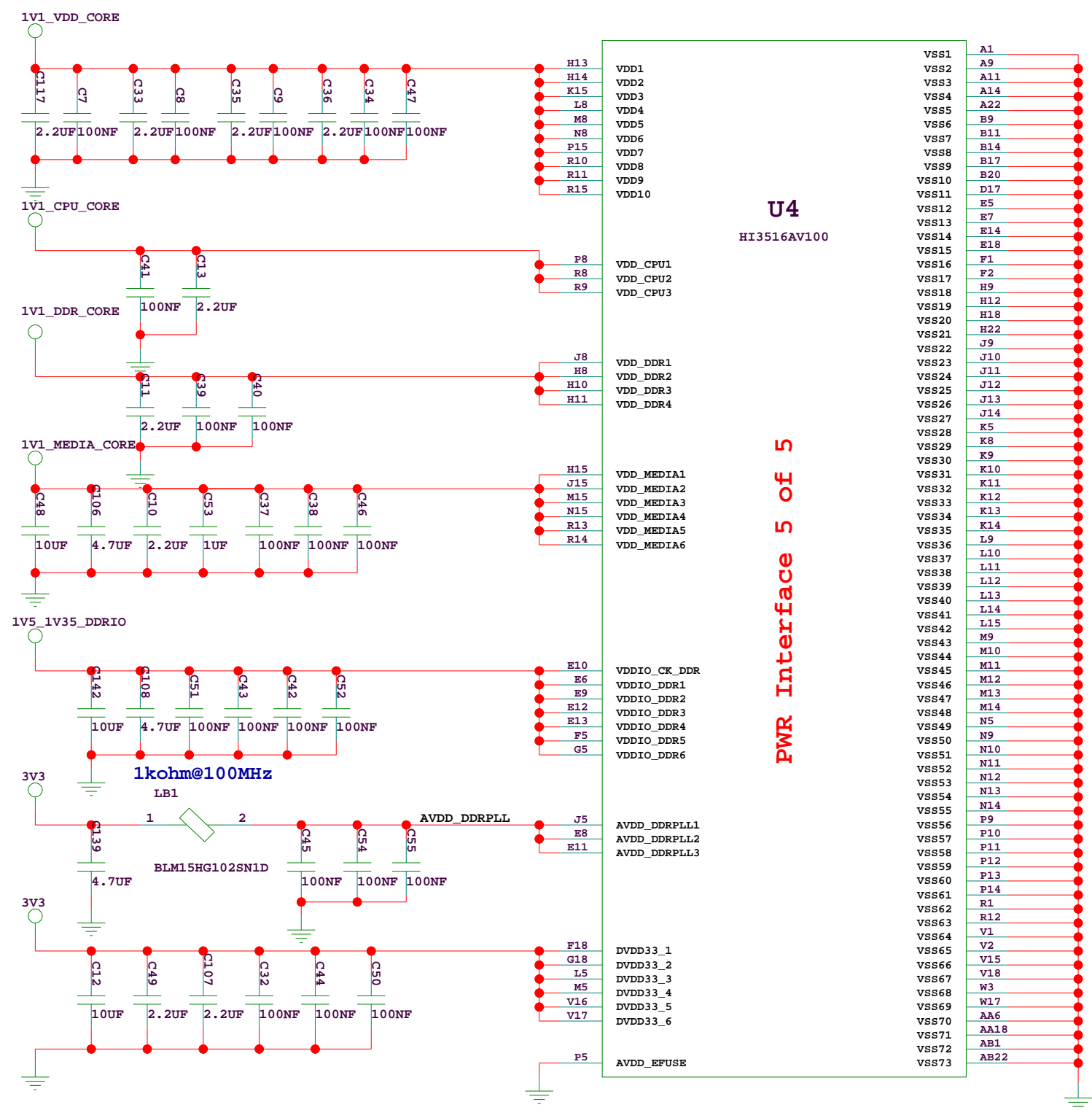
## B



The type and specification of the components refer to the BOM

D

Power of Hi3516A



U4  
HI3516AV100

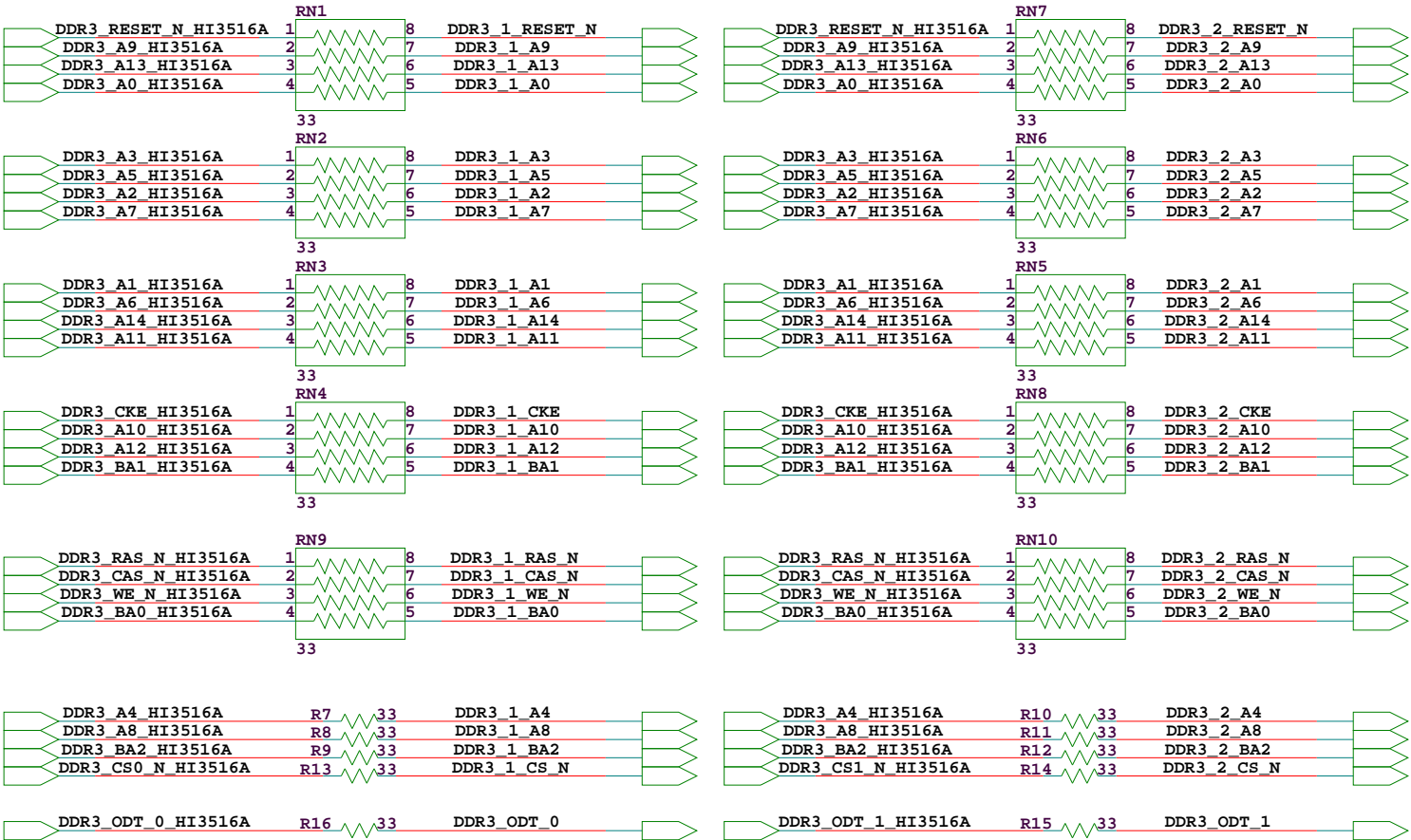
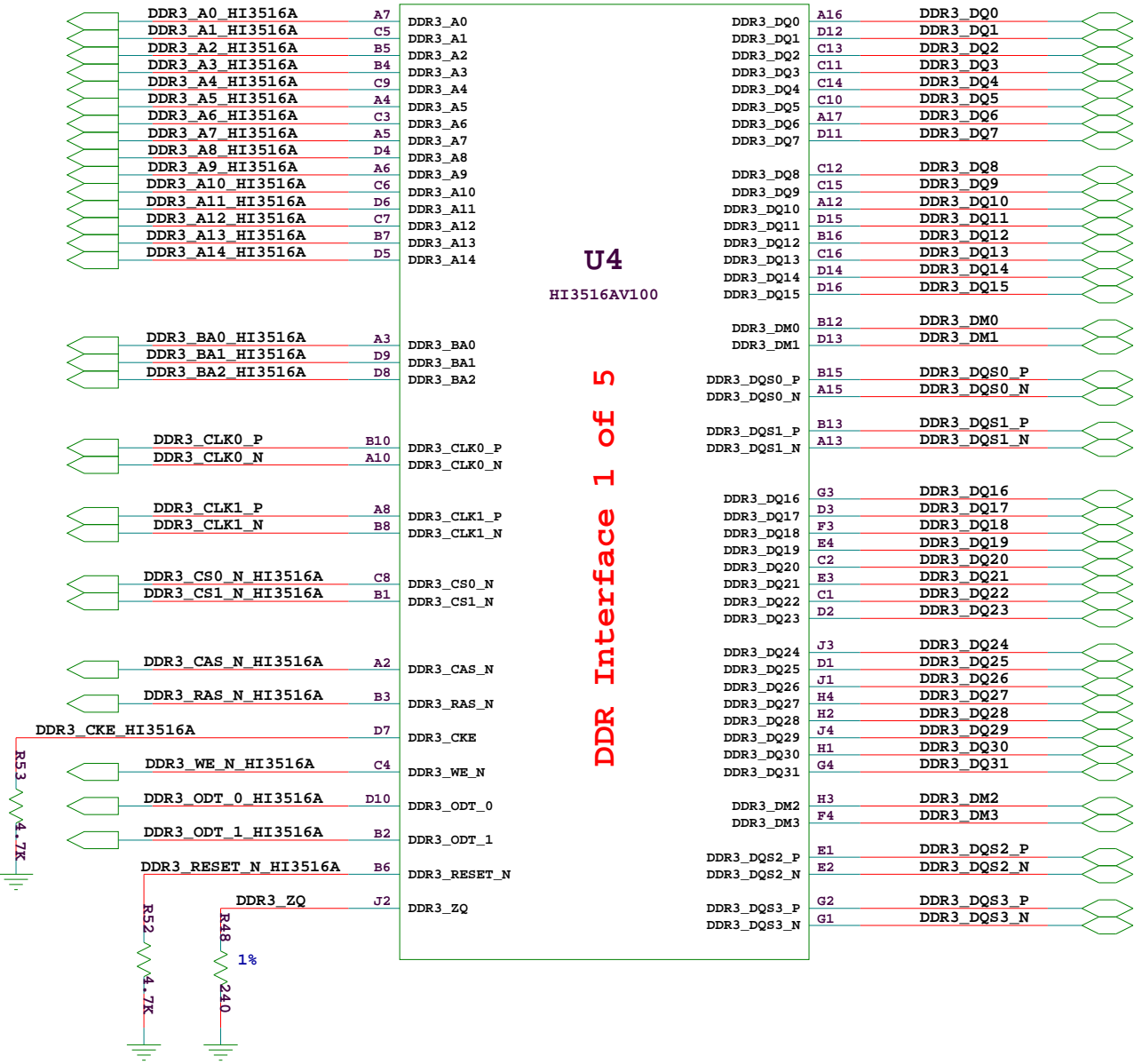
PWR Interface 5 of 5

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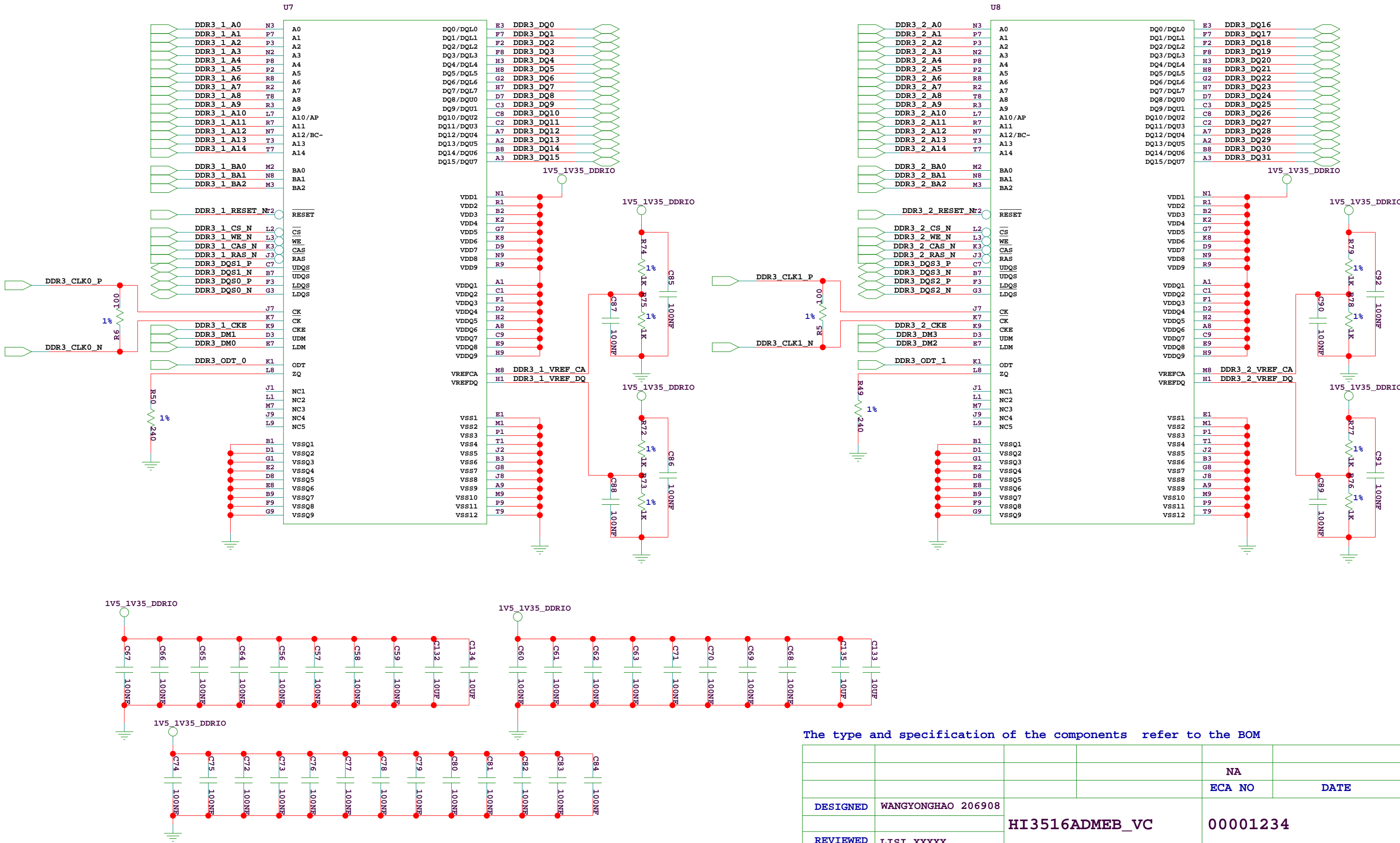
DDR of Hi3516A



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DDR



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## A



B

C

DD

## A



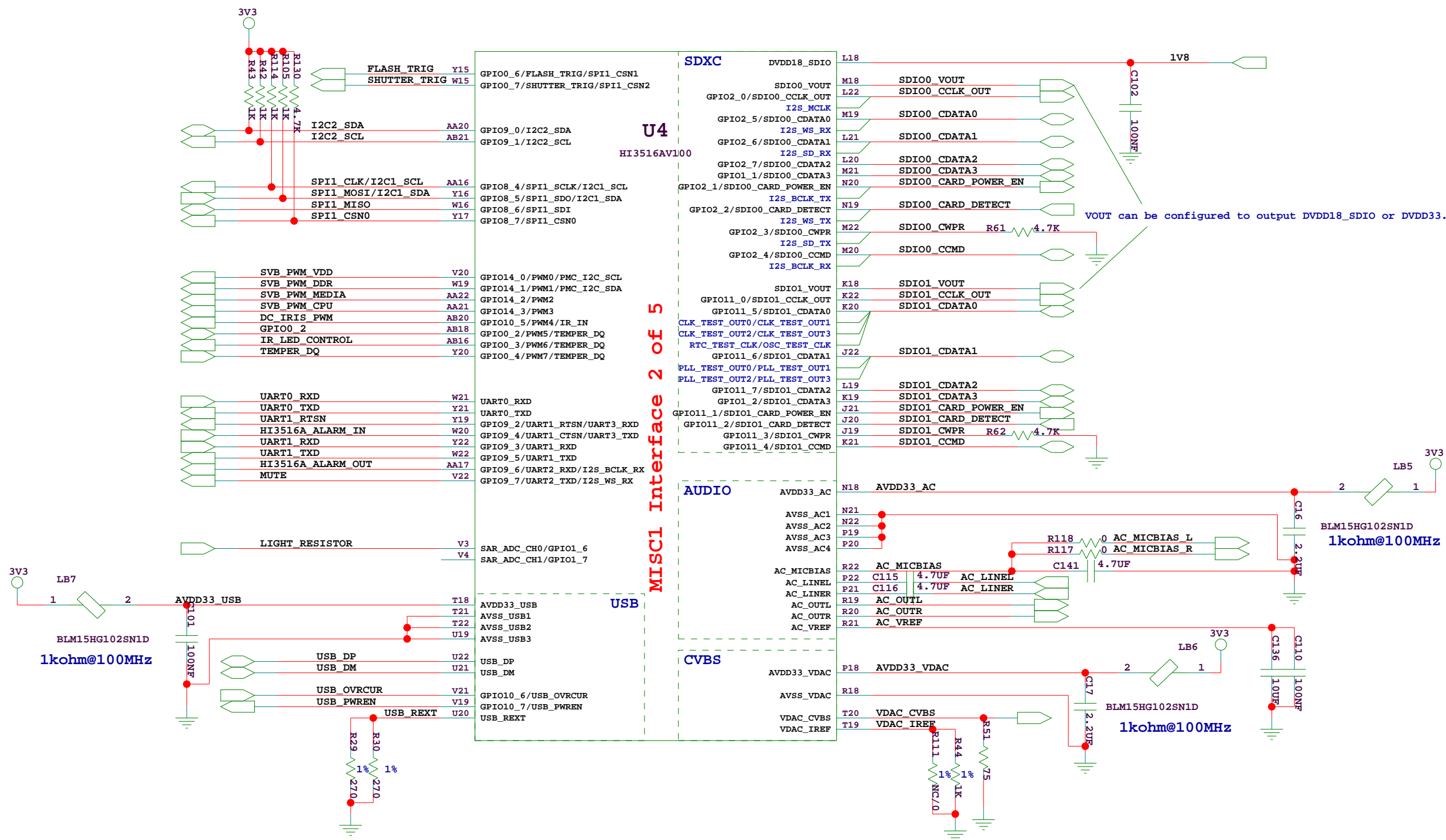
C

D

D

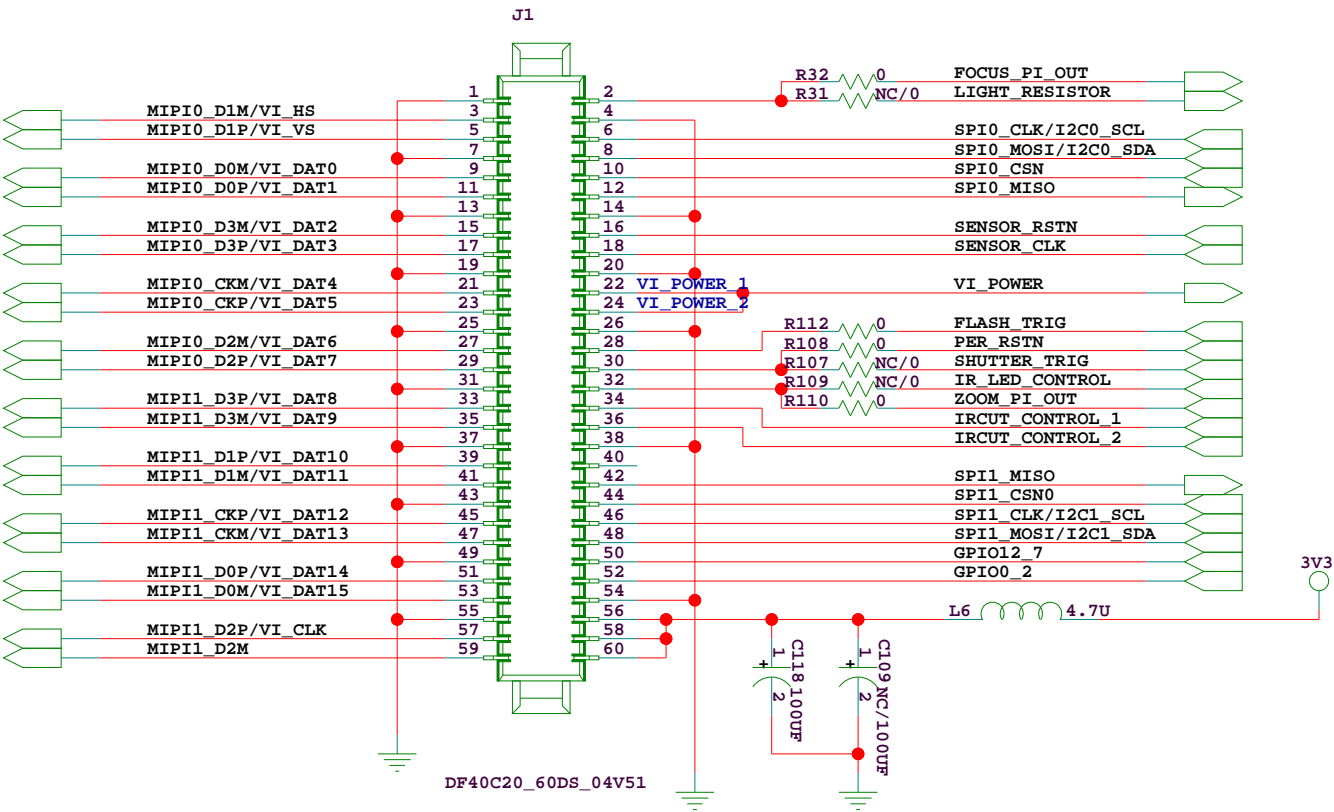


Peripheral of Hi3516A



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Sensor Connector

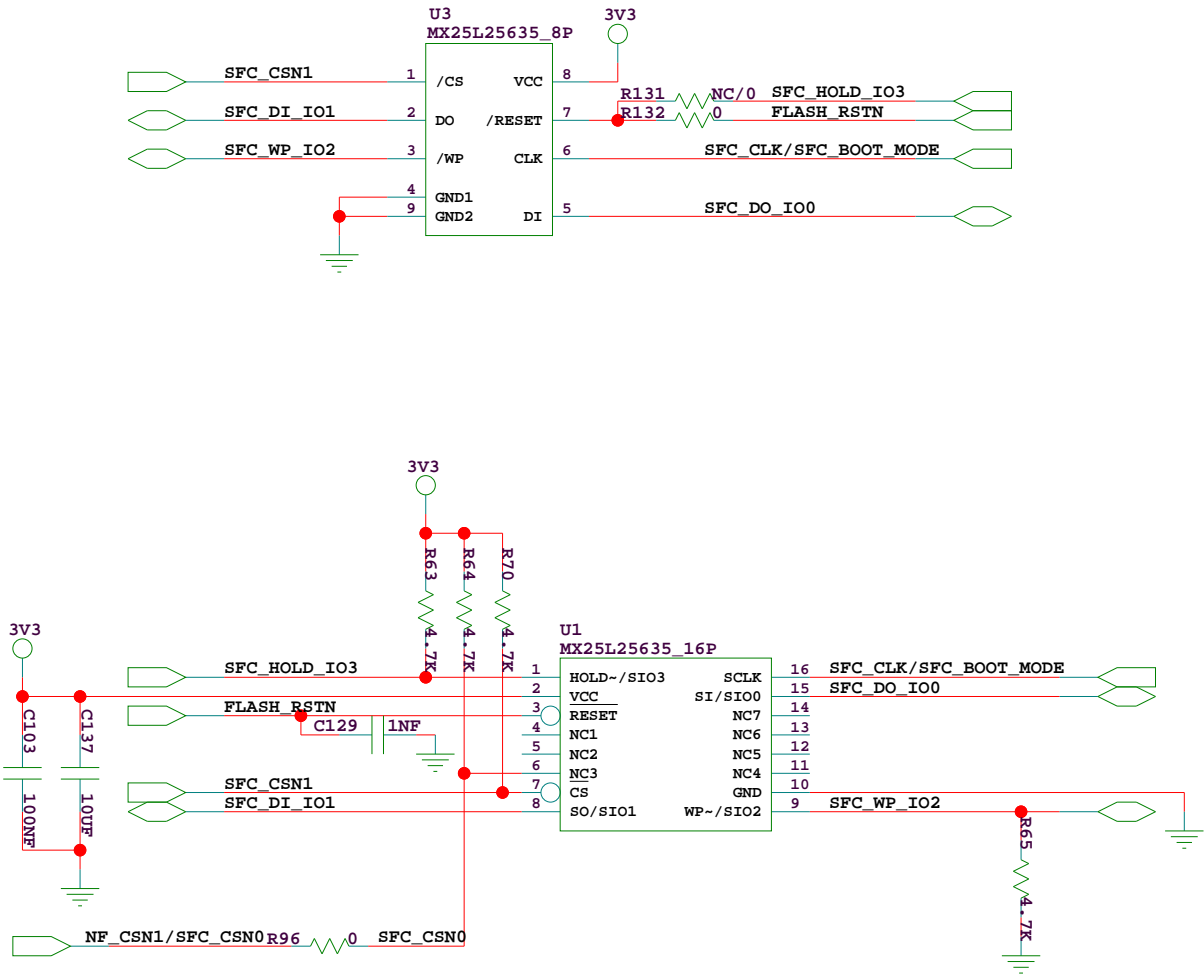


The type and specification of the components refer to the BOM

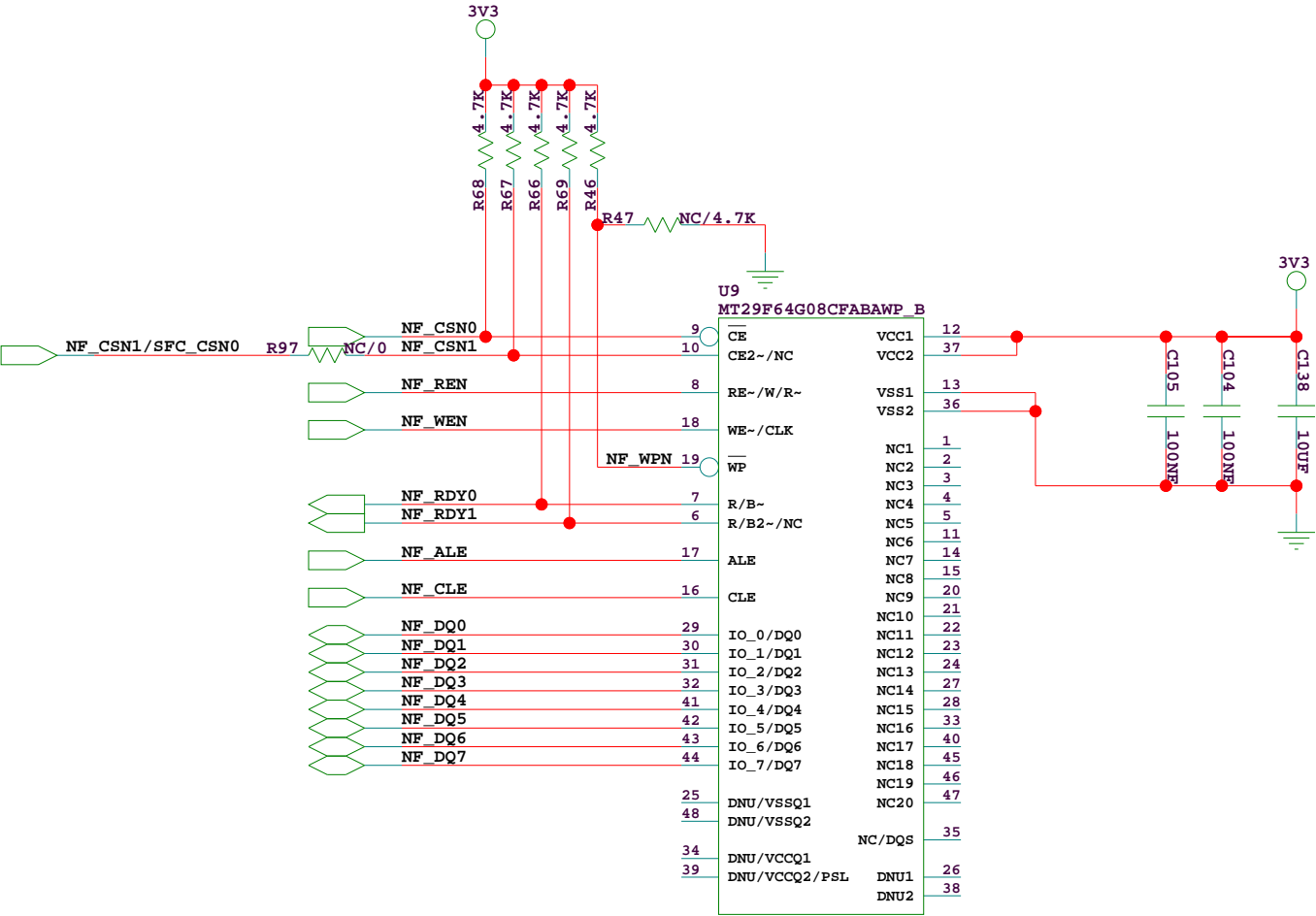
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		VER	PART_NUMBER	SHEET 12 OF 15	
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FLASH

SPI FLASH(BOOT FROM CSN1)



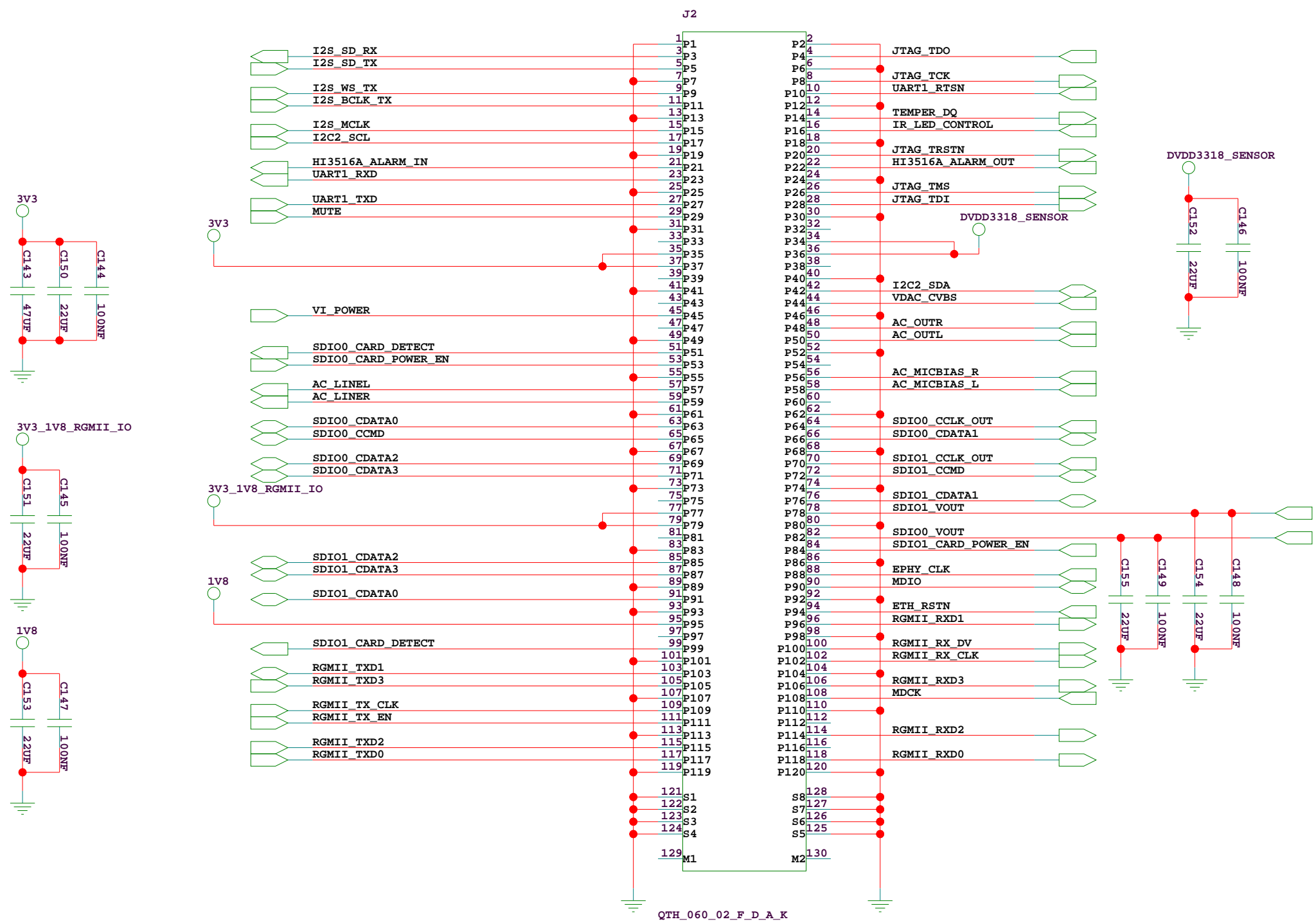
ONFI NAND FLASH(BOOT FROM CSN0)



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CONNECTOR TO PERB J16

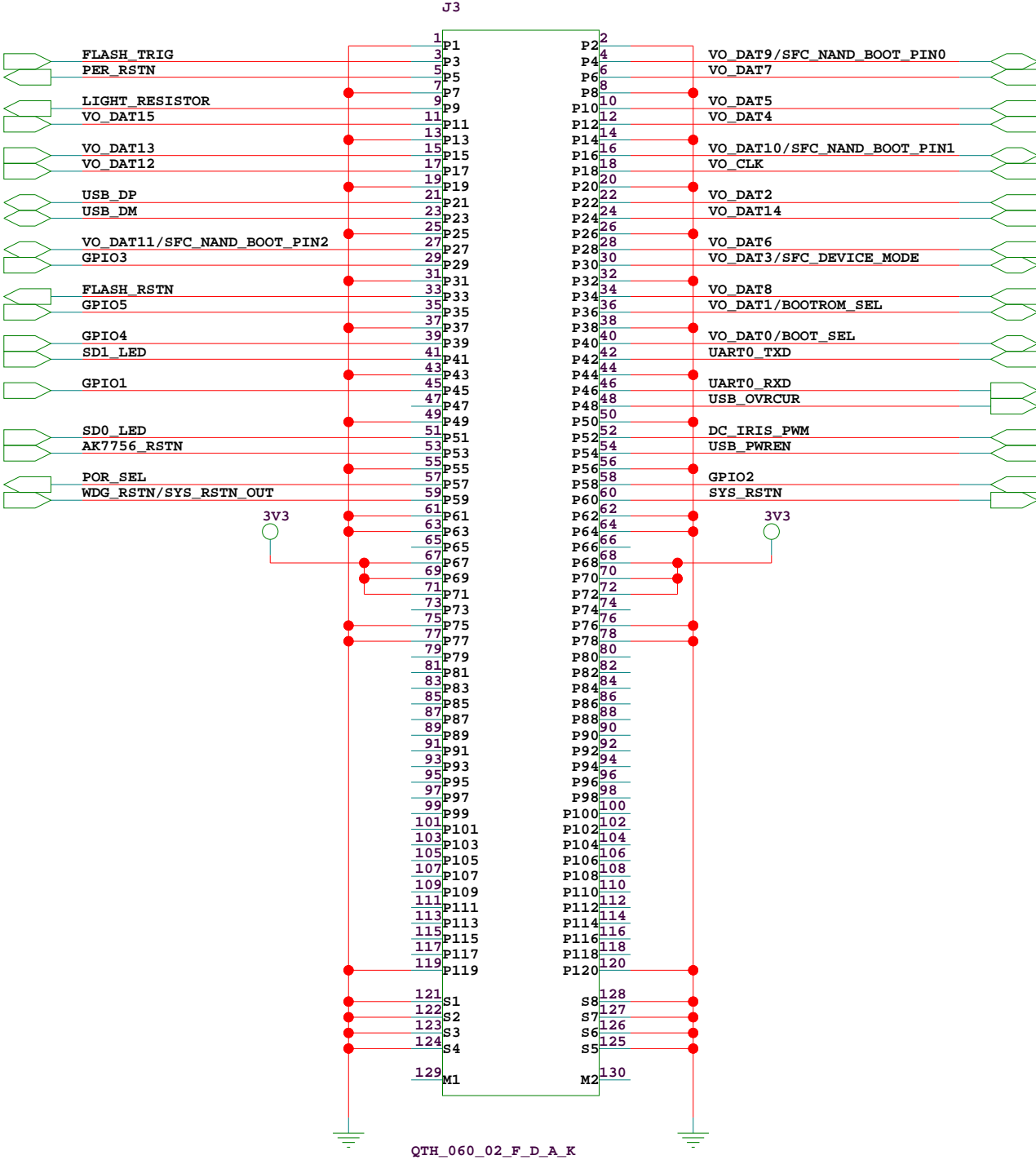


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CONNECTOR TO PERB J17



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		VER	PART_NUMBER	SHEET 15 OF 15	
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