

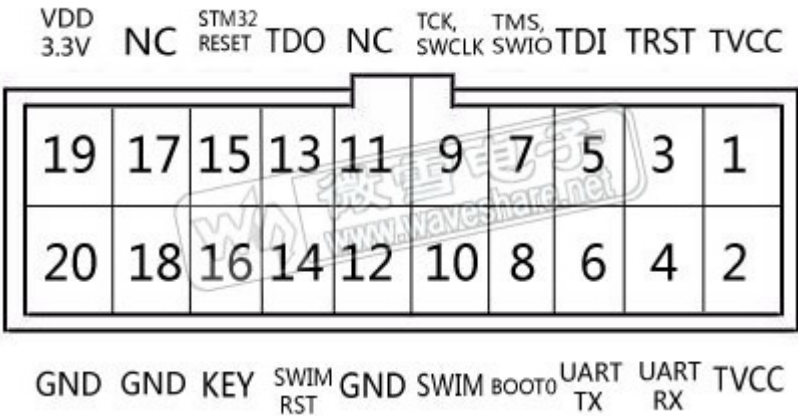
ST-Link/V2 JTAG/SWD接口

ST-Link/V2 JTAG/SWD接口是如何定义的？

下面为ST-Link/V2 JTAG/SWD接口定义：

仿真器端口	连接目标板	功能
1. TVCC	MCU电源VCC	连接STM32目标板的电源VCC
2. TVCC	MCU电源VCC	连接STM32目标板的电源VCC
3. TRST	GND	GROUND
4. UART-RX	GND	GROUND
5. TDI	TDI	连接STM32的JTAG TDI
6. UART-TX	GND	GROUND
7. TMS, SWIO	TMS, SWIO	连接STM32的JTAG的TMS, SWD的SW IO
8. BOOT0	GND	GROUND
9. TCK, SWCLK	TCK, SWCLK	连接STM32的JTAG的TCK, SWD的SW CLK
10. SWIM	GND	GROUND
11. NC	NC	Unused
12. GND	GND	GROUND
13. TDO	TDO	连接STM32的JTAG TDO
14. SWIM-RST	GND	GROUND
15. STM32-RESET	RESET	连接STM32目标板的RESET端口
16. KEY	NC	GROUND
17. NC	NC	Unused
18. GND	GND	GROUND
19. VDD	NC	VDD (3.3V)
20. GND	GND	GROUND

下面是ST-Link/V2 JTAG/SWD标准的接口排列：



ST-Link/V2 JTAG/SWD指定的标准接口