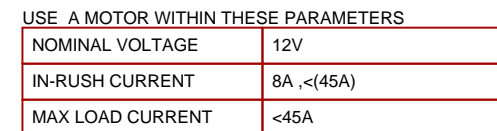

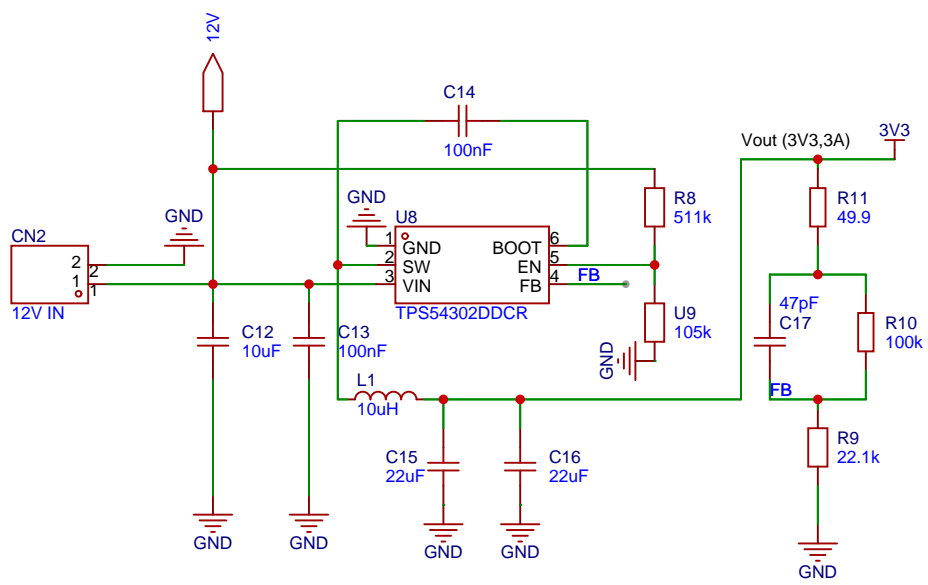


CLOCK  
RESET  
BOOT  
DEBUG/PROGRAM  
POWER  
START/STOP BUTTON  
DC MOTOR INTERFACE  
DUTY CYCLE CONTROL



Schematic	CPU+MOTOR CONTROL			Update Date	2025-01-28
				Create Date	2025-01-28
Page	CPU			Part Number	
Drawn	Edwin Mwiti	dc-motor-control-cj			
Reviewed	TBD				
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POWER SUPPLY



Schematic	POWER SUPPLY			Update Date	2025-01-28
				Create Date	2025-01-28
Page	P2			Part Number	
Drawn	Edwin Mwiti	dc-motor-control-cj			
Reviewed	TBD				
		VER	SIZE	PAGE	2 OF 2
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