

Side Channel Attacks (DPA)

on HaHa v3.0 Board

We will implement a Differential Power Analysis (DPA) attack on DES.

Original document creators:

Instructor: Dr. Swarup Bhunia

Co-Instructors/TAs: Shuo Yang and Reiner Dizon-Paradis

Modified by Jeff Goeders (BYU)

Theory Background

1. Side channel attack techniques

In cryptography, a side channel attack is any attack based on information gained from the physical implementation of a cryptosystem, rather than brute force or theoretical weaknesses in the algorithms (compare cryptanalysis). For example, timing information, power consumption, electromagnetic leaks or even sound can provide an extra source of information, which can be exploited to break the system. Some side-channel attacks require technical knowledge of the internal operation of the system on which the cryptography is implemented, although others such as differential power analysis are effective as black-box attacks. Many powerful side-channel attacks are based on statistical methods pioneered by Paul Kocher.

1.1 Ways of accessing the module

When analyzing the security of a cryptographic hardware module, it can be useful to perform a systematic review of the attack surface — the set of physical, electrical and logical interfaces that are exposed to a potential opponent. According to this observation, side channel attacks can be divided into the following classes: invasive attacks, semi-invasive attacks, and non-invasive attacks.

An Invasive attack involves de-packaging to get direct access to the internal components of cryptographic modules or devices. A typical example of this is that the attackers may open a hole in the passivation layer of a cryptographic module and place a probing needle on a data bus to see the data transfer. The concept of semi-invasive attack is first developed by Skorobogatov and Anderson. This kind of attack involves access to the device, but without damaging the passivation layer or making electrical contact other than with the authorized surface. For example, in a fault-induced attack, the attacker may use a laser beam to ionize a device to change some of its memories and thus change the output of this device. A non-invasive attack involves close observation or manipulation of the device's operation. This attack only exploits externally available information that is often unintentionally leaked. A typical example of such an attack is timing analysis: measuring the time consumed by a device to execute an operation and correlating this with the computation performed by the device in order to deduce the value of the secret keys.

1.2 Known side channel attacks

1.2.1 Power attacks

In this experiment, we will focus on power attacks. Power attacks measure the circuit's processing time and current consumption to infer what is going on inside it. Of all types of SCA attacks known today, the number of literature on power analysis attacks and the relevant countermeasures is the biggest. Power analysis attack is the current research focus of side-channel attacks. Power analysis attacks have been demonstrated to be very powerful attacks for most straightforward implementations of symmetric and public key ciphers.

Basically, power analysis attack can be divided into SPA and DPA. In SPA attacks, the aim is essential to guess from the power trace which particular instruction is being executed at a certain time and what values the input and output have. Therefore, the adversary needs an exact knowledge of the implementation to mount such an attack. On the other hand, DPA attack does not need the knowledge of the implementation details and alternatively exploiting statistical methods in the analysis process. DPA is one of the most powerful SCA attacks, yet it can be mounted using very little resources.

1.2.2 Timing attacks

A timing attack is, essentially, a way of obtaining some user's private information by carefully measuring the time it takes the user to carry out cryptographic operations. The principle of this attack is very simple: to exploit the timing variance in the operation.

Timing attacks were introduced in 1996 by Kocher, where RSA modular exponentiation was being attacked. The basic assumptions of timing analysis are 1. The runtime of a cryptographic operation depends to some extent on the key. With present hardware, this is likely to be the case, but note that there are various efficient hardware-based proposals to make the timing attack less feasible through 'noise injection'. Software approaches to make the timing attack infeasible are based on the idea that the computations in two branches of a conditional should take the same amount of time ('branch equalization'). 2. A sufficiently large number of encryptions can be carried out, during which time the key does not change. A challenge-response protocol is ideal for timing attacks. 3. Time can be measured with known error. The smaller the error, the fewer time measurements are required.

1.2.3 EM attacks

As electrical devices, the components of a computer often generate electromagnetic radiation as part of their operation. An adversary that can observe these emanations and can understand their causal relationship to the underlying computation and data may be able to infer a surprising amount of information about this computation and data. This ability can be devastating, should the computer be a trusted computing platform intended to keep this information from the adversary. Similar to the power analysis attacks, Electromagnetic Analysis attacks can also be divided into two main categories: Simple Electromagnetic Analysis and Differential Electromagnetic Analysis.

2. SPA

SPA is a technique that involves directly interpreting power consumption measurements collected during cryptographic operations. SPA can yield information about a device's operation as well as key material.

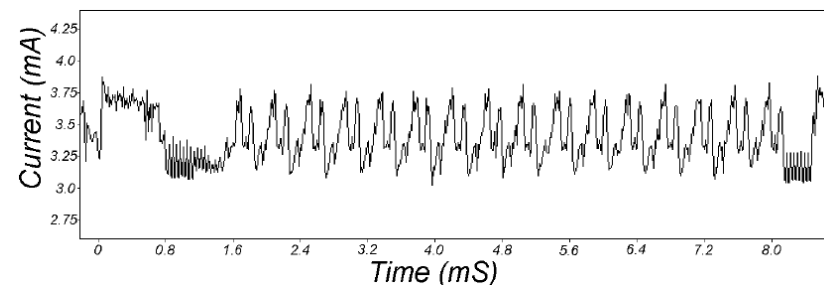


Figure 1 SPA trace showing an entire DES operation.

A trace refers to a set of power consumption measurements taken across a cryptographic operation. For example, a 1-millisecond operation sampled at 5 MHz yields a trace containing 5000 points. Figure 1 shows a SPA trace from a typical smart card as it performs a DES operation. Note that the 16 DES rounds are clearly visible.

Figure 2 is a more detailed view of the same trace showing the second and third rounds of a DES encryption operation. Many details of the DES operation are now visible. For example, the 28-bit DES key registers C and D are rotated once in round 2 (left arrow) and twice in round 3 (right arrows). In Figure 2, small variations between the rounds just can be perceived. Many of these discernable features are SPA weaknesses caused by conditional jumps based on key bits and computational intermediates.

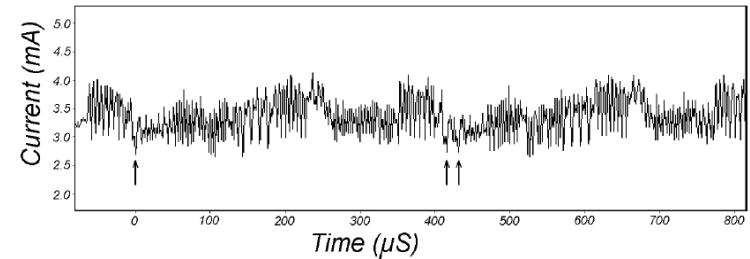


Figure 2 SPA trace showing DES rounds 2 and 3.

3. DPA

In addition to large-scale power variations due to the instruction sequence, there are effects correlated to data values being manipulated. These variations tend to be smaller and are sometimes overshadowed by measurement errors and other noise. In such cases, it is still often possible to break the system using statistical functions tailored to the target algorithm.

In an acquisition campaign, m power curves T_i (also casually called “traces”) are garnered. Each trace T_i corresponds to a given leakage. For the sake of simplicity, we focus on a part of the leakage, related to a very restricted set of gates in the netlist. We assume this leakage can be summarized into two typical behaviors: high dissipation to charge a net versus low dissipation to discharge it, and activity versus no-activity.

These two behaviors are related to an inner Boolean variable, called a decision (selection) function D_i . If the secret key is unknown, this variable is also unknown; however, it depends in practice on only a couple of bits from the key, which can be tested exhaustively. The decision function $D_i \in \{0,1\}$ is an oracle for an attacker: if it is the correct decision function, it can allow for an exhibition of the two behaviors, otherwise it is simply irrelevant. The idea behind DPA is to exhibit an asymptotic difference between the behaviors. The ad hoc criterion is introduced as:

$$\text{DPA} = \frac{1}{m_0} \sum_{i/D_i=0} T_i - \frac{1}{m_1} \sum_{i/D_i=1} T_i \dots (1)$$

Where m_0 and m_1 denote the number of traces for each decision.

In [3], Paul Kocher defined the selection function $D(C, b, K_s)$ as computing the value of bit $0 \leq b \leq 32$ of the DES intermediate L at the beginning of the 16th round for ciphertext C , where the 6 key bits entering the S box corresponding to bit b are represented by $0 \leq K_s \leq 2^6$. Note that if K_s is incorrect, evaluating $D(C, b, K_s)$ will yield the correct value for bit b with probability $P \approx \frac{1}{2}$ for each ciphertext.

To implement the DPA attack, an attacker first observes m encryption operations and captures power traces $\mathbf{T}_{1..m}[1..k]$ containing k samples each. In addition, the attacker records the ciphertexts $C_{1..m}$. No knowledge of the plaintext is required.

DPA analysis uses power consumption measurements to determine whether a key block guess K_s is correct. The attacker computes a k -sample differential trace $\Delta_D[1..k]$ by finding the difference between the average of the traces for which $D(C_i, b, K_s)$ is one and the average of the traces for which $D(C, b, K_s)$ is zero. Thus $\Delta_D[j]$ is the average over $C_{1..m}$ of the effect due to the value represented by the selection function D on the power consumption measurements at point j .

If K_s is incorrect, the bit computed using D will differ from the actual target bit for about half of the ciphertexts C_i . The selection function $D(C_i, b, K_s)$ is thus effectively uncorrelated to what was actually computed by the target device. If a random function is used to divide a set into two subsets, the difference in the averages of the subsets should approach zero as the subset sizes approach infinity.

If K_s is correct, however, the computed value for $D(C_i, b, K_s)$ will equal the actual value of target bit b with probability 1. The selection function is thus correlated to the value of the bit manipulated in the 16th round. As a result, the $\Delta_D[j]$ approaches the effect of the target bit on the power consumption as $m \rightarrow \infty$. Other data values, measurement errors, etc. that are not correlated to D approach zero. Because power consumption is correlated to data bit values, the plot of Δ_D will be at with spikes in regions where D is correlated to the values being processed.

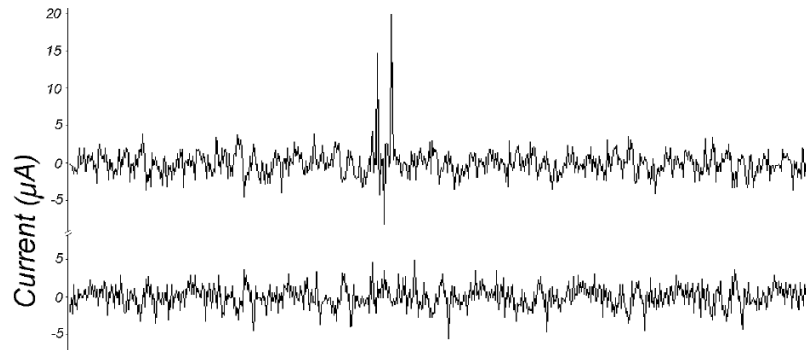


Figure 3 DPA traces: correct guess (top) and incorrect guess (bottom).

Experiment Set-up: Configuration

Lab requirements:

1. A computer.
2. Python to process data.

Instructions and Questions

In this lab, you do not need to take any physical measurements. To do a DPA, from hundreds to tens of thousands of measurements are required. We don't have time to do that many measurements in this experiment. Therefore, you will process the data provided to you. There are in total about 80000 traces provided.

The provided traces are very similar to what is shown in Figure 1. They are the power consumption traces that include an entire DES operation of 16 rounds.

Each trace contains both the plaintext and ciphertext (although we only need one of these to perform the attack -- in this lab we will only use the plaintext to perform the attack). If you load one of the traces and plot its data, it will look like the waveform in Figure 9.

As these 80000 traces are obtained when the DES are encrypting different plaintext (with the same key), the waveforms are different from each other. However, you can hardly tell the difference by looking with your eyes. If you do subtraction between two traces, you can see they can almost cancel out each other, as shown in Figure 10.

The first step of DPA is to divide these traces into two groups using your selection function. Then, for each group, you can calculate a new trace, that is the average of all traces in the group. This can be done easily using in numpy; like so: `group1_avg = np.average(group1, axis=0)`. These two average traces may still look very similar to the trace in Figure 9. However, with an average trace computed for each group, you can calculate a new array, which is the difference between the two groups. Again, this is very easy in Python: `diff_trace = group1 - group2`.

When you calculate the difference between these two average traces and plot out the difference, an exciting thing happen. If you divided the traces using the right way in the first step, you can see that these two average traces **cannot** cancel each other anymore. There will be a peak (Figure 11), which may be positive or negative. You can easily plot a trace with matplotlib: `plt.plot(trace); plt.show();`.

The key point is how you divide these traces into two groups. If you divide them randomly and calculate the difference of the average trace for the two groups, they cancel each other out. If you divide them the wrong way, there will be no difference from a random partitioning. You should design a good selection function which, if the key guess is right, can divide the traces in a correct and efficient way, so that the two average traces cannot cancel each other.

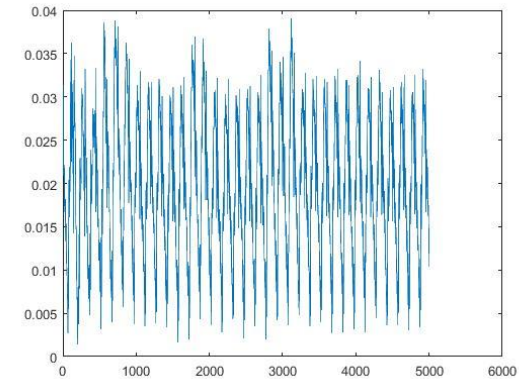


Figure 9 Trace example.

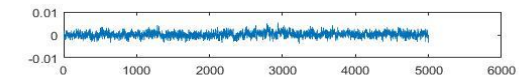


Figure 10 The difference of two traces.

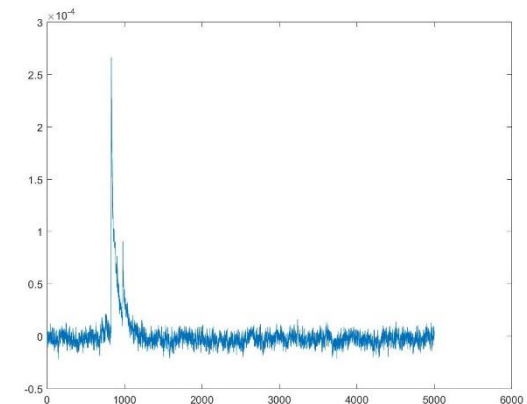


Figure 11 Difference of two average traces when the key guess is correct.

How do we design the selection function? We know that the DES does the same operation for 16 rounds and the weakness of DES is always in the first round or the last round. (Why?) In this experiment, we can try to get the sub-key for the first round. Once we know a sub-key for one round, we can calculate out the original key. (Why?) In this lab, you will figure out the round 1 key, and in next week's lab, you will use this to figure out the full key.

We also know that semiconductors use current while switching. We can concentrate on one bit of one register (register R shown in Figure 12 is recommended). Register R begins as the right-hand side of the input plaintext, after the input permutation, referred to as R0 in Figure 12. The first round concludes with register R being updated to a new value (R1). We will use the hypothesis that if a bit in R flips from R0 to R1, then there will be a different power signature than if it does not change. If a given bit in R flips during the first round, we put the trace into one group. If it doesn't flip during the first round, we put it into the other group. Therefore, after we put all the traces into these two groups correctly, the traces in one group all tend to consume more power (at an unknown but certain time), and there will be a peak (at that certain time) in the difference trace.

Now we can design the selection function. The function will take 3 inputs: what the plaintext (M) is, which bit ($1 \leq b \leq 32$) of register R you choose to inspect, and your guess for the round 1 subkey, K . While the round subkey is 48-bits (which is too large to try all guesses), if you inspect the DES algorithm, you will see that each bit of R1 comes from one of the eight sboxes, and each sbox is influenced by a unique 6-bit range of the 48-bit round key. This means that you can guess the round subkey 6 bits at a time, making the attack quite feasible. For each 6-bit sbox key, you will need to try all combinations ($0 \leq K_{sbox_i} \leq 63$), and perform a partitioning of the traces for each key guess. The one with the biggest peak will be the right guess. You will then repeat this for each sbox (8 times).

The bit you inspect (b) can be chosen by you. Each sbox influences 4 bits of the 32-bit R register, so make sure you choose one of these 4 bits that are relevant to the sbox you are targeting. Note that after the 8 sboxes finish their substitution, there will be a permutation (the P box in Figure 12). That means the 4-bit output of the first sbox (which operates on the most significant 4 bits of R0), will **not** go to the most significant 4 bits of R1. You will need to be careful when selecting b to be sure the bit you watch in R is influenced by the sbox you are targeting.

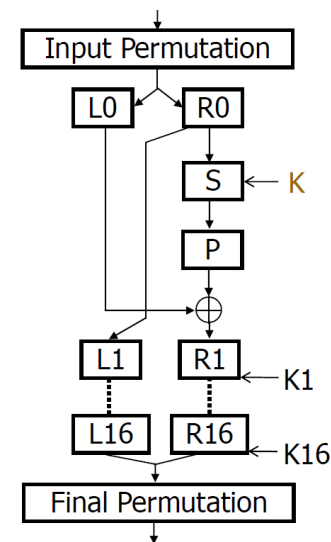


Figure 12 DES operation.

Complete the following steps, and add them to your report:

1. The first round and last round of encryption algorithms are often targeted by hackers. Why are these rounds often the weakness
2. Which bits in R1 will the first sbox's output go to? Refer to the DES code to answer this question. (Here 'first' means this sbox takes the 6 most significant bits from the XOR result of the sub key. Make sure to answer it correctly. Your DPA attack won't work without choosing an appropriate bit in the R register to watch, when trying to guess the 6-bit portion of the round key.
3. Create a python script that works to determine the 6-bit key for the first sbox
 - a. Choose a bit from the previous question as *b*.
 - b. Create a selection function in Python. The output of the function is either 0 or 1. If bit *b* stays the same during the first round (from R0 to R1), the output is 0. If bit *b* flips, the output is 1. (See the course website for advice on how to do this)
 - c. Try all 64 values for the 6-bit sbox key.
 - d. Identify the 6-bit sbox key that produces the largest peak.
4. Repeat this process for each of the remaining 7 sboxes.
 - a. For each sbox, you will need to choose a new *b* bit-index to use for your selection function. Make sure to select one that is appropriate for the sbox you are targeting.
5. For each sbox, add the trace difference graph to your report showing the K value that produces the highest peak, as well as the trace difference graph when K=63. This is a total of 16 graphs.
6. In your report, provide the round1 key that you have obtained. Provide this as a list of 8 6-bit keys, in decimal, from most significant to least significant.
 - a. Example: (23, 62, 7, 36, 2, 50, 41, 14)

References and Further Reading

- [1] https://en.wikipedia.org/wiki/Exponentiation_by_squaring
- [2] https://en.wikipedia.org/wiki/Data_Encryption_Standard
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