

**A**

1

**B**

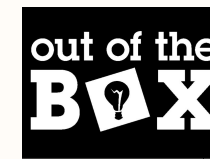
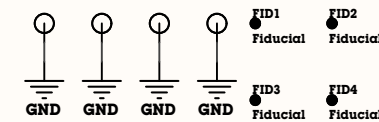
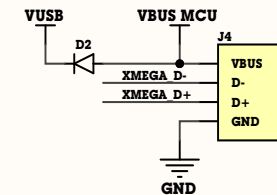
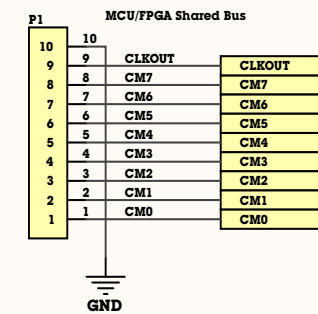
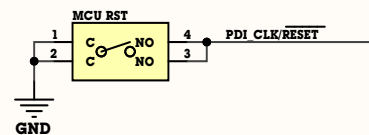
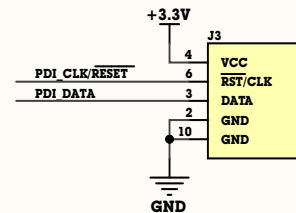
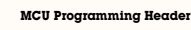
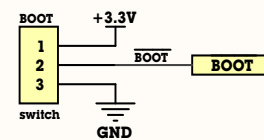
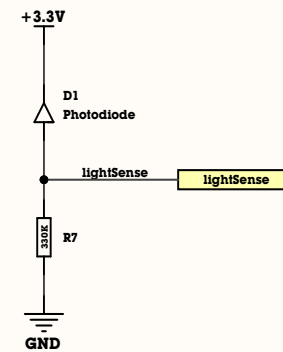
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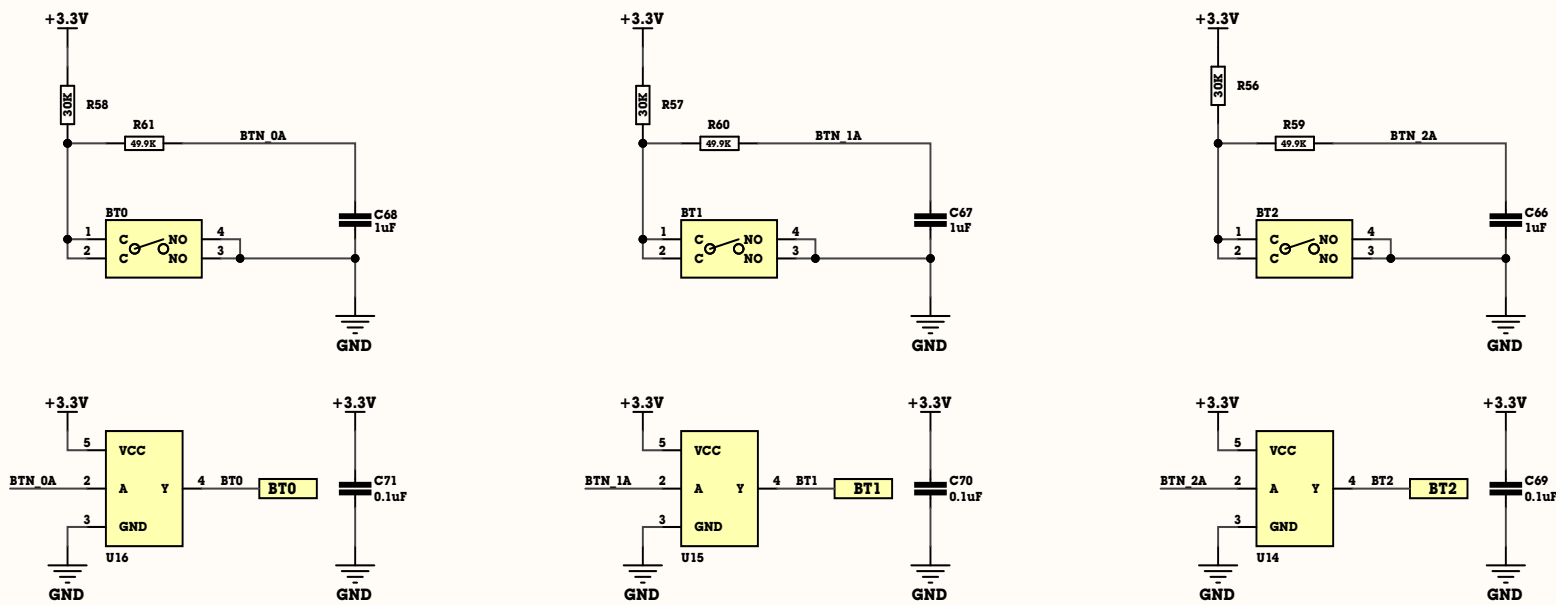
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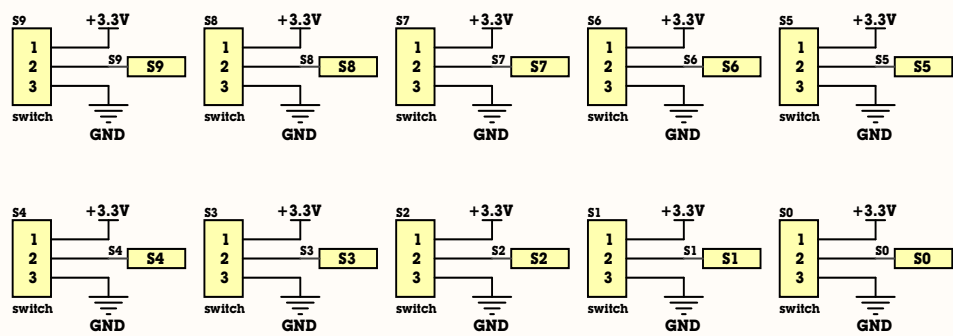
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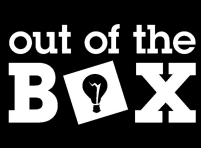
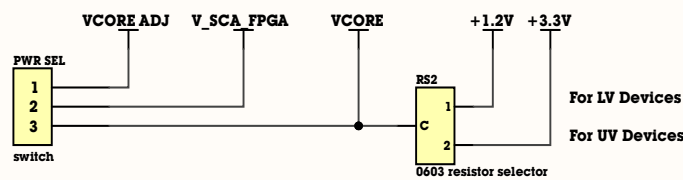
FPGA Buttons



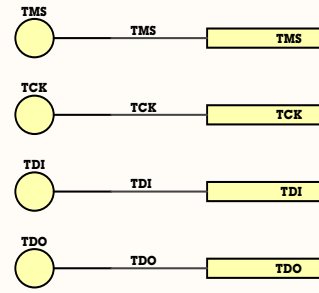
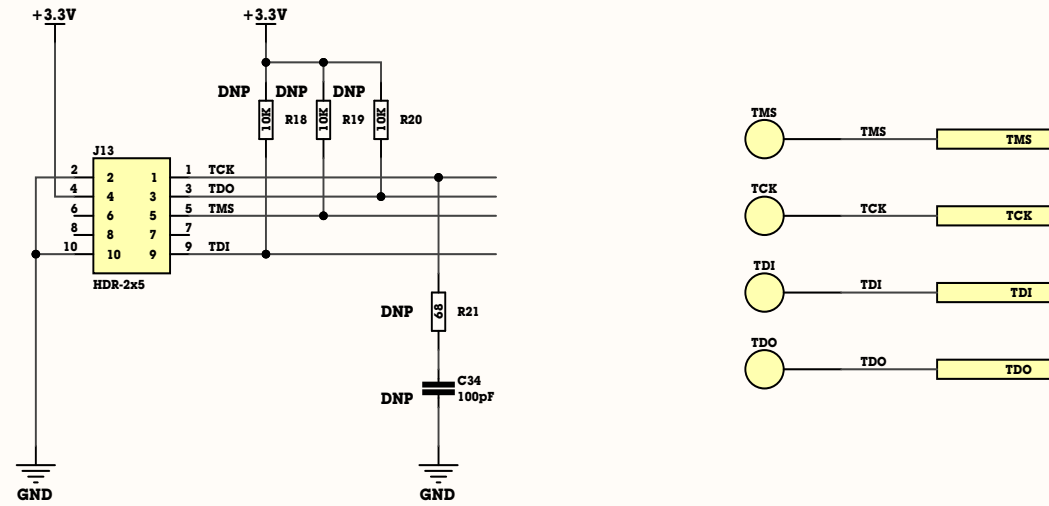
FPGA Slide Switches



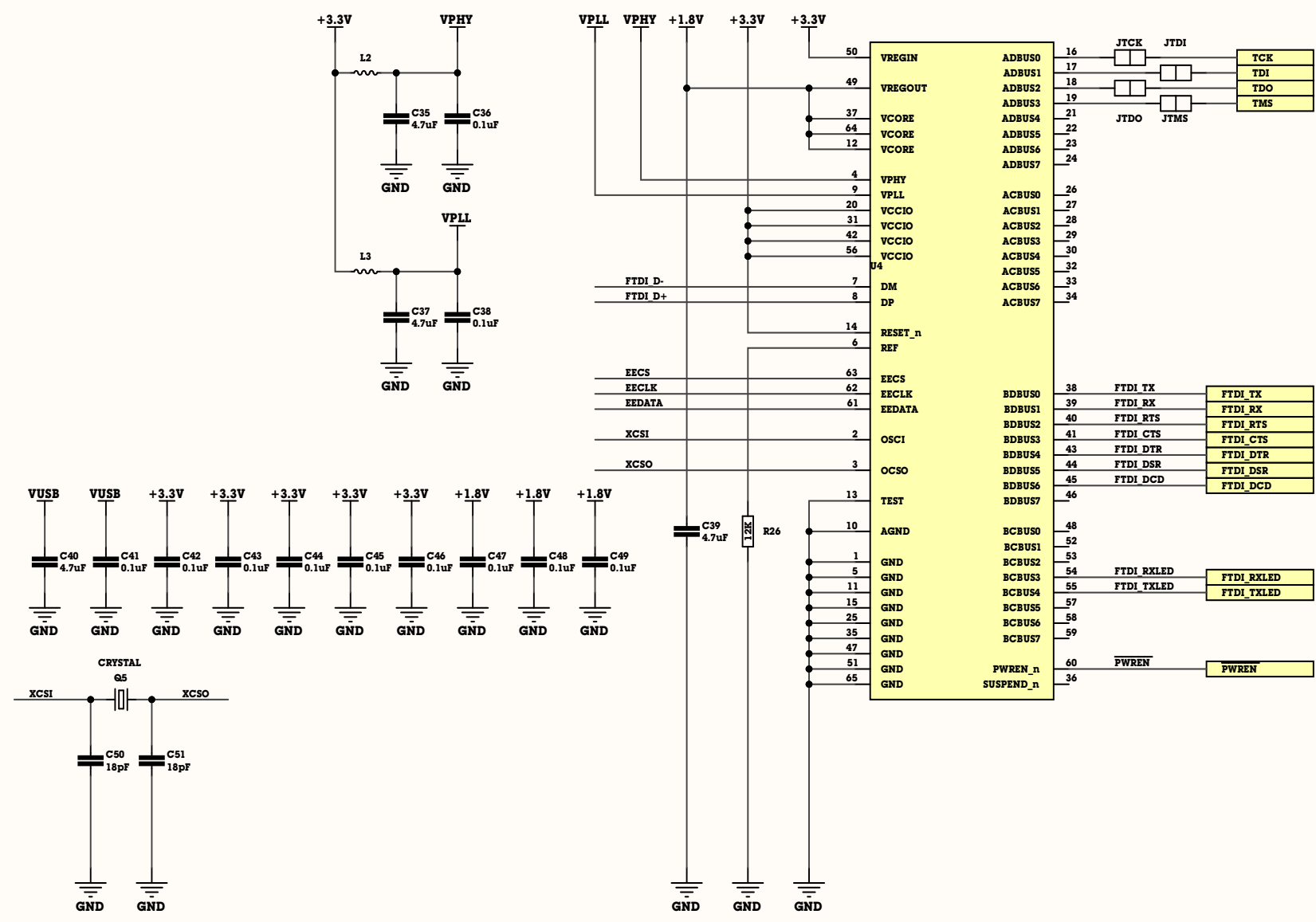
FPGA Side Channel Power Source Select



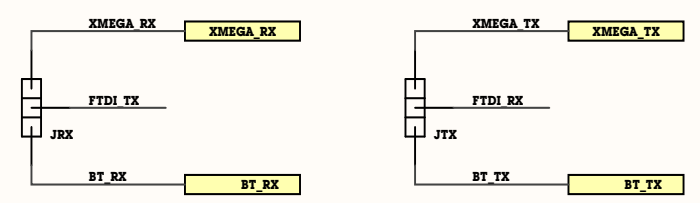
External Programmer JTAG Connector



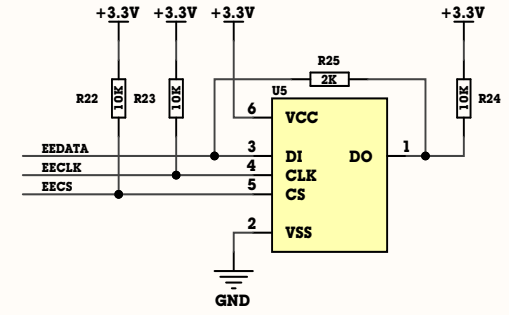
FTDI On-board JTAG and USB to Uart Bridge



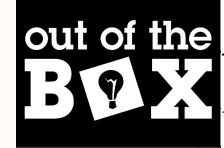
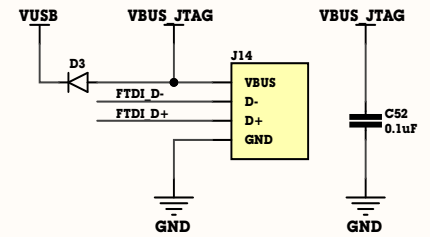
RX/TX Selection Header

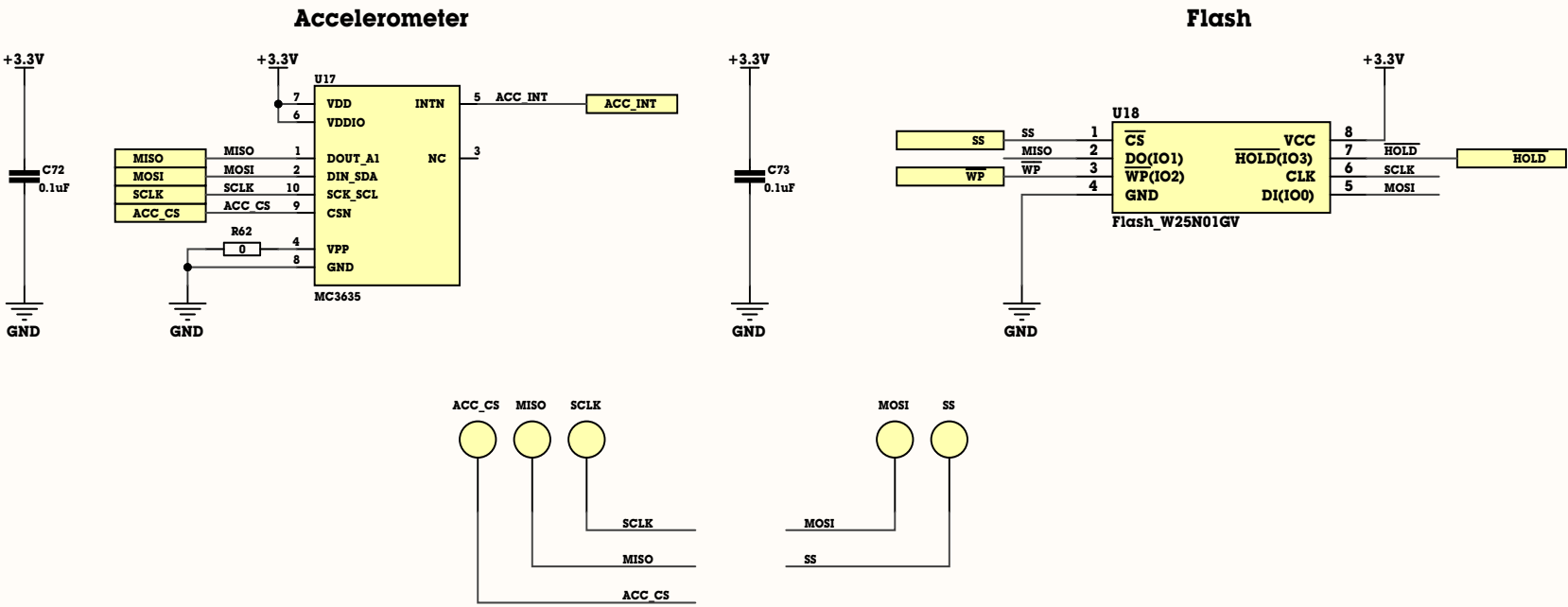
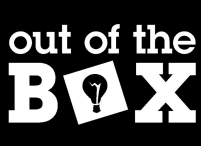


EEPROM for FTDI

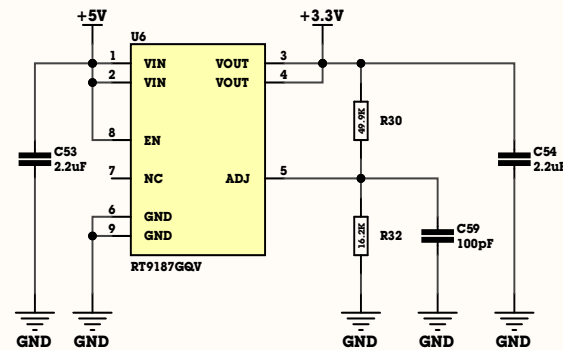


JTAG USB

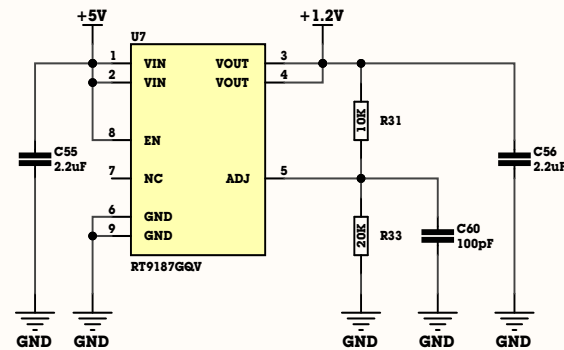




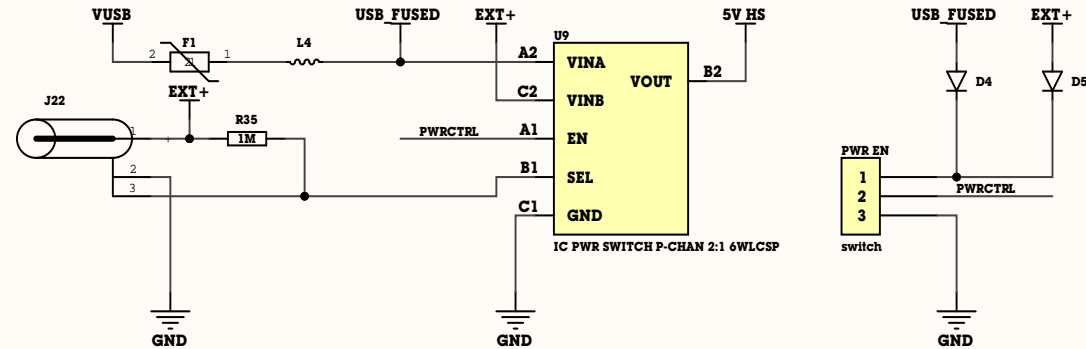
3.3V Supply



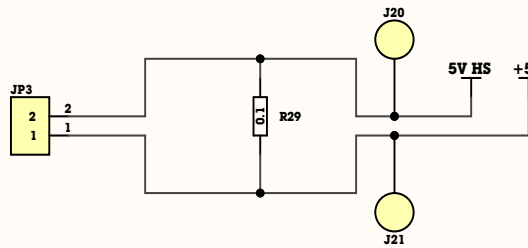
1.2V Supply  
Only populated for Dual Supply FPGA Variant



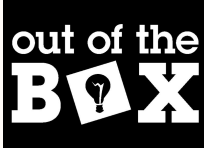
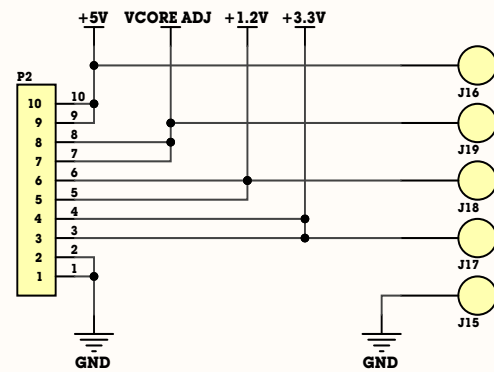
Ideal Diode Power Select



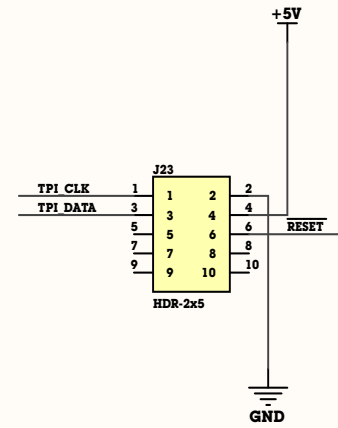
Current Sensing for Whole Board



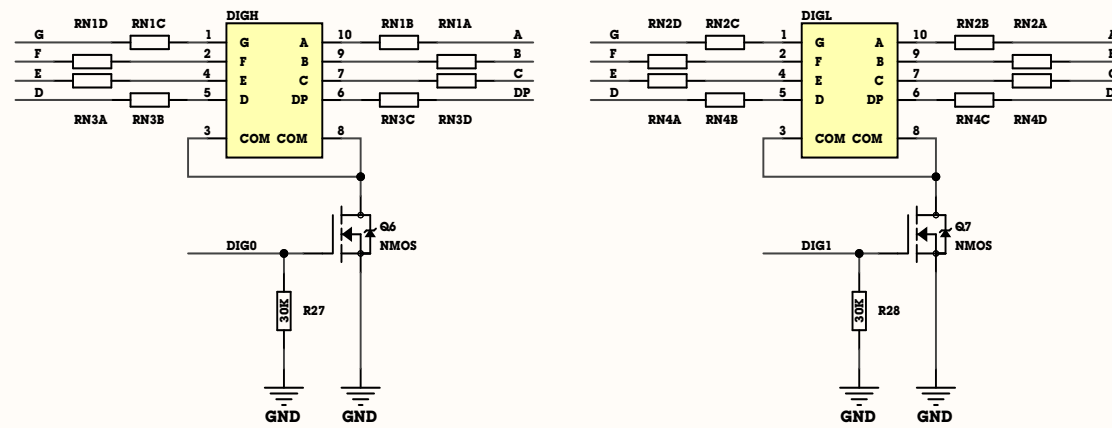
Supply Sources and Probe Points



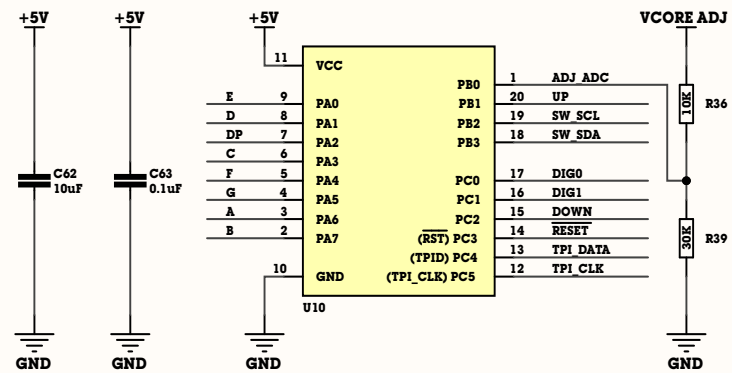
### Adjustable Supply Program Header



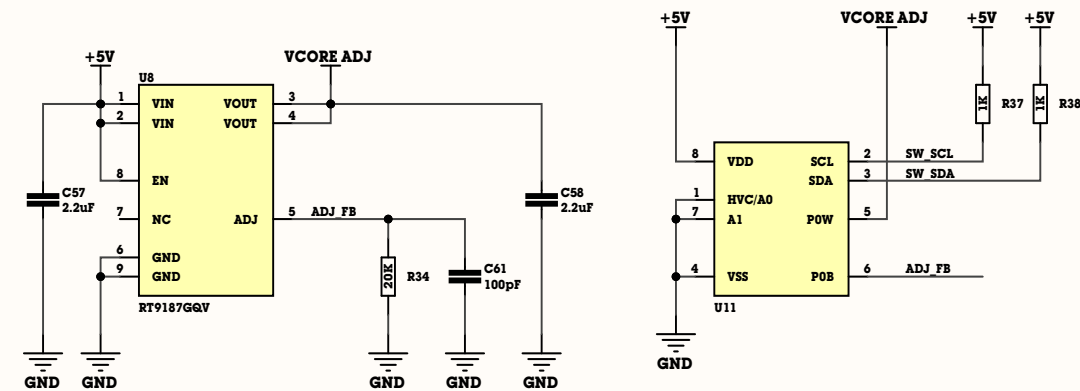
### Adjustable Supply Voltage Display



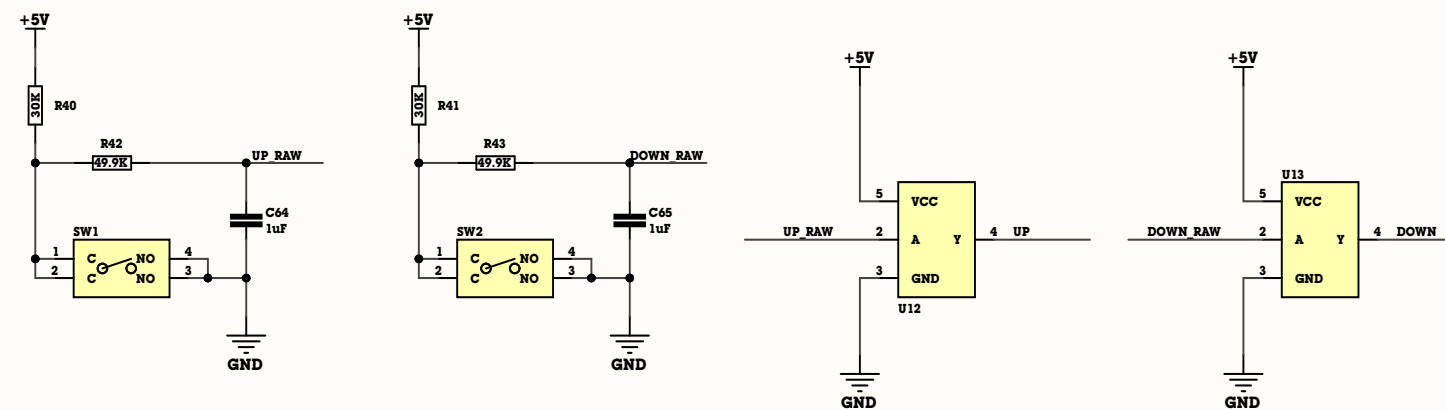
### Adjustable Supply MCU

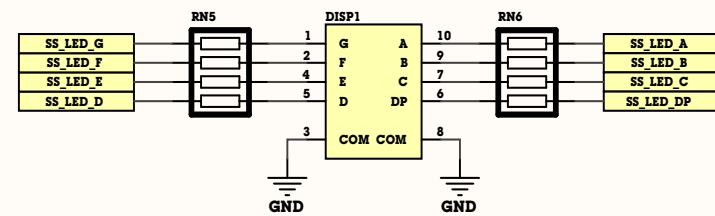
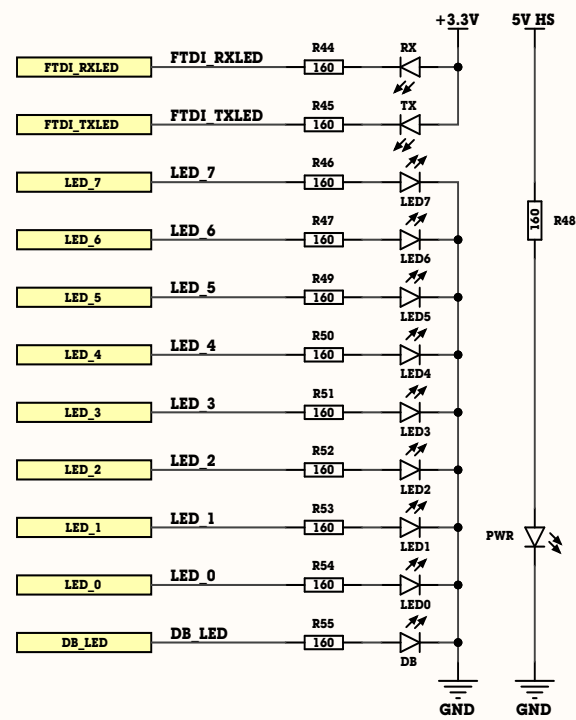


### Adjustable Supply

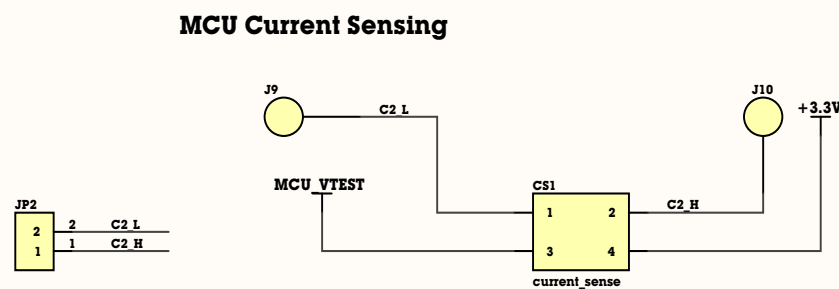
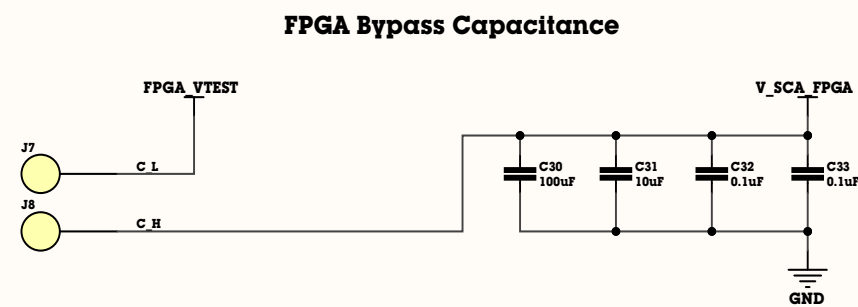
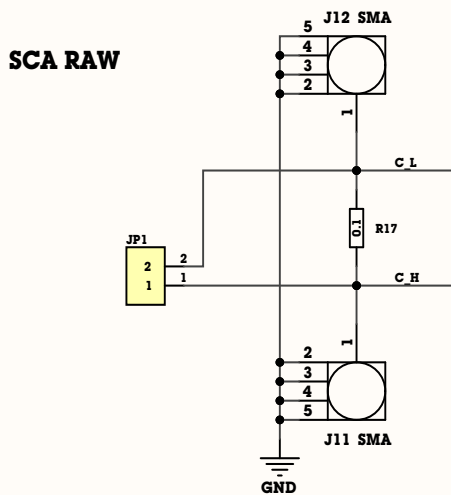
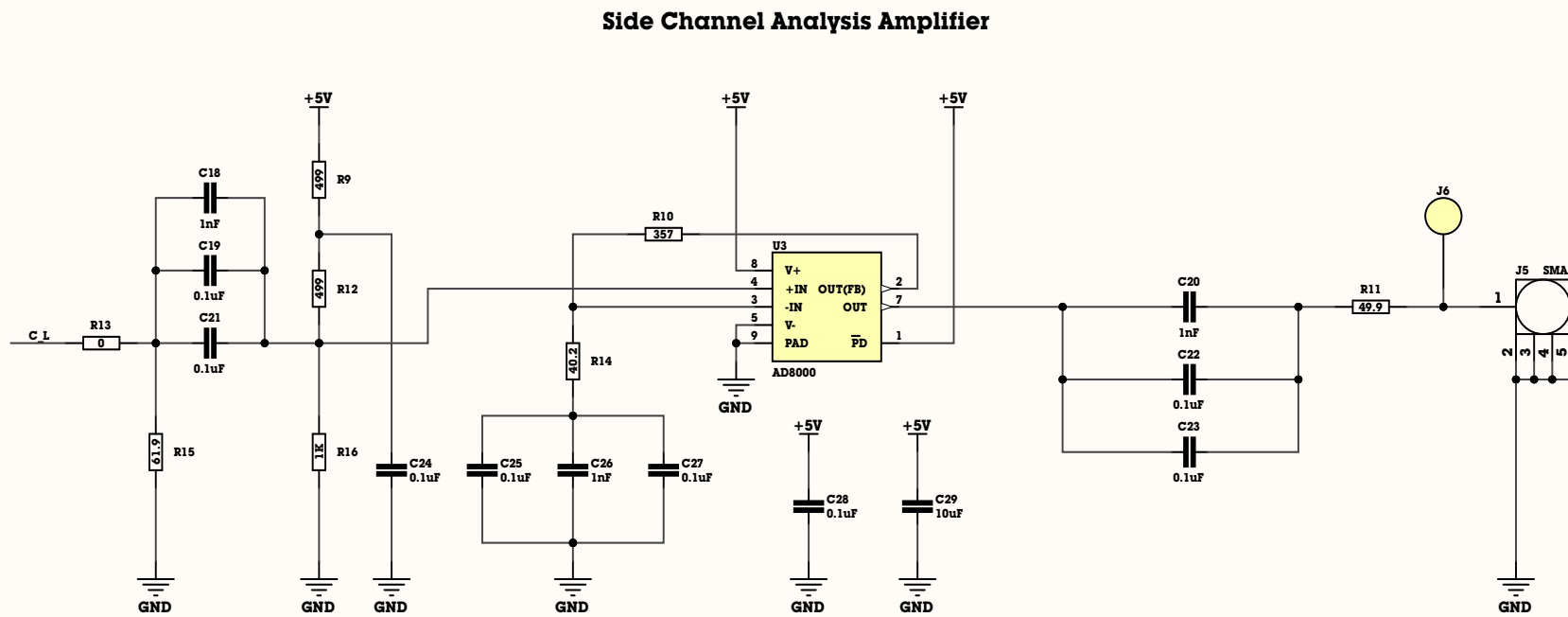


### Adjustable Supply UI









Connection for Optional BT Module

