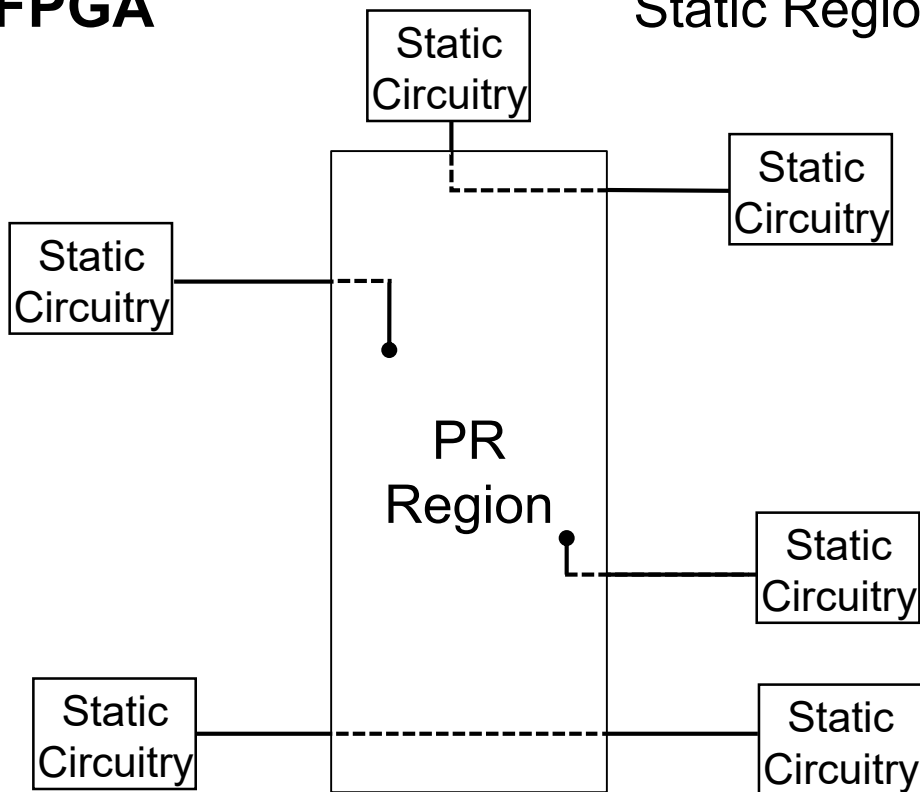


FPGA

Static Region



—● Partition Pin

----- Reserved Static Resource