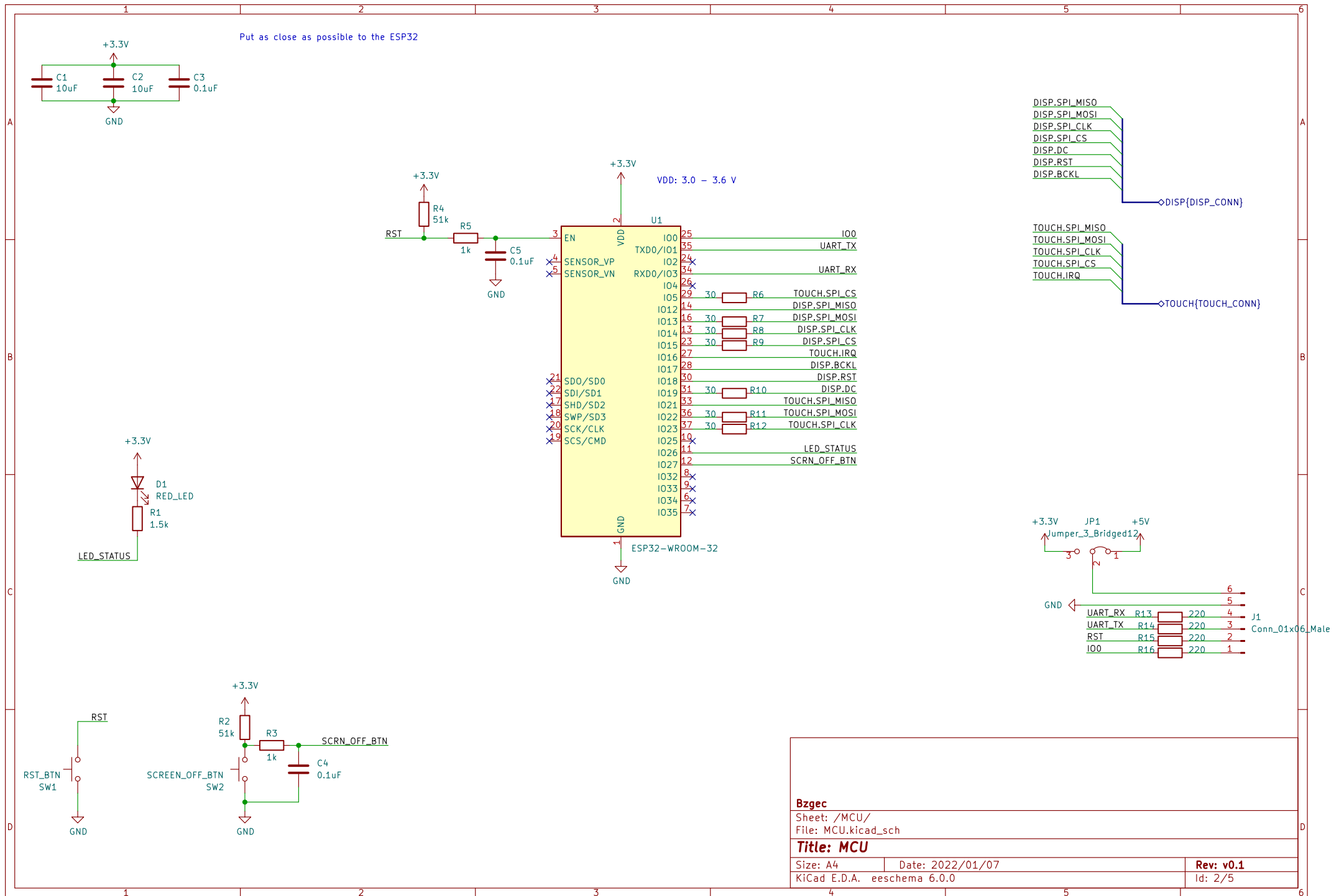
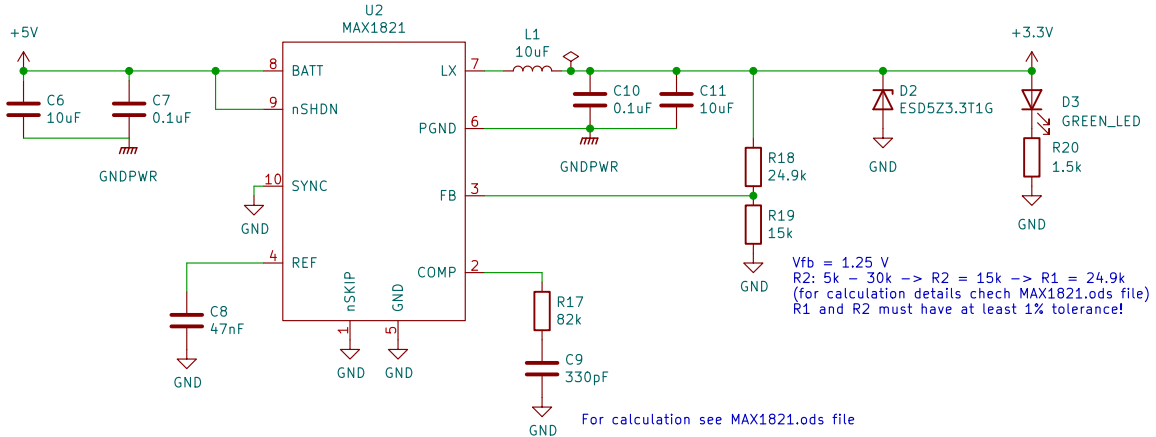


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Title: ESP32_display		
Size: A5	Date: 2022/01/07	Rev: v0.1
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Vin: 2.6 – 5.5 V
Supply Voltage Input. Connect BATT to a 2.6V to 5.5V.
Bypass BATT to PGND with a low-ESR 10µF capacitor.
The output capacitor must have low impedance at the switching frequency (1 MHz).



Connect the inductor, input filter capacitor, and output filter capacitor as close together as possible, and keep their traces short, direct, and wide. Connect their ground pins at a single common node in a star-ground configuration.
The external voltage-feedback network should be very close to the FB pin, within 0.2in (5mm). Keep noisy traces (the LX pin, for example) away from the voltage-feedback network; also, keep them separate, using grounded copper.
Connect GND and PGND at a single point, as close as possible to the MAX1820/MAX1821.



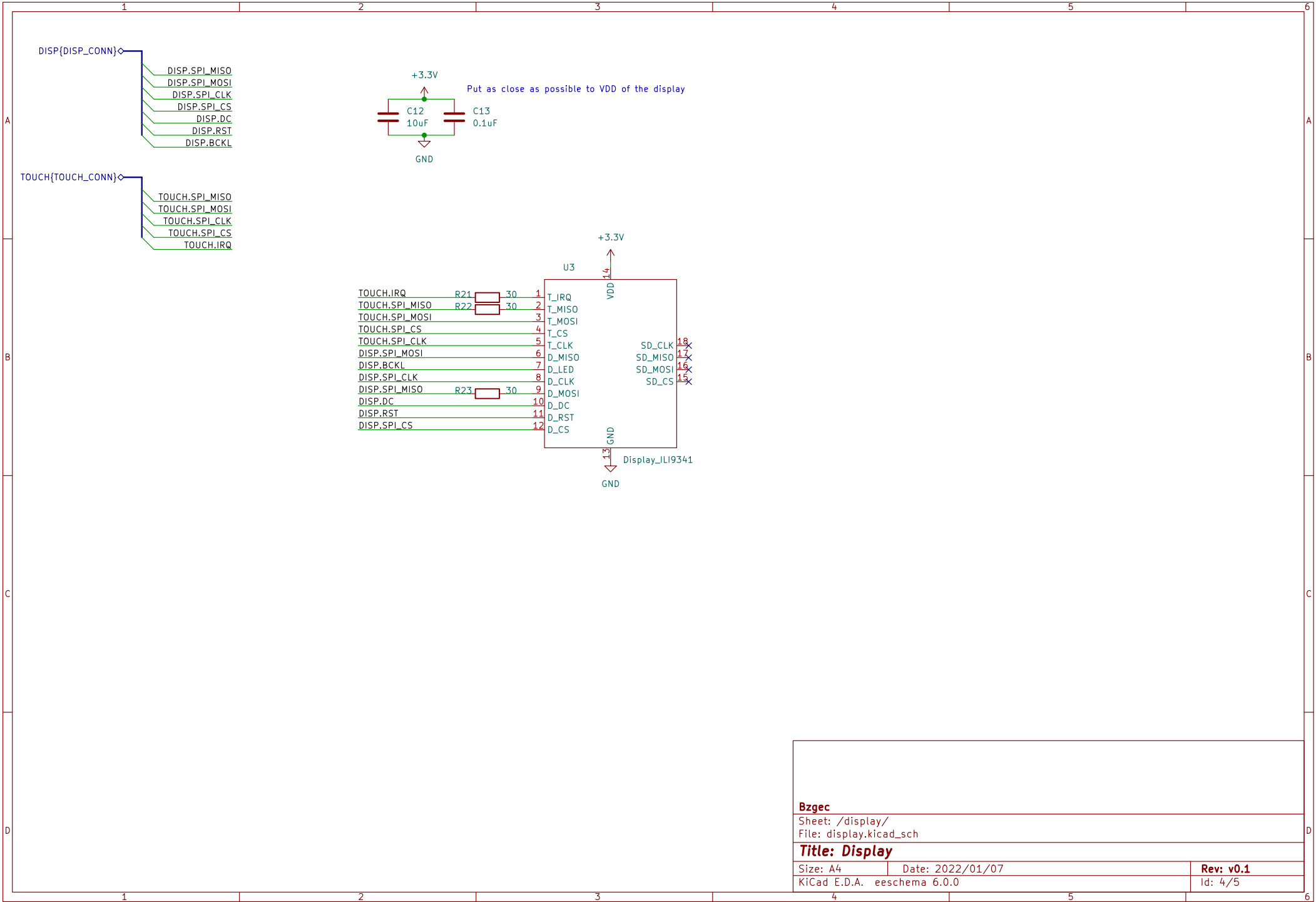
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Size: A4 Date: 2022/01/07
KiCad E.D.A. eeschema 6.0.0

Rev: v0.1
Id: 3/5



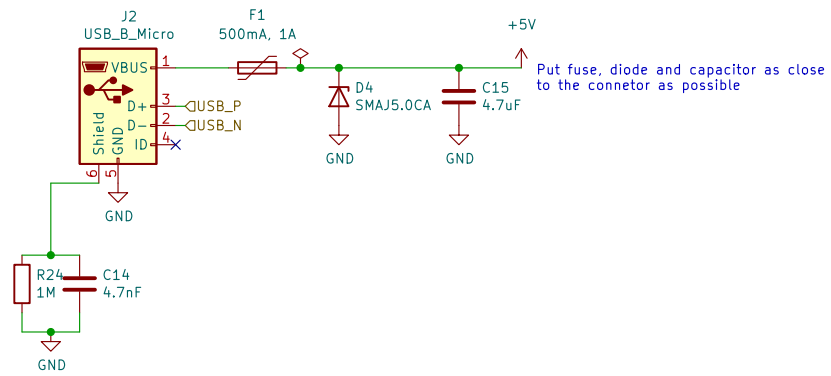
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Sheet: /display/
File: display.kicad_sch

Title: Display

Size: A4 Date: 2022/01/07
KiCad E.D.A. eeschema 6.0.0

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Id: 4/5



Bzgec

Sheet: /USB_CONN/

File: USB_CONN.kicad_sch

Title: USB connector

Size: A4

Date: 2022/01/07

Rev: v0.1

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Id: 5/5