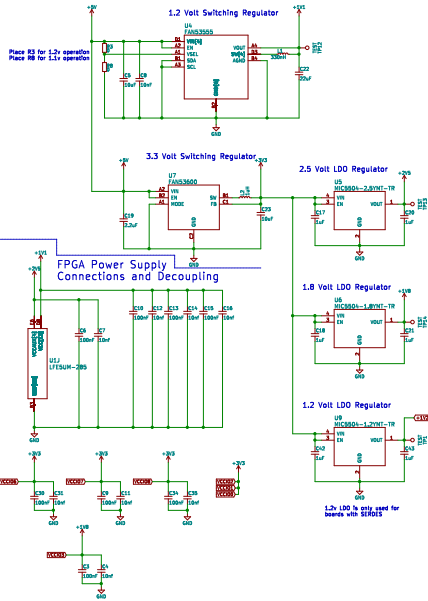
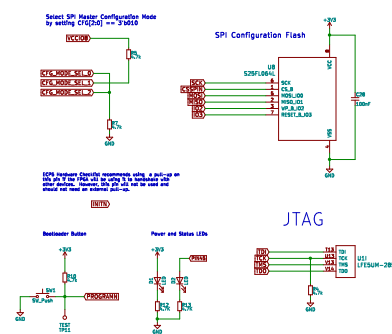


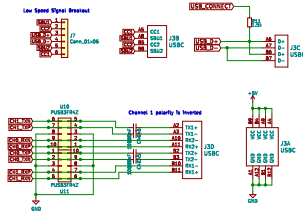
Voltage Regulator Tree



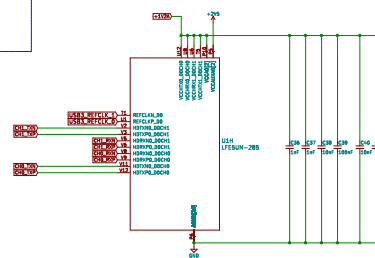
FPGA Bitstream Configuration



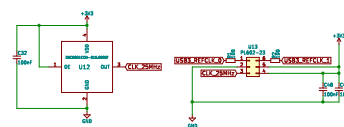
USB Type-C Connector



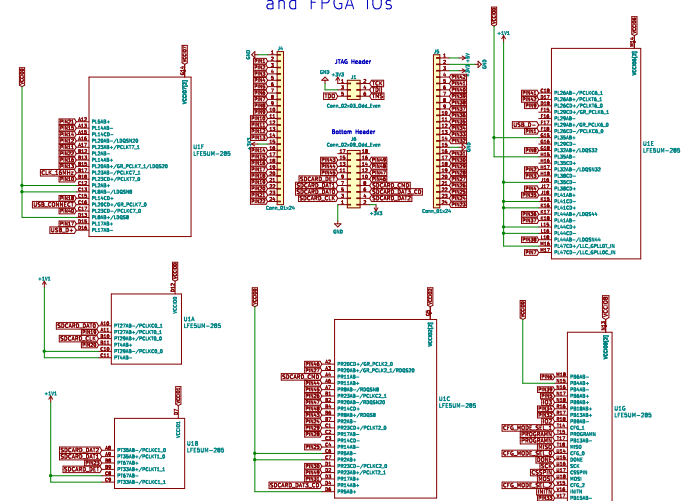
USB3 FPGA SERDES



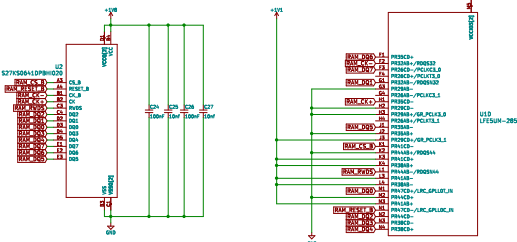
USB3 Reference Clock



Board Connectors and FPGA I/Os



HyperRAM



System Clock

