

1 Homework 1

1.1 Textbook Problem 4.15.5

Consider three different processors P1, P2, and P3 executing the same instruction set. P1 has a 3 GHz clock rate and a CPI of 1.5. P2 has a 2.5 GHz clock rate and a CPI of 1.0. P3 has a 4.0 GHz clock rate and has a CPI of 2.2.

Processor	Clock Rate	CPI	Performance
P1	3 GHz	1.5	2 GHz
P2	2.5 GHz	1.0	2.5 GHz
P3	4.0 GHz	2.2	1.8 GHz

- (a) Which processor has the highest performance expressed in instructions per second?

Answer:

Instructions per Second = Clock Rate/CPI

$$\begin{aligned} P1 &= 3 \text{ GHz} / 1.5 = 2 \text{ GHz} \\ P2 &= 2.5 \text{ GHz} / 1.0 = 2.5 \text{ GHz} \\ P3 &= 4.0 \text{ GHz} / 2.2 = 1.8 \text{ GHz} \end{aligned}$$

P2 has the highest performance expressed in instructions per second

- (b) If the processors each execute a program in 10 seconds, find the number of cycles and the number of instructions.

Answer:

Number of Cycles = Clock Rate * Execution Time

$$\begin{aligned} P1 &= 3 \text{ GHz} * 10\text{s} = 30 \text{ Billion Cycles} \\ P2 &= 2.5 \text{ GHz} * 10\text{s} = 25 \text{ Billion Cycles} \\ P3 &= 4.0 \text{ GHz} * 10\text{s} = 40 \text{ Billion Cycles} \end{aligned}$$

Number of Instructions = Instructions per Second * Execution Time

$$\begin{aligned} P1 &= 2 \text{ Hz} * 10\text{s} = 20 \text{ Billion Instructions} \\ P2 &= 2.5 \text{ GHz} * 10\text{s} = 25 \text{ Billion Instructions} \\ P3 &= 1.8 \text{ GHz} * 10\text{s} = 18 \text{ Billion Instructions} \end{aligned}$$

- (c) We are trying to reduce the execution time by 30% but this leads to an increase of 20% in the CPI. What clock rate should we have to get this time reduction?

Answer:

New CPI = 1.2 * Old CPI
New Execution Time = 0.7 * Old Execution Time
Clock Rate = (CPI * Instructions per Second) / Execution Time

$$\begin{aligned} P1 &= (20 * 1.8) / 0.7 \approx 5.14 \text{ GHz} \\ P2 &= (25 * 1.2) / 0.7 \approx 4.29 \text{ GHz} \\ P3 &= (18 * 2.64) / 0.7 \approx 6.79 \text{ GHz} \end{aligned}$$

1.2 Textbook Problem 4.15.8

Compilers can have a profound impact on the performance of an application. Assume that for a program, compiler A results in a dynamic instruction count of $1.0E9$ and has an execution time of 1.1 s, while compiler B results in a dynamic instruction count of $1.2E9$ and an execution time of 1.5 s.

- (a) Find the average CPI for each program given that the processor has a clock cycle time of 1 ns.
- (b) Assume the compiled programs run on two different processors. If the execution times on the two processors are the same, how much faster is the clock of the processor running compiler A's code versus the clock of the processor running compiler B's code?
- (c) A new compiler is developed that uses only $6.0E8$ instructions and has an average CPI of 1.1. What is the speedup of using this new compiler versus using compiler A or B on the original processor?

1.3 Textbook Problem 4.15.10

1.4 Textbook Problem 4.15.13

1.5 Textbook Problem 4.15.14

1.6 Textbook Problem 4.15.15

1.7 Textbook Problem 4.15.16