

# Artem Shimko

Software engineer and FPGA developer

Nis, Serbia

(+381) 616-547-441 | artyom.shimko@gmail.com | [linkedin](#) | [github](#)

## Summary

5+ years experience in embedded systems development (Xilinx FPGAs and SoC, Embedded Linux).

## Languages

1. Russian: Native;
2. Serbian: Based
3. English: Limited working proficiency;

## Skills

1. Programming Languages: C, C++, SystemVerilog, asm (ARM), Python, Bash, tcl;
2. IDEs: SDK, Eclipse, Vivado, Vitis;
3. Technologies: 1 - 10GE, PCIe, Linux LKMs, bare metal drivers, FPGA IP cores, Linux userspace, Linux system programming, bare metal applications, patches;
4. Tools: Docker, Git, Jira, make, Azure DevOps, Petalinux;

## Work Experience

**Promwad, electronics design house**, Niš, Centralna Srbija, Serbia

Software Engineer

Jun. 2022 - Present

- ❖ Working with petalinux, writing LKMs and standalone drivers for Zynqmp and developing FPGA modules and IP cores;
- ❖ Developing system programs for the SoC PS and host PC for testing interfaces;

**Eurica**, St Petersburg City, Russia

Software and FPGA Developer

Sep 2020 - May 2022, 1 yr 9 mos

- ❖ Participated in the development of data transmission platforms for ethernet (1GE and 10GE) and PCIe from SOC PL and PS sides (Zynqmp and Zynq).

**Kotlin-Novator**, St Petersburg City, Russia

Software engineer and FPGA developer

Oct 2017 - Aug 2020, 2 yrs 11 mos

- ❖ Developed a bootloader with a backup function and simultaneous high-speed transfer of an image to each of the processors in the computing unit using PCIe;
- ❖ Implemented a high-speed data transmission interface based on an optical channel;

## Education

**SUAI (Saint Petersburg State University of Aerospace and Instrumentation)**, St.Petersburg City, Russian

MEng in Computer Science and Engineering

Sep 2020 - Aug 2022