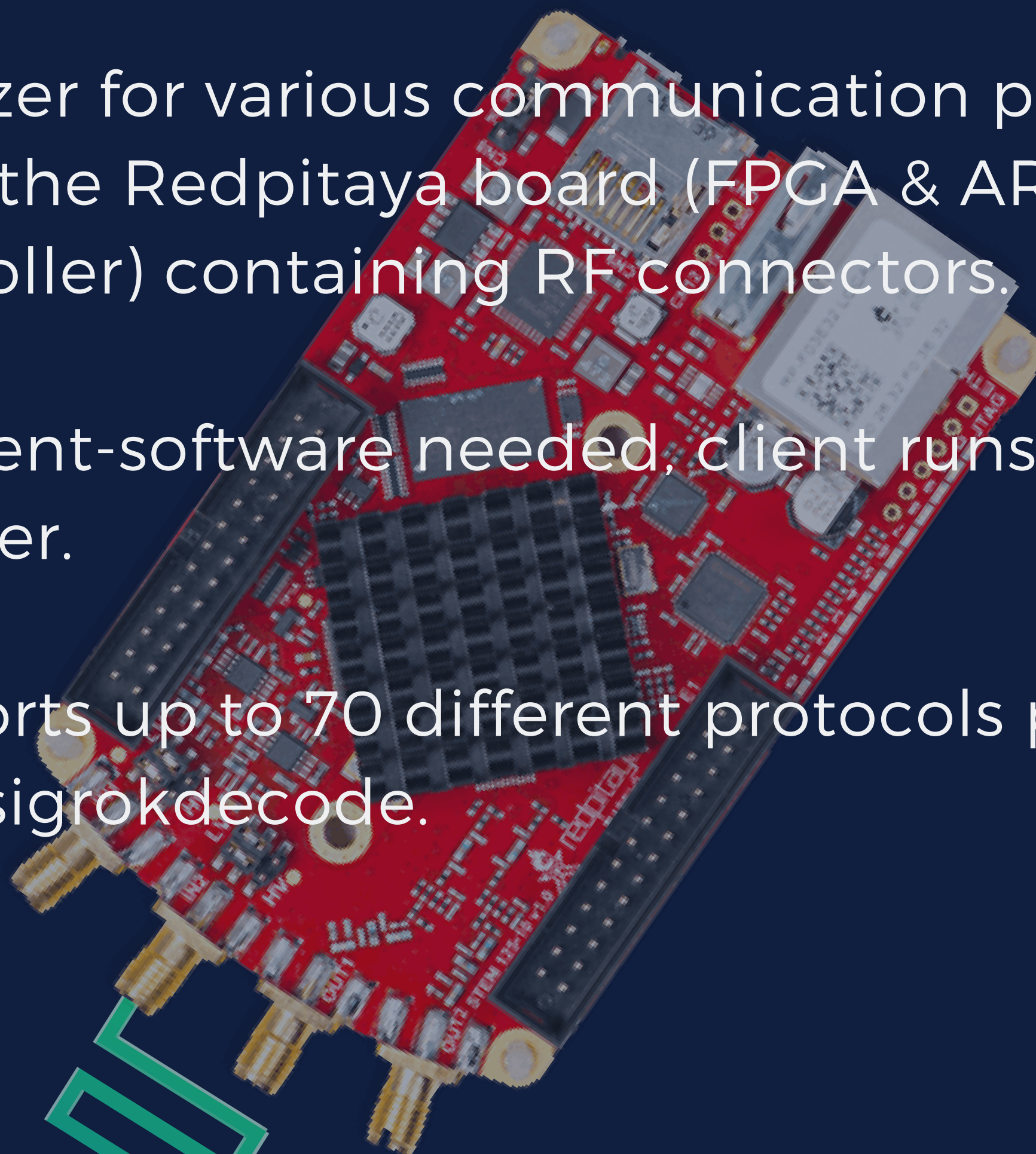OTH REGENSBURG
FACHSCHAFT INFORMATIK MATHEMATIK
BETREUUNG: PROF. DANIEL MÜNCH

REDPITAYA LOGIC-ANALYZER

- Analyzer for various communication protocols using the Redpitaya board (FPGA & ARM-Controller) containing RF connectors.

- No client-software needed, client runs in the browser.

- Supports up to 70 different protocols provided by libsigrokdecode.

Alexander Schmid, Florian Henneke, Michael Schneider, Sebastian Ederer