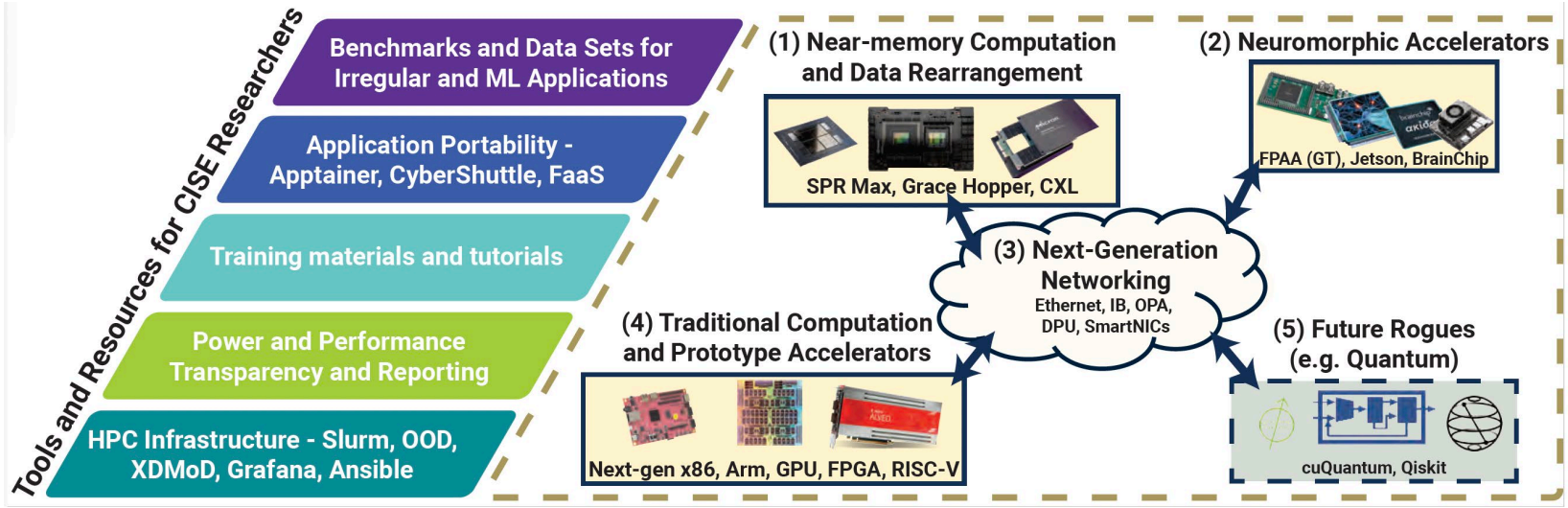
Georgia Tech Rogues Gallery

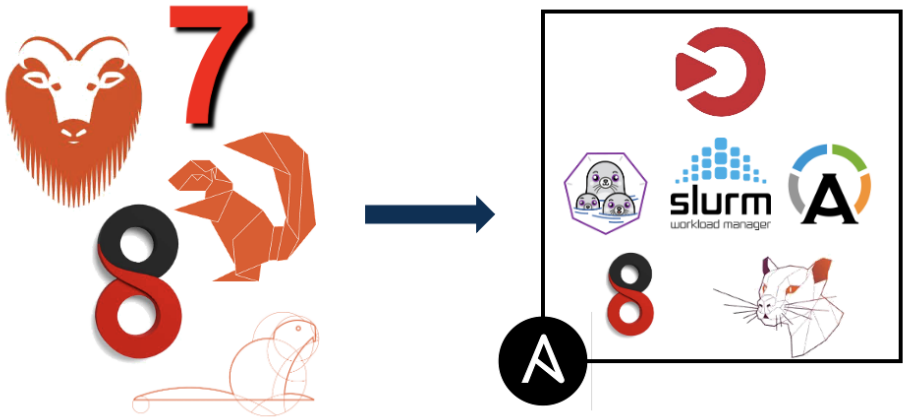


- Intel (ICX, SPR, SPR-Max) and AMD (Milan) x86
- Nvidia (A40, A100, H100) and AMD (MI210) GPUs
- Xilinx (Alveo and Zynq) and Intel (Arria and Stratix) FPGAs
- Lucata Pathfinder Near-memory system
- Fujitsu (A64FX), Ampere (Neoverse), Nvidia (Grace) Arm
- InfiniBand, OmniPath, and Ethernet fabrics
- Nvidia DPUs (BF2 and BF3) and Xilinx SmartNICs

Georgia Tech College of Computing
Center for Research into Novel Computing Hierarchies

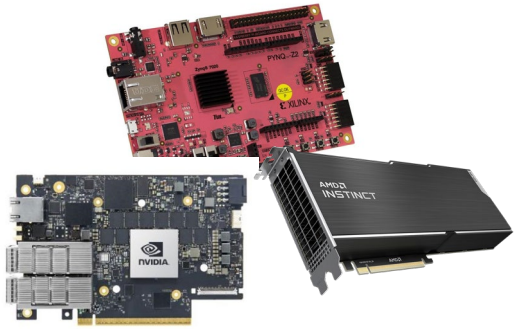
Keys to Growth and Success

1 Adopting Enterprise Infrastructure/Best-practices



2 Focusing on Essential Software

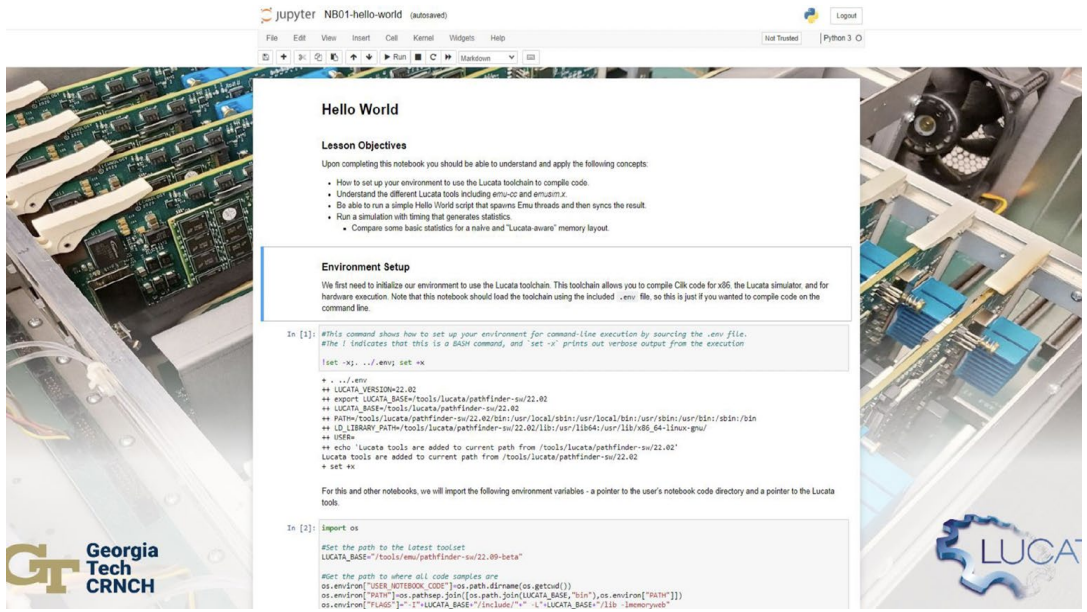
3 Target Specific Needs or Applications

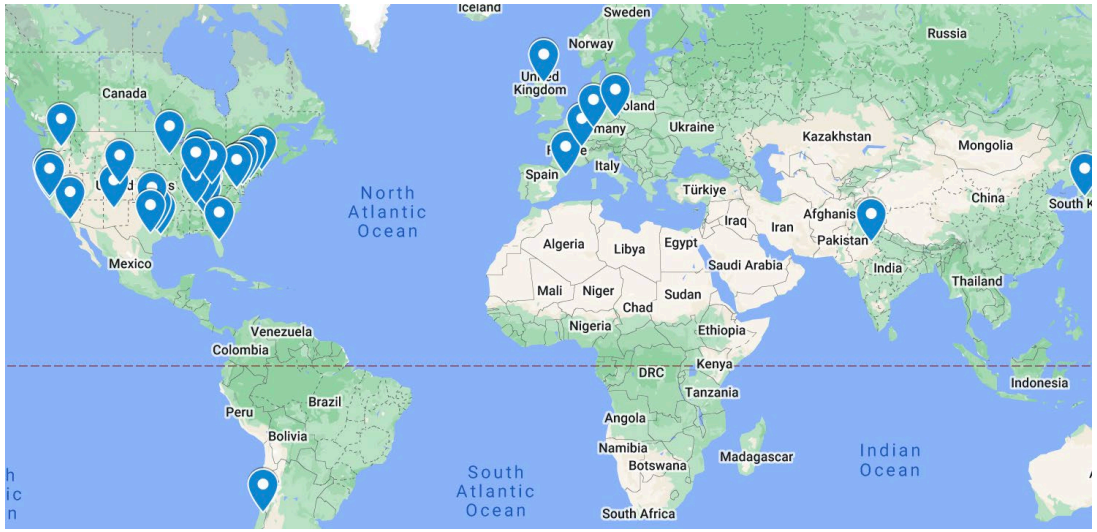


Impact of the Rogues Gallery: Research

Tutorials to Empower Users

- 13 tutorials including ASPLOS, PEARC, HPEC, MICRO, HI, SC
- Also support faculty-driven efforts
  - CSE tutorial for Vortex, an open-source RISC-V GPGPU

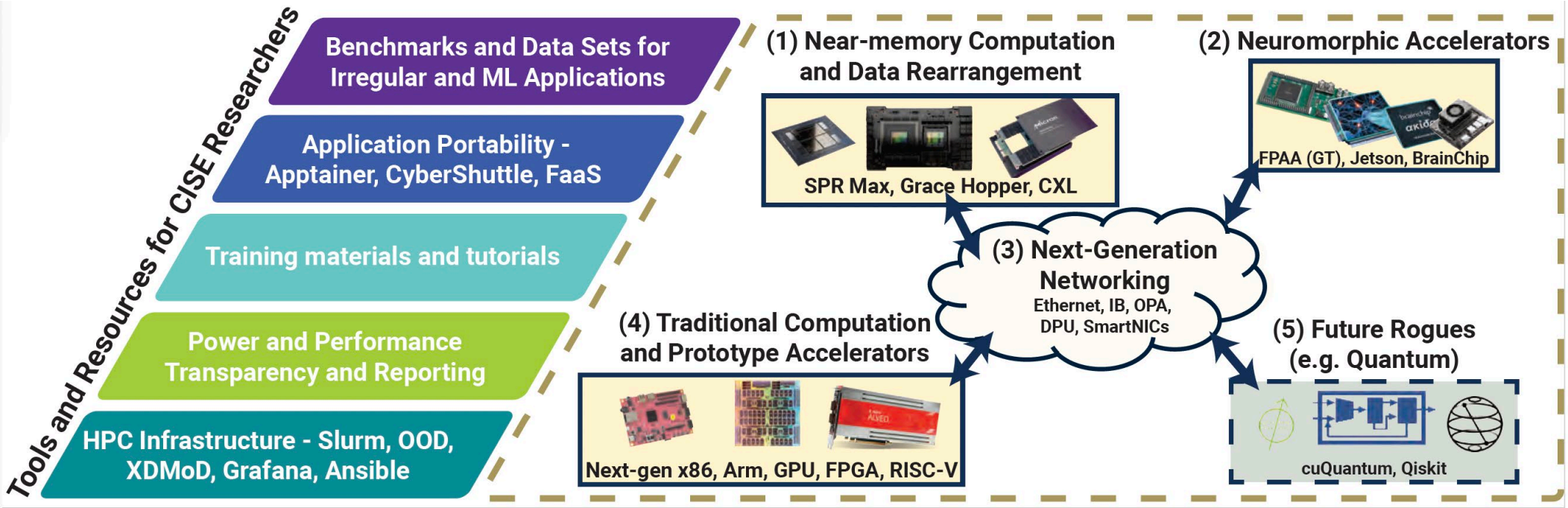




Supporting the Research Community Beyond GT

- 400 total users, of those 80 are guests
  - National labs, vendors, other academics
- User-driven documentation
- Roughly 25+ publications, 15 workshops
  - Novel algorithms/workflows leveraging the architecture and/or administering heterogeneity

CRNCH RG



Learn more and request access: https://crnch-rg.cc.gatech.edu/

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