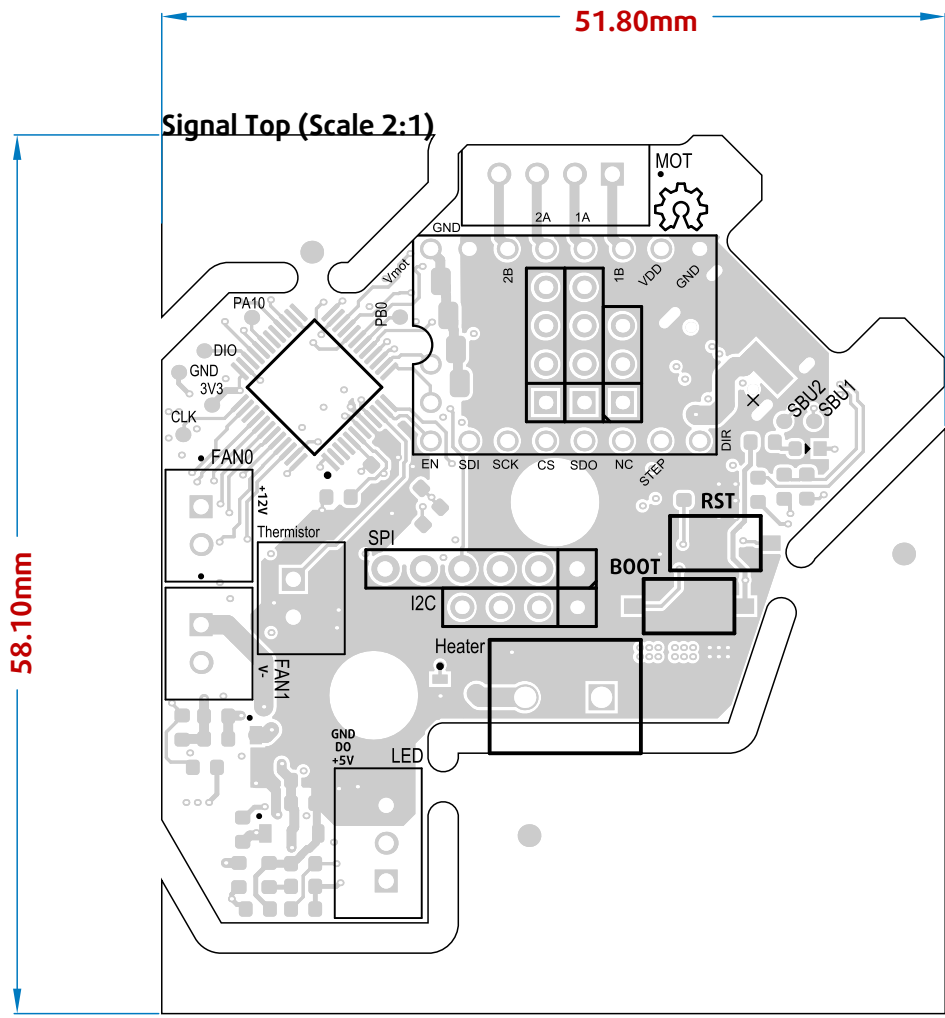
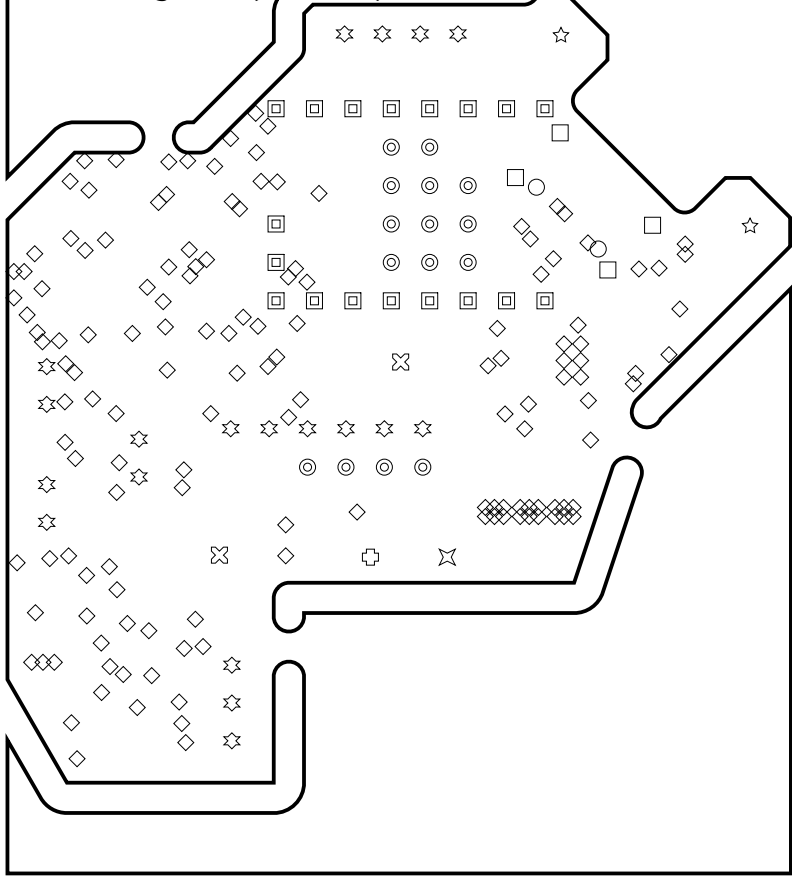


<b>PCB Name</b>	<b>StealthBurner-PD</b>
<b>Size</b>	<b>Xmm x Xmm (see drawing)</b>
<b>Attached Layerstack mandatory</b>	<b>no</b>
<b>Layers</b>	<b>4</b>
<b>Thickness</b>	<b>1.6mm</b>
<b>Material</b>	<b>FR4 / Epoxy</b>
<b>Copper-Thickness top/bottom</b>	<b>35µm</b>
<b>Copper-Thickness inner layers</b>	<b>17.5µm</b>
<b>Surface</b>	<b>chemical Ni/Au</b>
<b>Solder mask</b>	<b>double-sided (black or matte-black)</b>
<b>Peelable mask</b>	<b>no</b>
<b>Scoring</b>	<b>no</b>
<b>UL required</b>	<b>no</b>
<b>Outline/Shape</b>	<b>milling</b>
<b>Chamfer</b>	<b>no</b>
<b>Electrical Test</b>	<b>yes</b>
<b>Impedance</b>	<b>no</b>
<b>IPC/Perfag</b>	<b>Class 2</b>
<b>internal millings</b>	<b>yes</b>
<b>Silkscreen Top</b>	<b>yes</b>
<b>Silkscreen Bottom</b>	<b>yes</b>
<b>Pressfit</b>	<b>no</b>



Drill Drawing View (Scale 2:1)



Drill Table

Symbol	Count	Hole Size	Plated	Via / Pad	Hole Tolerance
◇	144	0.20mm(8mil)	Plated	Via	
○	2	0.67mm(27mil)	Non-Plated	Pad	
□	4	0.70mm(28mil)	Plated	Pad	
◎	15	0.89mm(35mil)	Plated	Pad	
▣	18	0.91mm(36mil)	Plated	Pad	
☆	19	1.00mm(39mil)	Plated	Pad	
⊕	1	1.40mm(55mil)	Plated	Pad	
✕	1	1.40mm(55mil)	Plated	Pad	
☆	2	2.20mm(87mil)	Plated	Pad	
✕	2	3.20mm(126mil)	Plated	Pad	
	208 Total				