

Q1:

- a) Done
- b) See “oaa54_cs3339_HW2_Q1_part_1.b.asm” for implementation
- c) See “oaa54_cs3339_HW2_Q1.c_loop1-4.png”
- d) It took me 26 instructions and pseudo-instructions to implement the code from part 1.c in MIPS (this includes setting up the array and printing to the screen). It took 15 of those instructions to implement the C code directly.
- e) See “oaa54_cs3339_HW2_Q1_part_1.e.asm” for implementation

Q2:

- a) See the recursive section from part B or C
- b) See “oaa54_cs3339_HW2_q2.part2.b.asm” and “oaa54_cs3339_HW2_Q2.b_ss1-10.png”
- c) See “oaa54_cs3339_HW2_q2.part2.c.asm”

Q3:

See “oaa54_cs3339_HW2_q3.asm”