SPECIFICATION FOR APPROVAL

on

(•)	Final	Spe	cific	ation
١.	•	,			•	

Title		10.1" WX TFT LC	CD
Customer		SUPPLIER	LG Display Co., Ltd.
MODEL		*MODEL	LP101WX1
		Suffix	SLN2

^{*}When you obtain standard approval, please use the above model name without suffix

	APPROVED BY	SIGNATURE				
_	1					
_	1					
_	1					
	Please return 1 copy for your confirmation with your signature and comments.					

APPROVED BY	SIGNATURE			
S. W. Paeng / Manager				
REVIEWED BY				
P. A. Choi / Manager				
PREPARED BY				
K. C. Choi / Engineer				
Products Engineering Dept. LG Display Co., Ltd				

Ver. 1.1 May. 15, 2011 1 / 27



Contents

No	ITEM					
	COVER	1				
	CONTENTS	2				
	RECORD OF REVISIONS	3				
1	GENERAL DESCRIPTION	4				
2	ABSOLUTE MAXIMUM RATINGS	5				
3	ELECTRICAL SPECIFICATIONS					
3-1	ELECTRICAL CHARACTREISTICS	6				
3-2	INTERFACE CONNECTION	7				
3-3	LVDS SIGNAL TIMING SPECIFICATIONS	8				
3-4	SIGNAL TIMING SPECIFICATIONS	10				
3-5	SIGNAL TIMING WAVEFORMS	10				
3-6	COLOR INPUT DATA REFERNECE	11				
3-7	POWER SEQUENCE	12				
4	OPTICAL SFECIFICATIONS	13				
5	MECHANICAL CHARACTERISTICS	16				
6	RELIABLITY	20				
7	INTERNATIONAL STANDARDS]				
7-1	SAFETY	21				
7-2	EMC	21				
8	PACKING]				
8-1	DESIGNATION OF LOT MARK	22				
8-2	PACKING FORM	22				
9	PRECAUTIONS	23				
Α	APPENDIX. Enhanced Extended Display Identification Data	25				

Ver. 1.1 May. 15, 2011 2 / 27



RECORD OF REVISIONS

Revision No	Revision Date	Page	Description	EDID ver
0.0	Dec. 2 2010	All	First Draft (Preliminary Specification)	-
0.1	Dec. 17 2010	22	Packing Form Adding	-
		6	LED Power Input Voltage change(7~21V→5~21V)	-
		4	Anti-Reflection adding	-
0.2	Dec. 20 2010	19	Rear View update	-
		4	Power Consumption update	-
0.3	Dec. 31 2010	4	Power Consumption update	-
		6	Logic Current, Power Consumption update	-
		14	Response Time update(GTG Adding)	-
0.4	Feb. 10 2011	11	Timing Table update	-
		25~27	EDID update	-
		18,19,22	Mechanical Drawing, Packing update	-
0.5	Mar. 27 2011	1	Model Name change(LP101WX1-SLN1→LP101WX1-SLN2)	-
		4,6	Logic Power Consumption update (1.16W max. White Pattern→1.15W max @White Pattern)	-
		4	Pixel Pitch change	-
		14	R,G, B Color Coordinates Spec update	-
		15	Gray Scale Spec update.	-
		25~27	EDID update	-
1.0	Apr. 25 2011	-	Final CAS	-
		25~27	EDID update	-
		4	Luminance update(TBD→350 cd/m²)	
		19	Rear Dimension update.	-
		22	Packing Form update	-
		11	Dclk Spec update(Dclk(Max.) : 74.5Mhz→77.0Mhz	
1.1	Mayr. 11 2011	19	Mechanical Drawing Label P/N is corrected	
		11	Dclk typ. Is corrected. (71Mhz → 69.3Mhz)	

Ver. 1.1 May. 15, 2011 3 / 27

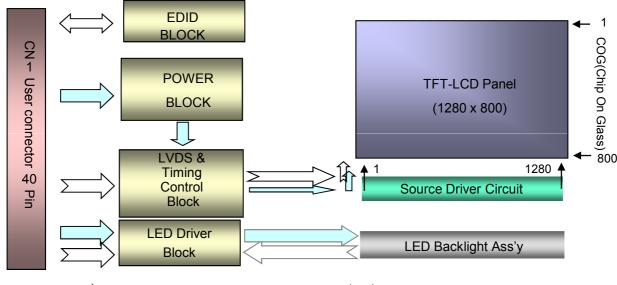


1. General Description

The LP101WX1 is a Color Active Matrix Liquid Crystal Display with an integral LED backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 10.1inches diagonally measured active display area with HD resolution(1280 horizontal by 800 vertical pixel array). Each pixel is divided into Red, Green and Blue subpixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP101WX1 has been designed to apply the interface method that enables low power, high speed, low EMI.

The LP101WX1 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP101WX1 characteristics provide an excellent flat display for office automation products such as Notebook PC.



General Features	Control & Data	Power	EDID signal & Power

Active Screen Size	10.1 inches diagonal
Outline Dimension	229.46(H) × 149.2(V)x5.2(D,Max.) [mm]
Pixel Pitch	0.1695mm × 0.1695 mm
Pixel Format	1280 horiz. By 800 vert. Pixels RGB strip arrangement
Color Depth	6-bit, 262,144 colors
Luminance, White	350 cd/m ² (Typ.5 point)
Power Consumption	Total 3.33 W(Max.) (Logic :1.15 W (max. @ White), B/L : 2.18W (max.@ VLED 12V)
Weight	160g (Max.)
Display Operating Mode	Transmissive mode, normally Black
Surface Treatment	Glare treatment of the front polarizer(Anti-Reflection≤1.5%)
RoHS Compliance	Yes
BFR/PVC/As Free	Yes for all

Ver. 1.1 May. 15, 2011 4 / 27



2. Absolute Maximum Ratings

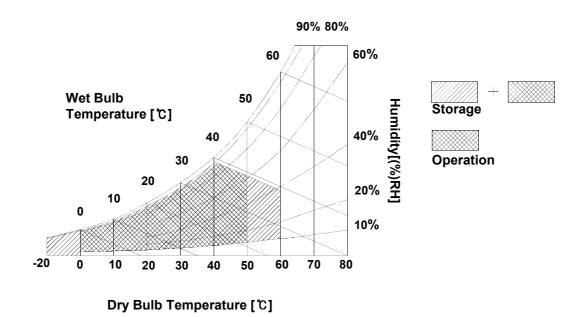
The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol		ues	Units	Notes	
Farameter	Syllibol	Min	Max	Office	Notes	
Power Input Voltage	VCC	-0.3	4.0	Vdc	at 25 ± 5°C	
Operating Temperature	Тор	0	50	°C	1	
Storage Temperature	Нѕт	-20	60	°C	1	
Operating Ambient Humidity	Нор	10	90	%RH	1	
Storage Humidity	Нѕт	10	90	%RH	1	

Note: 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39°C Max, and no condensation of water.



Ver. 1.1 May. 15, 2011 5 / 27



3. Electrical Specifications

3-1. Electrical Characteristics

The LP101WX1 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the LED BL.

Table 2. ELECTRICAL CHARACTERISTICS

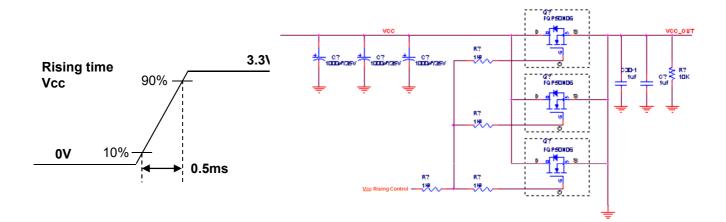
Parameter		Complete		Values		l l mit	Notes
		Symbol	Min	Тур	Max	Unit	Notes
LOGIC:							
Power Supply Input Voltage		VCC	3.0	3.3	3.6	V	1
Power Supply Input Current	White	ICC	-	303	348	mA	2
Power Consumption		PCC	-	1.0	1.15	W	2
Power Supply Inrush Current		ICC_P	-		1500	mA	3
LVDS Impedance		ZLVDS	90	100	110	Ω	4
BACKLIGHT : (with LED Driver)							
LED Power Input Voltage		VLED	5.0	12.0	21.0	V	5
LED Power Input Current		ILED	-	170	181	mA	6
LED Power Consumption		PLED	-	2.04	2.18	W	6
LED Power Inrush Current		ILED_P	-		2000	mA	7
PWM Duty Ratio			1	-	100	%	8
PWM Jitter		-	0	-	0.2	%	9
PWM Impedance		ZPWM	20	40	60	kΩ	
PWM Frequency		FPWM	200	-	1000	Hz	
PWM High Level Voltage		V_{PWM_H}	2.2	-	5.3	V	
PWM Low Level Voltage		V_{PWM_L}	0	-	0.3	V	
LED_EN Impedance		ZPWM	20	40	60	kΩ	
LED_EN High Voltage		VLED_EN _H	2.2	-	5.3	V	
LED_EN Low Voltage		VLED_EN _L	0	-	0.3	V	
Life Time			12,000	-	-	Hrs	10

Ver. 1.1 May. 15, 2011 6 / 27



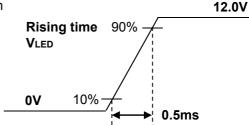
Note)

- 1. The measuring position is the connector of LCM and the test conditions are under 25 $^{\circ}$ C, fv = 60Hz, Black pattern.
- 2. The specified lcc current and power consumption are under the Vcc = 3.3V , 25°C , fv = 60Hz condition and White pattern.
- 3. The below figures are the measuring Vcc condition and the Vcc control block LGD used. The Vcc condition is same as the minimum of T1 at Power on sequence.



- 4. This impedance value is needed for proper display and measured form LVDS Tx to the mating connector.
- 5. The measuring position is the connector of LCM and the test conditions are under 25 $^{\circ}$ C.
- 6. The current and power consumption with LED Driver are under the Vled = 12.0V, 25℃, Dimming of Max luminance and White pattern with the normal frame frequency operated(60Hz).
- 7. The below figures are the measuring Vled condition and the Vled control block LGD used.

VLED control block is same with Vcc control block.



- 8. The operation of LED Driver below minimum dimming ratio may cause flickering or reliability issue.
- 9. This Spec. is not effective at 100% dimming ratio as an exception because it has DC level equivalent to 0Hz. In spite of acceptable range as defined, the PWM Frequency should be fixed and stable for more consistent brightness control at any specific level desired.
- 10. The life time is determined as the time at which brightness of LCD is 50% compare to that of minimum value specified in table 7. under general user condition.

Ver. 1.1 May. 15, 2011 7 / 27



3-2. Interface Connections

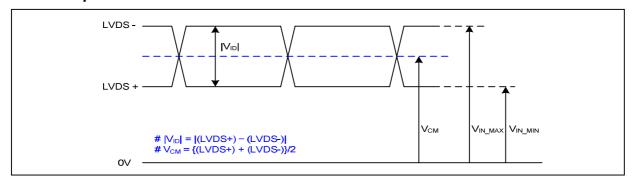
This LCD employs two interface connections, a 40 pin connector used for the module electronics interface and the other connector used for the integral backlight system.

Pin	Symbol	Description	Notes
1	NC	No Connection	[Interface Chip]
2	VCC	LCD Logic and driver power (3.3V Typ.)	1. LCD :
3	VCC	LCD Logic and driver power (3.3V Typ.)	SiW, SW0624(LCD Controller)
4	V EEDID	DDC Power (3.3V)	Including LVDS Receiver.
5	NC	No Connection	2. System :
6	Clk EEDID	DDC Clock	* Pin to Pin compatible with LVDS
7	DATA EEDID	DDC Data	
8	ORX0-	Negative LVDS differential data input	[Connector]
9	ORX0+	Positive LVDS differential data input	UJU IS050-L40B-C10
10	GND	High Speed Ground	LSMtron GT05Q-40S-H10 or equivalent
11	ORX1-	Negative LVDS differential data input	[Mating Connector]
12	ORX1+	Positive LVDS differential data input	[Mating Connector] 20345-#40E-## series or equivalent
13	GND	High Speed Ground	200-10 11-1011-11111 School of equivalent
14	ORX2-	Negative LVDS differential data input	[Connector pin arrangement]
15	ORX2+	Positive LVDS differential data input	3 3 3
16	GND	High Speed Ground	40 1
17	ORXC-	Negative LVDS differential clock input	
18	ORXC+	Positive LVDS differential clock input	
19	GND	High Speed Ground	
20	NC	No Connection	[LCD Module Rear View]
21	NC	No Connection	
22	GND	High Speed Ground	
23	NC	No Connection	
24	NC	No Connection	
25	GND	High Speed Ground	
26	NC	No Connection	
27	NC	No Connection	
28	GND	High Speed Ground	
29	NC	No Connection	
30	NC	No Connection	
31	GND	LED Backlight Ground	
32	GND	LED Backlight Ground	
33	GND	LED Backlight Ground	
34	NC	No Connection	
35	PWM	System PWM Signal input for dimming	
36	LED_EN	LED Backlight On/Off	
37	NC	No Connection	
38	VLED	LED Backlight Power (5V-21V)	
39	VLED	LED Backlight Power (5V-21V)	
40	VLED	LED Backlight Power (5V-21V)	



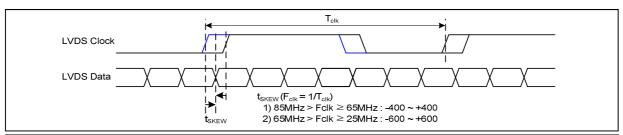
3-3. LVDS Signal Timing Specifications

3-3-1. DC Specification



Description	Symb ol	Min	Max	Unit	Notes
LVDS Differential Voltage	V _{ID}	100	600	mV	-
LVDS Common mode Voltage	V_{CM}	0.6	1.8	V	-
LVDS Input Voltage Range	V _{IN}	0.3	2.1	V	-

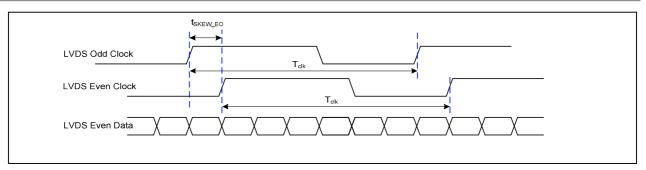
3-3-2. AC Specification



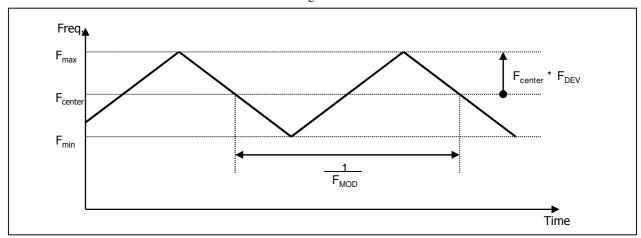
Description	Symbol	Min	Max	Unit	Notes
LVDS Clock to Data Skew Margin	t _{skew}	- 400	+ 400	ps	85MHz > Fclk ≥ 65MHz
LVDS Clock to Data Skew Margin	t _{skew}	- 600	+ 600	ps	65MHz > Fclk ≥ 25MHz
LVDS Clock to Clock Skew Margin (Even to Odd)	t _{skew_eo}	- 1/7	+ 1/7	T _{clk}	-
Maximum deviation of input clock frequency during SSC	F _{DEV}	-	± 3	%	-
Maximum modulation frequency of input clock during SSC	F _{MOD}	-	200	KHz	-

Ver. 1.1 May. 15, 2011 9 / 27





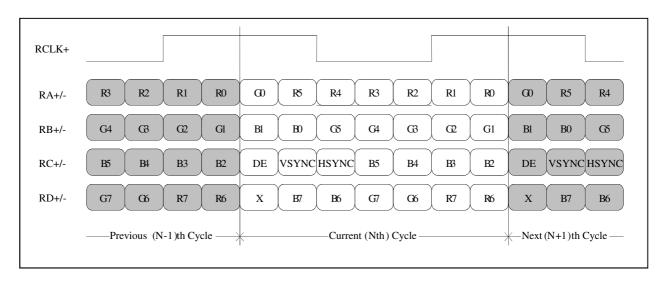
< Clock skew margin between channel >



< Spread Spectrum >

3-3-3. Data Format

- LVDS 1 Port



< LVDS Data Format >

Ver. 1.1	May. 15, 2011	10 / 27
V C1. 1.1	Way. 15, 2011	

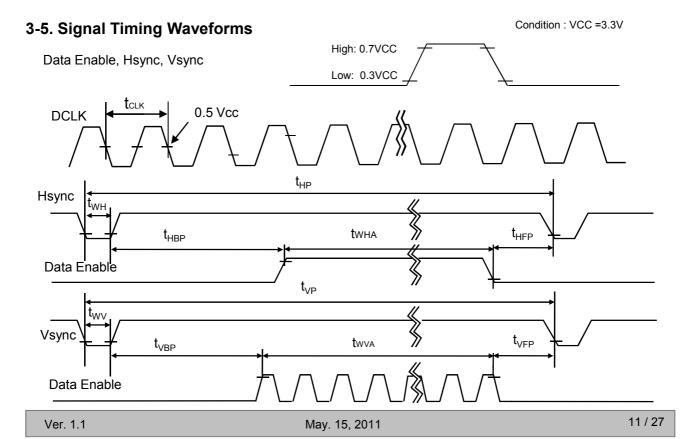


3-4. Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for its proper operation.

Table 5. TIMING TABLE

ITEM	Symbol		Min	Тур	Max	Unit	Note
DCLK	Frequency	f_{CLK}	67.5	69.3	77	MHz	
	Period	T_{hp}	1366	1440	1488		
Hsync	Width	t _{wH}	16	32	48	tCLK	
	Width-Active	t _{WHA}	1280	1280	1280		
	Period	t _{VP}	811	823	847		
Vsync	Width	t _{wv}	3	6	9	tHP	
	Width-Active	t _{wva}	800	800	800		
	Horizontal back porch	t _{HBP}	54	80	98	+Cl I/	
Data	Horizontal front porch	t_{HFP}	16	48	62	tCLK	
Enable	Vertical back porch	t_{VBP}	7	15	35	+UD	
	Vertical front porch	t_{VFP}	1	2	3	tHP	





3-6. Color Input Data Reference

The brightness of each primary color (red,green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

								Inp	out Co	olor D	ata							
Color			RE	ΞD					GRE	EEN					BL	UE		
COIOI		3				LSB	MSE	3				LSB	MSE	3				LSB
	R 5	R 4	R 3	R 2	R 1	R 0	G 5	G 4	G 3	G 2	G 1	G 0	B 5	B 4	В3	B 2	B 1	B 0
Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0		0	0	0
Red	1	.1 	.1	1	1	1	0	0		0	0	0	0	0		0	0	0
Green	0	0	0	0	0	0	1	1	. 1				0	0	0	0	0	0
Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	. 1	1	
Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
				· · · · ·														
RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
				· · · · ·						 						 		
GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
GREEN (63)	0	0	0	0	0	0	1	 1	1	1	1	1	0	0	0	0	0	0
1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	0	0	0	0		0	 0	0	0	0	0	0	1	1	1	1	 1	0
	0	0			0	0		0			 0	0	1	 1	1	 1	 1	1
	Red Green Blue Cyan Magenta Yellow White RED (00) RED (01) RED (63) GREEN (00) GREEN (01) GREEN (62) GREEN (63) BLUE (00) BLUE (01)	MSE R 5	MSB R 5 R 4 R 5 R 4 R 5 R 4 R 5 R 4 R 5 R 4 R 5 R 4 R 5 R 4 R 5 R 4 R 5 R 4 R 5 R 5 R 4 R 5 R 5 R 4 R 5	MSB R5 R4 R3 R6 R6 R6 R6 R6 R6 R6	MSB R5 R4 R3 R2	MSB R5 R4 R3 R2 R1 R2 R1 R2 R4 R3 R2 R1 RED R2 R3 R2 R1 R2 R3 R2 R1 R2 R3 R2 R1 R2 R3 R3 R3 R3 R3 R3 R3	MSB	MSB	Color NS R R R R R R R R R	Color NSB RED LSB MSB MSB RED RED	CRED RED RED RSD RSD GREEN GREEN Black 0	MSB	NSB RED LSB MSB GREEN LSB MSB RED LSB MSB LSB MSB RED LSB MSB RED LSB MSB RED LSB MSB RED RE	Name	Name	Color	No. No.	Color MSB

Ver. 1.1 May. 15, 2011 12 / 27



3-7. Power Sequence

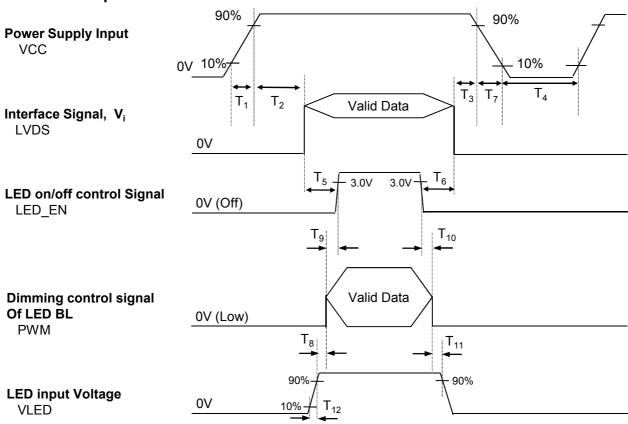


Table 6. POWER SEQUENCE TABLE

Logic	Logic Value Un	Units	LED		Value		Units		
Parameter	Min.	Тур.	Max.	Offics	Parameter	Min.	Тур.	Max.	Ullits
T ₁	0.5	-	10	ms	T ₈	10	ı	-	ms
T ₂	0	1	50	ms	T ₉	0	ı	-	ms
T ₃	0	1	50	ms	T ₁₀	0	ı	-	ms
T ₄	400	1	-	ms	T ₁₁	10	ı	-	ms
T ₅	200	1	-	ms	T ₁₂	0.5	ı	-	ms
T ₆	200	-	-	ms					
T ₇	3	-	10	ms					

Note)

- 1. Do not insert the mating cable when system turn on.
- 2. Valid Data have to meet "3-3. LVDS Signal Timing Specifications"
- 3. LVDS, LED_EN and PWM need to be on pull-down condition on invalid status.
- 4. LGD recommend the rising sequence of VLED after the Vcc and valid status of LVDS turn on.

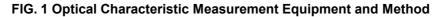
Ver. 1.1 May. 15, 2011 13 / 27



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to 0° .

FIG. 1 presents additional information concerning the measurement equipment and method.



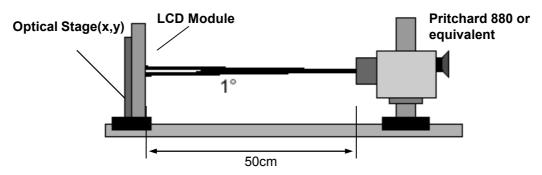


Table 8. OPTICAL CHARACTERISTICS

Ta=25°C, VCC=3.3V, f_V =60Hz, f_{CLK} = 69.3 MHz

Parameter	Symbol		Values		Units	Notes
Parameter	Symbol	Min	Тур	Max	Units	Notes
Contrast Ratio	CR	500	-	-		1
Surface Luminance, white	L _{WH}	300	350	-	cd/m ²	2
Luminance Variation	δ_{WHITE}	-	1.4	1.6		3
Response Time	Tr _R + Tr _D	-	35	50	ms	4
	GTG(9x9)	-	24	45	ms	
Color Coordinates						
RED	RX	0.558	0.588	0.618		
	RY	0.323	0.353	0.383		
GREEN	GX	0.303	0.333	0.363		
	GY	0.551	0.581	0.611		
BLUE	ВХ	0.128	0.158	0.188		
	BY	0.100	0.130	0.160		
WHITE	WX	0.283	0.313	0.343		
	WY	0.299	0.329	0.359		
Viewing Angle						5
x axis, right(Φ=0°)	Θr	80	-	-	degree	
х axis, left (Ф=180°)	Θl	80	-	-	degree	
y axis, up (Φ=90°)	Θu	80	-	-	degree	
y axis, down (Φ=270°)	Θd	80	-	-	degree	
Gray Scale			2.2			6

Ver. 1.1 May. 15, 2011 14 / 27



Note)

1. Contrast Ratio(CR) is defined mathematically as

Contrast Ratio =

Surface Luminance with all black pixels

2. Surface luminance is the average of 5 point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 1.

$$L_{WH}$$
 = Average($L_1, L_2, \dots L_5$)

3. The variation in surface luminance , The panel total variation (δ_{WHITE}) is determined by measuring L_N at each test position 1 through 13 and then defined as followed numerical formula. For more information see FIG 2.

$$\delta_{\text{WHITE}} = \frac{\text{Maximum}(L_1, L_2, \dots L_{13})}{\text{Minimum}(L_1, L_2, \dots L_{13})}$$

- 4. Response time is the time required for the display to transition from white to black (rise time, Tr_R) and from black to white(Decay Time, Tr_D). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
- 6. Gray scale specification

*
$$f_V = 60Hz$$

Gray Level	Luminance [%] (Typ)
LO	0.11
L7	0.82
L15	4.04
L23	10.00
L31	18.60
L39	32.10
L47	49.30
L55	73.30
L63	100.00

Ver. 1.1 May. 15, 2011 15 / 27



FIG. 2 Luminance

<measuring point for surface luminance & measuring point for luminance variation>

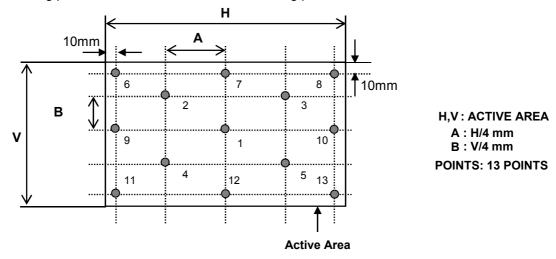


FIG. 3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

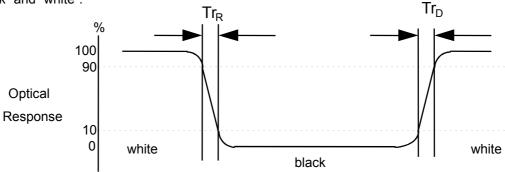
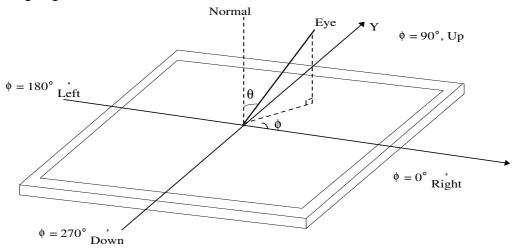


FIG. 4 Viewing angle



Ver. 1.1 May. 15, 2011 16 / 27



5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model LP101WX1. In addition the figures in the next page are detailed mechanical drawing of the LCD.

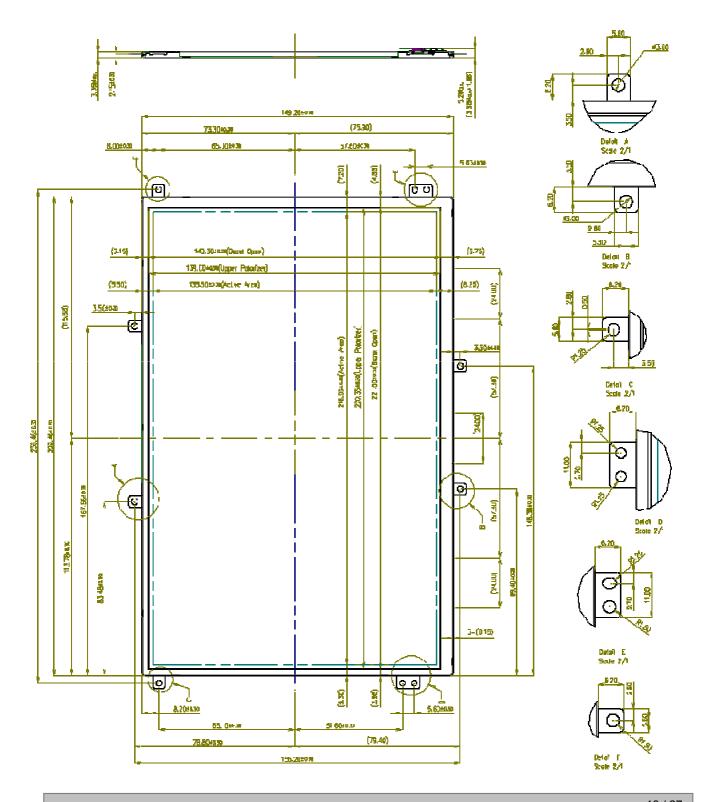
	Horizontal	229.46 ± 0.5mm			
Outline Dimension	Vertical	149.2 ± 0.5mm			
	Thickness	3.35mm (max), 5.2Max(w/ PCB)			
Bezel Area	Horizontal	221.60± 0.5mm			
bezei Alea	Vertical	140.30± 0.5mm			
Active Display Area	Horizontal	216.96 mm			
Active Display Area	Vertical	135.60 mm			
Weight	160g (Max)				
Surface Treatment	Glare treatment of the front polarizer				

Ver. 1.1 May. 15, 2011 17 / 27



<FRONT VIEW>

Note) Unit:[mm], General tolerance: \pm 0.5mm

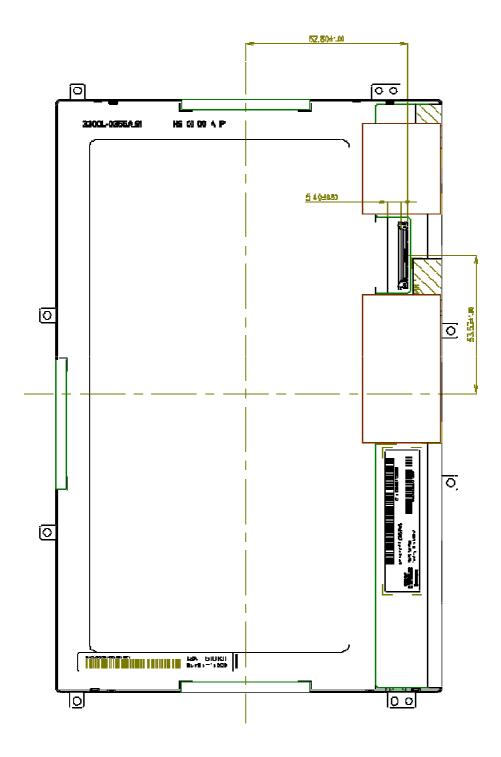


Ver. 1.1 May. 15, 2011 18 / 27



<REAR VIEW>

Note) Unit:[mm], General tolerance: ± 0.5mm



Ver. 1.1 May. 15, 2011 19 / 27



6. Reliability

Environment test condition

No.	Test Item	Conditions					
1	High temperature storage test	Ta= 60°C, 240h					
2	Low temperature storage test	Ta= -20°C, 240h					
3	High temperature operation test	Ta= 50°C, 50%RH, 240h					
4	Low temperature operation test	Ta= 0°C, 240h					
5	Vibration test (non-operating)	Random, 1.0Grms, X,Y,Z Direction Test time : each direction 1hour					
6	Shock test (non-operating)	Half sine wave, 180G, 2ms one shock of each six faces(I.e. run 180G 6ms for all six faces)					
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr					

{ Result Evaluation Criteria }
There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

20 / 27 Ver. 1.1 May. 15, 2011



7. International Standards

7-1. Safety

- a) UL 60950-1, Second Edition, Underwriters Laboratories Inc.
 Information Technology Equipment Safety Part 1: General Requirements.
- b) CAN/CSA C22.2 No.60950-1-07, Second Edition, Canadian Standards Association. Information Technology Equipment Safety Part 1: General Requirements.
- c) EN 60950-1:2006 + A11:2009, European Committee for Electrotechnical Standardization (CENELEC). Information Technology Equipment Safety Part 1 : General Requirements.
- d) IEC 60950-1:2005, Second Edition, The International Electrotechnical Commission (IEC). Information Technology Equipment Safety Part 1 : General Requirements.

7-2. EMC

- a) ANSI C63.4 "American National Standard for Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the Range of 9 kHz to 40 GHz." American National Standards Institute (ANSI), 2003.
- b) CISPR 22 "Information technology equipment Radio disturbance characteristics Limit and methods of measurement." International Special Committee on Radio Interference (CISPR), 2005.
- c) CISPR 13 "Sound and television broadcast receivers and associated equipment Radio disturbance characteristics Limits and method of measurement." International Special Committee on Radio Interference (CISPR), 2006.

7-3. Environment

a) RoHS, Directive 2002/95/EC of the European Parliament and of the council of 27 January 2003

Ver. 1.1 May. 15, 2011 21 / 27



8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

A,B,C : SIZE(INCH) D : YEAR

E: MONTH $F \sim M$: SERIAL NO.

Note

1. YEAR

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Mark	Α	В	С	D	Е	F	G	Н	J	K

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box: 30pcs

b) Box Size: 478x365x244



9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm 200 \text{mV}(\text{Over and under shoot voltage})$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)

 And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.

Ver. 1.1 May. 15, 2011 23 / 27



9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.

 It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
 - Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

Ver. 1.1 May. 15, 2011 24 / 27



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 1/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
	0	00	Header	00	00000000
	1	01	Header	FF	111111111
	2	02	Header	FF	111111111
Header	3	03	Header	FF	111111111
lea l	4	04	Header	FF	111111111
h	5	05	Header	FF	111111111
	6	06	Header	FF	111111111
	7	07	Header	00	00000000
9	8	08	EISA manufacture code (3 Character ID) LGD	30	00110000
EDID	9	09	EISA manufacture code (Compressed ASC [])	E4	11100100
E	10	0A	Panel Supplier Reserved - Product Code 0324h	24	00100100
	11	0B	(Hex. LSB first)	03	00000011
2	12	0C	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	00000000
roduct Version	13	0D	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	00000000
odi ers	14	0E	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	00000000
Pro V	15	0F	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	00000000
1	16	10	Week of Manufacture 00 weeks	00	00000000
qo	17	11	Year of Manufacture 2011 years	15	00010101
Vendor / Product Version	18	12	EDID structure version # = 1	01	00000001
A	19	13	EDID revision # = 3	03	00000011
	20	14	Video input Definition = Digital signal	80	10000000
S.	21	15	Max H image size (Rounded cm) = 22 cm	16	00010110
1y zter	22	16	Max V image size (Rounded cm) = 14 cm	0E	00001110
ph.	23	17	Display gamma = (gamma*100)-100 = Example:(2.2*100)-100=120 = 2.2 Gamma	78	01111000
Display Parameters	24	18	Feature Support (no_DPMS, no_Active Off/Very Low Power, RGB color display, Timing BLK 1,no_GTF)	0A	00001010
es	25	19	Red/Green Low Bits (RxRy/GxGy)	97	10010111
Panel Color Coordinates	26	1A	Blue/White Low Bits (BxBy/WxWy)	95	10010101
din	27	1B	Red X Rx = 0.588	96	10010110
on	28	1C	Red Y Ry =0.353	5A	01011010
S	29	1D	Green X $Gx = 0.333$	55	01010101
or	30	1E	Green Y Gy =0.581	94	10010100
lo	31	1F	Blue X Bx = 0.158	28	00101000
) <i>i</i>	32	20	Blue Y By = 0.13	21	00100001
ŭ	33	21	White X Wx=0.313	50	01010000
Pe	34	22	White Y Wy =0.329	54	01010100
19 19	35	23	Established timing 1 (00h if not used)	00	00000000
Establ ished Timin	36	24	Established timing 2 (00h if not used)	00	00000000
Es is Ti	37	25	Manufacturer's timings (00h if not used)	00	00000000
	38	26	Standard timing ID1 (01h if not used)	01	00000001
	39	27	Standard timing ID1 (01h if not used)	01	00000001
	40	28	Standard timing ID2 (01h if not used)	01	00000001
	41	29	Standard timing ID2 (01h if not used)	01	00000001
Q	42	2A	Standard timing ID3 (01h if not used)	01	00000001
Standard Timing ID	43	2B	Standard timing ID3 (01h if not used)	01	00000001
ing	44	2C	Standard timing ID4 (01h if not used)	01	00000001
im.	45	2D	Standard timing ID4 (01h if not used)	01	00000001
1 T	46	2E	Standard timing ID5 (01h if not used)	01	00000001
arc	47	2F	Standard timing ID5 (01h if not used)	01	00000001
nq	48	30	Standard timing ID6 (01h if not used)	01	00000001
ta	49	31	Standard timing ID6 (01h if not used)	01	00000001
√ 2	50	32	Standard timing ID7 (01h if not used)	01	0000001
	51	33	Standard timing ID7 (0th it not used) Standard timing ID7 (0th if not used)	01	0000001
			Standard timing ID8 (01h if not used)		0000001
	52	34		01	
	53	35	Standard timing ID8 (01h if not used)	01	00000001

Ver. 1.1 May. 15, 2011 25 / 27



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 2/3

Signature		Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
150 38 Incinental Article (Newer Shito) 120 Peech 50 00000000		54	36	Pixel Clock/10,000 (LSB) 69.3 MHz @ 59.9Hz	12	00010010
ST 39		55	37	Pixel Clock/10,000 (MSB)	1B	00011011
SS		56	38	Horizontal Active (lower 8 bits) 1280 Pixels	00	00000000
10 10 10 10 10 10 10 10		57	39	Horizontal Blanking(Thp-HA) (lower 8 bits) 128 Pixels	80	10000000
10 3.0 Vertical Blanking (Tvp-HA) (apper-4-bits) 22 Lines 16 00011010		58	3A	Horizontal Active / Horizontal Blanking(Thp-HA) (upper 4:4bits)	50	01010000
19		59	3B	Vertical Avtive 800 Lines	20	00100000
19	1#	60	3C	Vertical Blanking (Tvp-HA) (DE Blanking typ.for DE only panels) 22 Lines	16	00010110
19	0 t	61	3D	Vertical Active : Vertical Blanking (Tvp-HA) (upper 4:4bits)	30	00110000
19	ipt	62	3E	Horizontal Sync. Offset (Thfp) 48 Pixels	30	00110000
19	SCL	63	3F	Horizontal Sync Pulse Width (HSPW) 32 Pixels	20	00100000
19	Des	64	40	Vertical Sync Offset(Tvfp): Sync Width (VSPW) 4 Lines: 7 Lines	47	01000111
19	00	65	41		00	00000000
19	in	66				11011001
19	Ţ.				_	10001000
69	1					
1						00000000
1					,	
To To To To To To To To				Non-Interlace, Normal display, no stereo, Digital Separate (Vsync_NEG, Hsync_POS), DE only note: LSB is		
The composition The compos		72	48	Flag	00	00000000
The color of the		73	49	Flag	00	00000000
The color of the		74	4A	Flag	00	00000000
1966 77		75	4B	Data Type Tag (Descriptor Defined by manufacturer)	00	00000000
SS 55 Descriptor Defined by manufacturer 00 00000000		76	4C		00	00000000
SS 55 Descriptor Defined by manufacturer 00 00000000	23	77	4D		00	00000000
SS 55 Descriptor Defined by manufacturer 00 00000000	. #					00000000
SS 55 Descriptor Defined by manufacturer 00 00000000)to					00000000
SS 55 Descriptor Defined by manufacturer 00 00000000	ir.					00000000
SS 55 Descriptor Defined by manufacturer 00 00000000	88					
SS 55 Descriptor Defined by manufacturer 00 00000000						
SS 55 Descriptor Defined by manufacturer 00 00000000	ing					
SS 55 Descriptor Defined by manufacturer 00 00000000	i.i.					
Section Sect						
ST Descriptor Defined by manufacturer 00 000000000 SS Descriptor Defined by manufacturer 00 000000000 SS Descriptor Defined by manufacturer 00 00000000 90 5A Flag 00 00000000 91 5B Flag 00 00000000 92 5C Flag 00 00000000 93 5D Data Type Tag (ASCII String) FE 11111110 94 5E Flag 00 00000000 95 5F ASCII String L 4C 01001100 96 60 ASCII String G 47 01000111 97 61 ASCII String G 47 01000111 98 62 ASCII String D 44 01000100 99 63 ASCII String i 69 0110011 100 64 ASCII String s 73 01110011 101 65 ASCII String D 70 01110000 102 66 ASCII String a 61 0110001 103 67 ASCII String a 61 0110001 104 68 ASCII String a 61 0110001 105 69 Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h) 00100000 106 6A Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h) 0A 00010100 106 6A Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h) 0A 00010100 106 0A 00001010 0A 0001010000000000						
Section Sect					-	
Second S				-		
Page						
SB Flag 00 00000000						
Page					,	
SECOND S						
SE Flag						
Proceedings Proceedings Procedings P				Data Type Tag (ASCII String)		
103 67 ASCII String a 61 01100001 104 68 ASCII String y 79 01111001 105 69 Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h) 0A 00001010 106 6A Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h) 20 00100000			5E			
103 67 ASCII String a 61 01100001 104 68 ASCII String y 79 01111001 105 69 Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h) 0A 00001010 106 6A Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h) 20 00100000	#3		5F			
103 67 ASCII String a 61 01100001 104 68 ASCII String y 79 01111001 105 69 Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h) 0A 00001010 106 6A Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h) 20 00100000	or		60	ASCII String G		
103 67 ASCII String a 61 01100001 104 68 ASCII String y 79 01111001 105 69 Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h) 0A 00001010 106 6A Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h) 20 00100000	ipt	97	61	ASCII String	20	00100000
103 67 ASCII String a 61 01100001 104 68 ASCII String y 79 01111001 105 69 Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h) 0A 00001010 106 6A Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h) 20 00100000	scr	98	62	ASCII String D		01000100
103 67 ASCII String a 61 01100001 104 68 ASCII String y 79 01111001 105 69 Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h) 0A 00001010 106 6A Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h) 20 00100000	De	99	63	ASCII String i	69	01101001
103 67 ASCII String a 61 01100001 104 68 ASCII String y 79 01111001 105 69 Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h) 0A 00001010 106 6A Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h) 20 00100000	0.0	100	64	ASCII String s	73	01110011
103 67 ASCII String a 61 01100001 104 68 ASCII String y 79 01111001 105 69 Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h) 0A 00001010 106 6A Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h) 20 00100000	ıi,	101	65	ASCII String p	70	01110000
103 67 ASCII String a 61 01100001 104 68 ASCII String y 79 01111001 105 69 Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h) 0A 00001010 106 6A Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h) 20 00100000	Ţ.	102	66		6C	01101100
104 68 ASCII String y 79 01111001 105 69 Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah,set remaining char = 20h) 0A 00001010 106 6A Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah,set remaining char = 20h) 20 00100000	-	103	67	ASCII String a	61	01100001
105 69 Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC II code 0Ah,set remaining char = 20h) 106 6A Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC II code 0Ah,set remaining char = 20h) 20 00100000						01111001
106 6A Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h) 20 00100000						00001010
		107	6B	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC code 0Ah, set remaining char = 20h)	20	00100000

Ver. 1.1 May. 15, 2011 26 / 27



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 3/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
	108	6C	Flag	00	00000000
	109	6D	Flag	00	00000000
	110	6E	Flag	00	00000000
	111	6F	Data Type Tag (ASCII String)	FE	11111110
	112	70	Flag	00	00000000
#	113	71	ASCII String L	4C	01001100
Jr. i	114	72	ASCII String P	50	01010000
ipte	115	73	ASCII String 1	31	00110001
5	116	74	ASCII String 0	30	00110000
Des	117	75	ASCII String 1	31	00110001
8 1	118	76	ASCII String W	57	01010111
Timing Descriptor #4	119	77	ASCII String X	58	01011000
Γin .	120	78	ASCII String 1	31	00110001
	121	79	ASCII String -	2D	00101101
	122	7A	ASCII String S	53	01010011
	123	7B	ASCII String L	4C	01001100
	124	7C	ASCII String N	4E	01001110
	125	7D	ASCII String 2	32	00110010
Сћес	126	7E	Extension flag (# of optional 128 panel ID extension block to follow, Typ = 0)	00	00000000
CI	127	7 F	Check Sum (The 1-byte sum of all 128 bytes in this panel ID block shall = 0)	EB	11101011

Ver. 1.1 May. 15, 2011 27 / 27