

Problem 1: (14 pts)

a) State whether the following statements are TRUE or FALSE. **(5pts)**

(0.5 pts each; if all correct, 5 more points as bonus)

- [.....] KVL is based on conservation of energy.
- [.....] KCL is based on conservation of charge.
- [.....] Summation of voltage drops across *any* closed loop is zero.
- [.....] Subtraction of voltage drops across *any* closed loop is zero.

- [.....] Non-linear resistors are memoryless components.
- [.....] If a circuit can be algebraically analyzed with node analysis, but not with mesh analysis; the circuit must contain at least one component that is not current controlled.
- [.....] Superposition principle is valid for all types circuits including memoryless, dynamic, linear, non-linear circuits.

- [.....] Tellegen's Theorem is only valid for resistive circuits.
- [.....] Finding (i,v) characteristic of a linear memoryless circuit is another method of finding its Thevenin equivalent.
- [.....] The branch current and branch voltage of a short circuited branch can be both zero.

b) Answer the following short questions: (9 pts)

- i) Determine the range of k for the component given in Figure i to be active. (3pts)

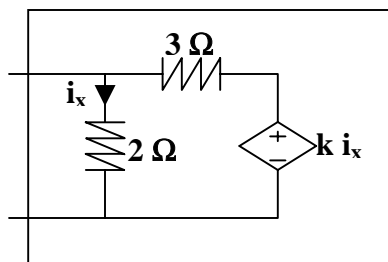


Figure i

Answer:

- ii) The component shown in Figure ii-a is a current controlled circuit element that can be non-bilateral. Show that the component in Figure ii-b is always bilateral. (3 pts)

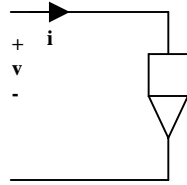


Figure ii-a

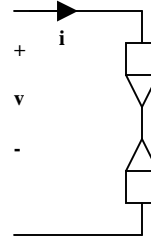
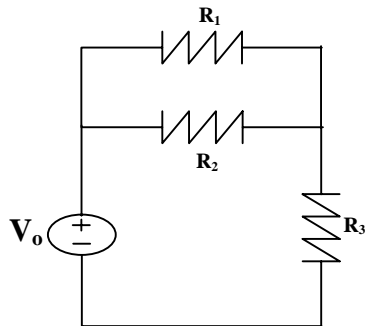


Figure ii-b

Answer :

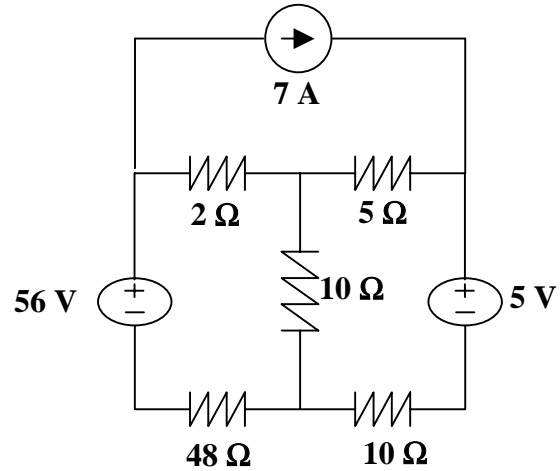
- iii) Find the dual of the following circuit. (No need to draw circuit graph or indicate current/voltage orientations) (3 pts)

Answer :



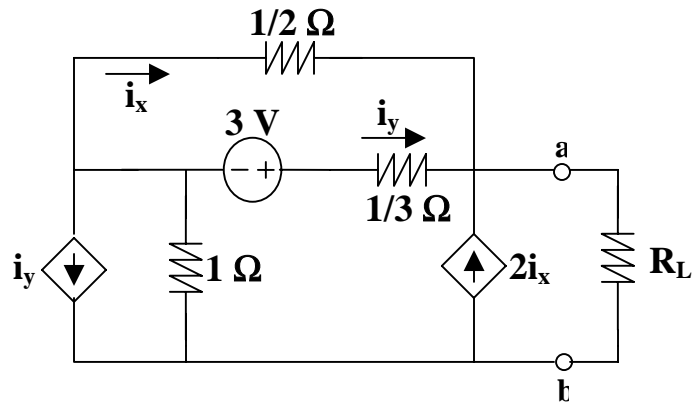
Problem 2: (14 pts)

Find the power supplied by 7 A current source using *source transformation* method.



Problem 3: (16 pts)

Determine the value of R_L for maximum power transfer to a-b terminals.



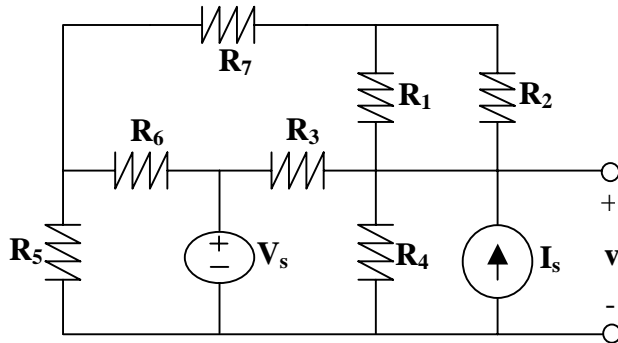
Problem 4: (21 pts)

- Is the following statement TRUE or FALSE ? (1 pt)

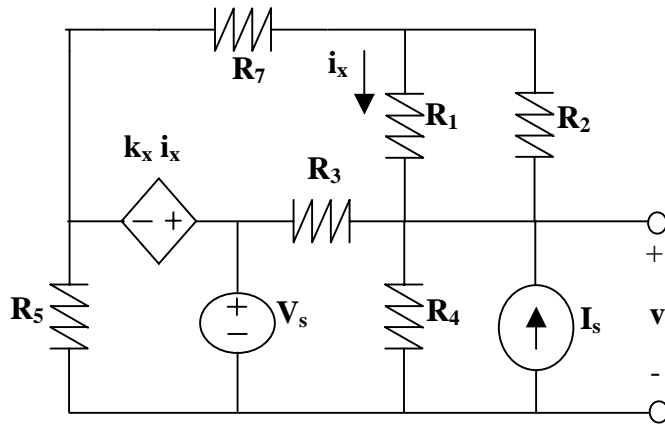
[.....] A circuit with “n” nodes and “k” voltage sources can be completely analyzed by (n-k) node equations with (n-k) node voltages as unknowns.

- Write the node equations for the solution of the circuits given below. Do not introduce more variables than asked.

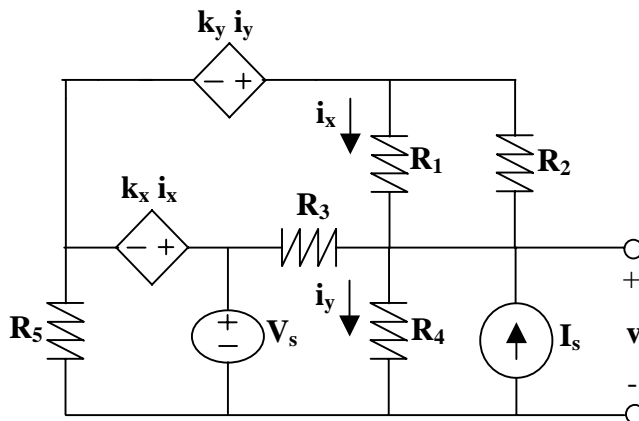
i) Write 3 equations with 3 unknowns to find v. Do not solve or simplify the equations. (5pts)



ii) Write 2 equations with 2 unknowns to find v . Do not solve for v . (7 pts)



iii) Write only one node equation to find v . Solve for v . (8 pts)



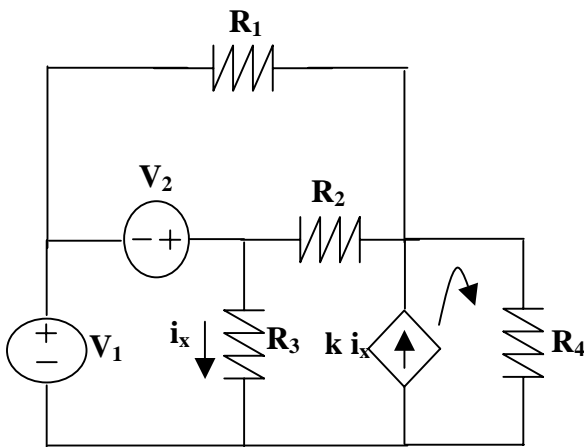
Problem 5: (21 pts)

- Is the following statement TRUE or FALSE ?

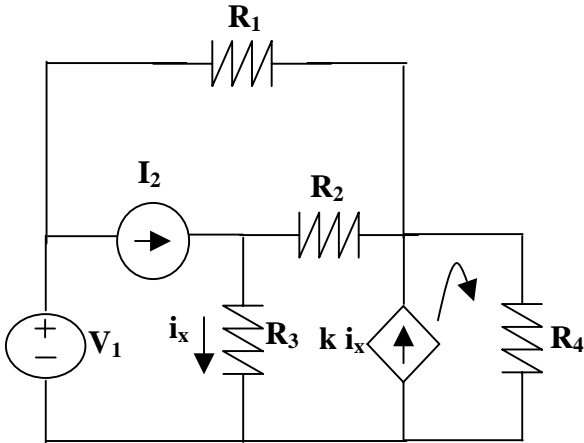
[.....] A circuit with “m” meshes and “k” current sources can be completely analyzed by (m-k) mesh equations with (m-k) unknowns.

- Write down the mesh equations for the circuits given below. One of the mesh currents in each circuit is given. Determine the other current variables and express the solution of circuit in terms of mesh equations.

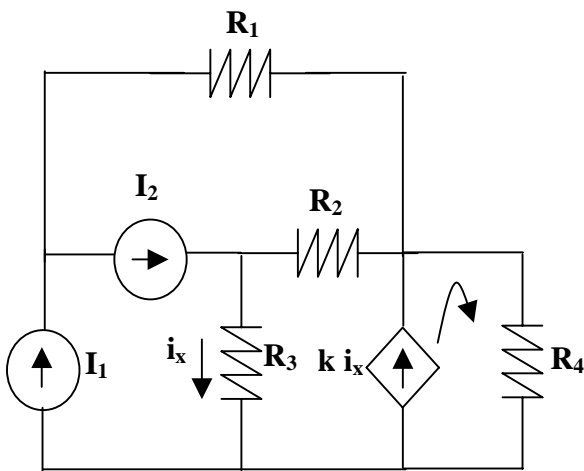
i) Write 3 mesh equations to find current i_x . Do not solve or simplify the equations. (5 pts)



ii) Write 2 mesh equations with 2 unknowns to find i_x . (7 pts)

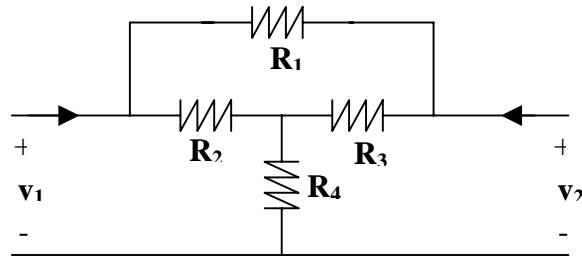


iii) Write only one equation to find i_x . Solve for i_x . (8 pts)

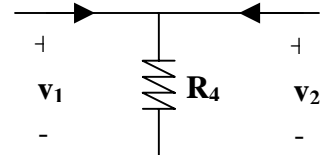
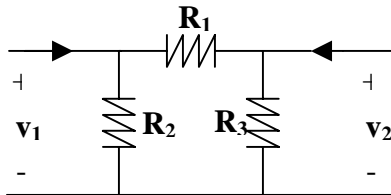


Problem 6: (14 pts)

i) Find the resistance parameters of the following two port: (6 pts)



ii) Show that the two port in part i) is the series combination of the following two ports: (4 pts)



iii) Find the resistance parameters of the two ports in part ii). Verify that resistance parameters found in part i) is the addition of parameters found in part ii). (4 pts)