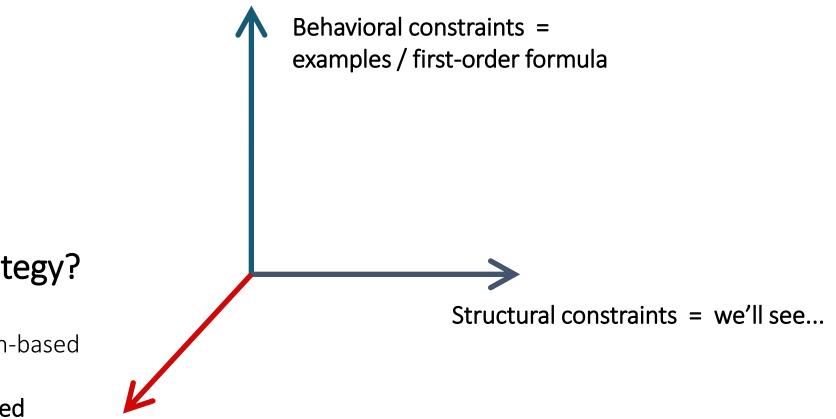
Lecture 8 Constraint-based search

Nadia Polikarpova

The problem statement



Search strategy?

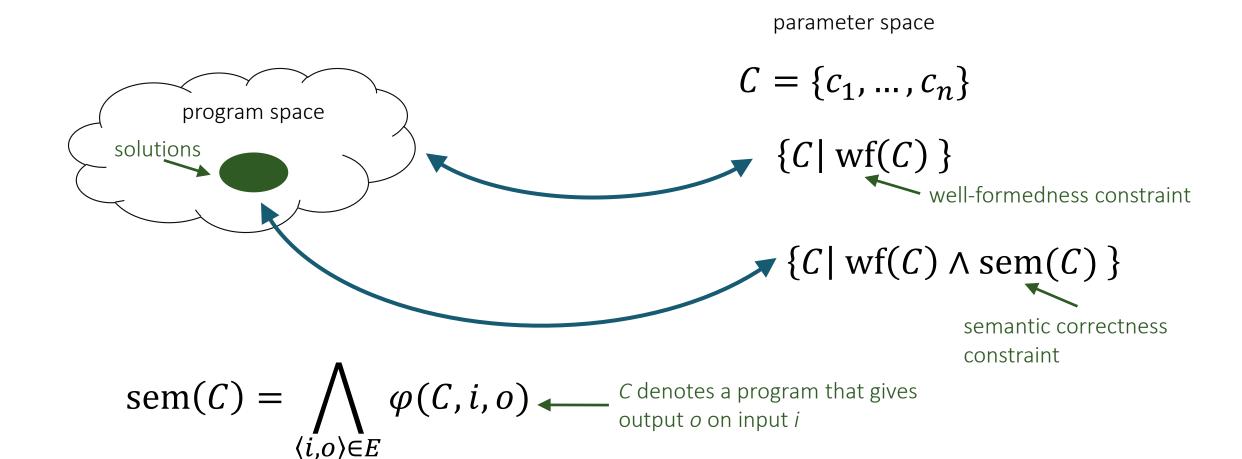
Enumerative Representation-based Stochastic

Constraint-based

Constraint-based search

Idea: encode the synthesis problem as a SAT/SMT problem and let a solver deal with it

What is an encoding?



How to define an encoding

```
Define the parameter space C = \{c_1, \dots, c_n\}
```

• decode : C → Prog (might not be defined for all C)

Define a formula $wf(c_1, ..., c_n)$

that holds iff decode[C] is defined

Define a formula $\varphi(c_1, ..., c_n, i, o)$

• that holds iff (decode[C])(i) = o

Constraint-based search

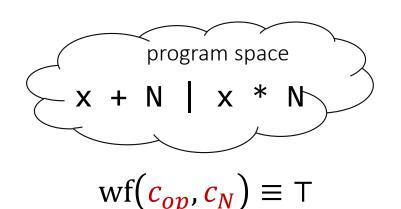
```
constraint-based (wf, \varphi, E = [i \rightarrow o]) {
    match SAT(wf(\mathcal{C}) \land \land_{\langle i,o \rangle \in E} \varphi(\mathcal{C},i,o)) with \longleftarrow for c_1, \ldots, c_n
    Unsat -> return "No solution" (i and o are fixed)

Model C* -> return decode[C*]
```

SAT encoding: example

```
x is a two-bit word
                                                                         parameter space
      program space
                                     (x = x_h x_1)
                                                                      C = \{c : Bool\}
                                     E = [11 \rightarrow 01]
                                                                       decode[0] \rightarrow x
                                                                       decode[1] \rightarrow x \& 1
wf(c) \equiv T
\varphi(c, i_h, i_l, o_h, o_l) \equiv (\neg c \Rightarrow o_h = i_h \land o_l = i_l)
\wedge (c \Rightarrow o_h = 0 \wedge o_l = i_l)
SAT(\varphi(c, 1, 1, 0, 1))
                                                                                     SAT solver
SAT((\neg c \Rightarrow 0 = 1 \land 1 = 1) \land (c \Rightarrow 0 = 0 \land 1 = 1))
                                                                                                      Model \{c \rightarrow 1\}
                                     return decode[1] i.e. x & 1
```

SMT encoding: example



N is an in integer literal x is an integer input

$$E = [2 \rightarrow 9]$$

parameter space

$$C = \{c_{op} : Bool, c_N : Int\}$$

$$decode[0,N] \rightarrow x + N$$

$$decode[1,N] \rightarrow x * N$$

$$\varphi(c_{op}, c_N, i, o) \equiv (\neg c_{op} \Rightarrow o = i + c_N) \land (c_{op} \Rightarrow o = i * c_N)$$

SAT
$$(\phi(c_{op}, c_N, 2,9))$$
SAT $((\neg c_{op} \Rightarrow 9 = 2 + c_N) \land (c_{op} \Rightarrow 9 = 2 * c_N))$

SMT solver

return decode[0,7] i.e. x + 7

What is a good encoding?

Sound

• if $wf(C) \wedge sem(C)$ then decode[C] is a solution

Complete

• if decode[C] is a solution then $wf(C) \wedge sem(C)$

Small parameter space

avoid symmetries

Solver-friendly

• decidable logic, compact constraint

DSL limitations

Program space can be parameterized with a finite set of parameters

Program semantics $\varphi(\mathcal{C},i,o)$ is expressible as a (decidable) SAT/SMT formula

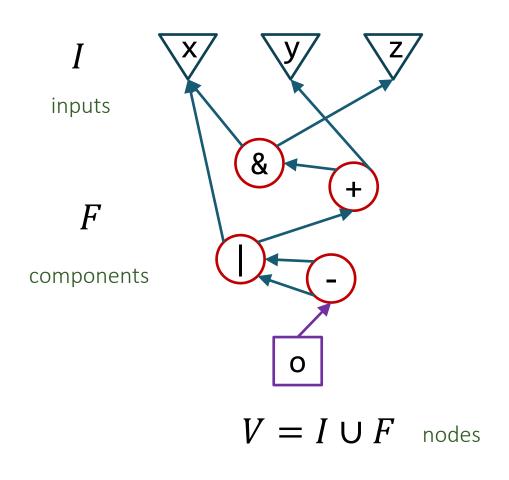
Counterexample

Brahma

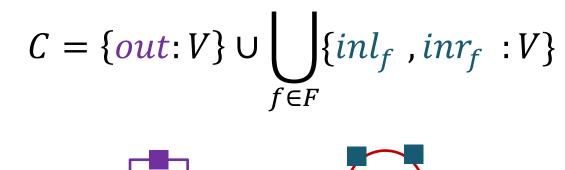
Idea: encode the space of loop-free (bit-vector) programs as an SMT constraint

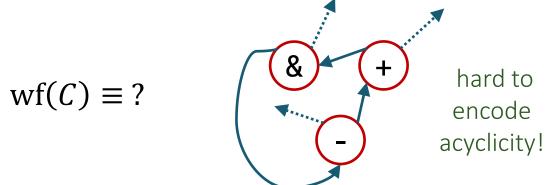
Brahma encoding: take 1

program = DAG

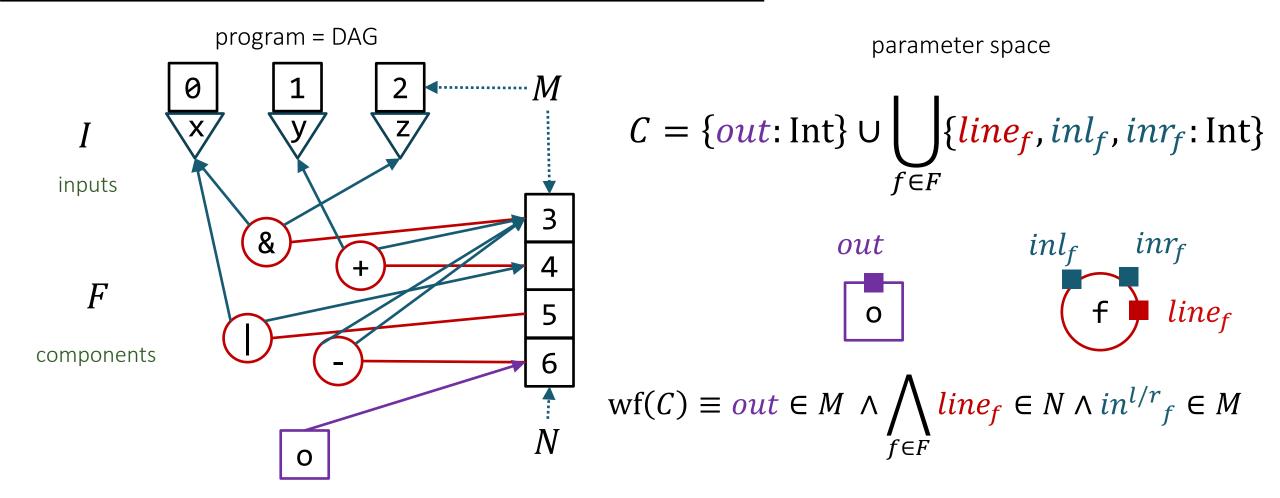


parameter space

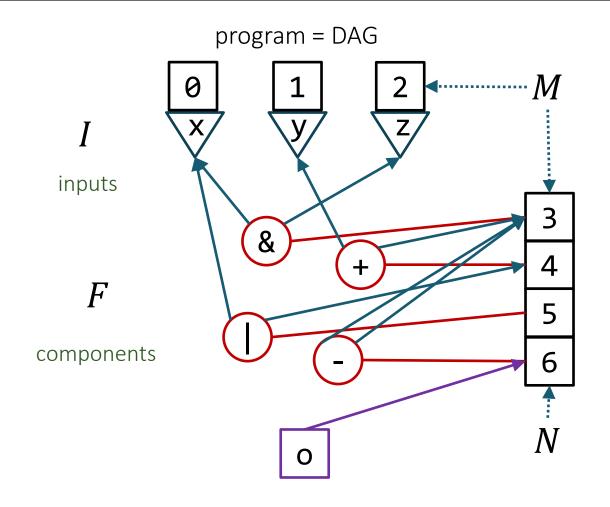




Brahma encoding: take 2



Brahma encoding: take 2



parameter space

$$C = \{out: Int\} \cup \bigcup_{f \in F} \{line_f, inl_f, inr_f: Int\}$$

$$T = \bigcup_{f \in F} \{L_f, R_f, O_f: BitVector\}$$

$$\varphi(C, I, O) \equiv \exists T. \bigwedge_{f \in F} O_f = F(L_f, R_f)$$

$$\land \bigwedge_{f, g \in F \cup I} line_f = in^{l/r}_g \Rightarrow O_f = L/R_f$$

 $\land line_f = out \Rightarrow O_f = O$

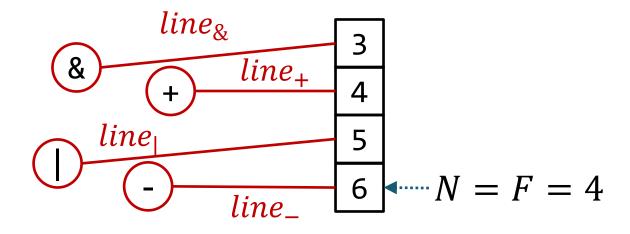
Brahma: questions

Behavioral Constraints? Structural Constraints? Search Strategy?

- First-order formula
- Expressions over a multiset of components
- Constraint based + CEGIS

Alternative encodings

Brahma encoding



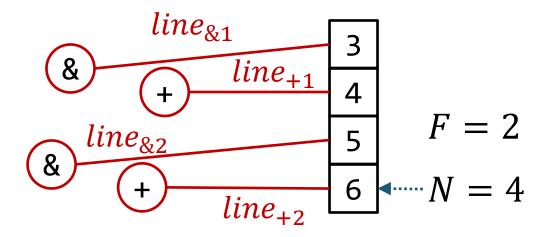
Linear encoding

$$t_3 = F_3(inl_3, inr_3)$$

 $t_4 = F_4(inl_4, inr_4)$
 $t_5 = F_4(inl_5, inr_5)$
 $t_6 = F_6(inl_6, inr_6)$

Alternative encodings

Brahma encoding



Linear encoding

$$t_3 = F_3(inl_3, inr_3)$$

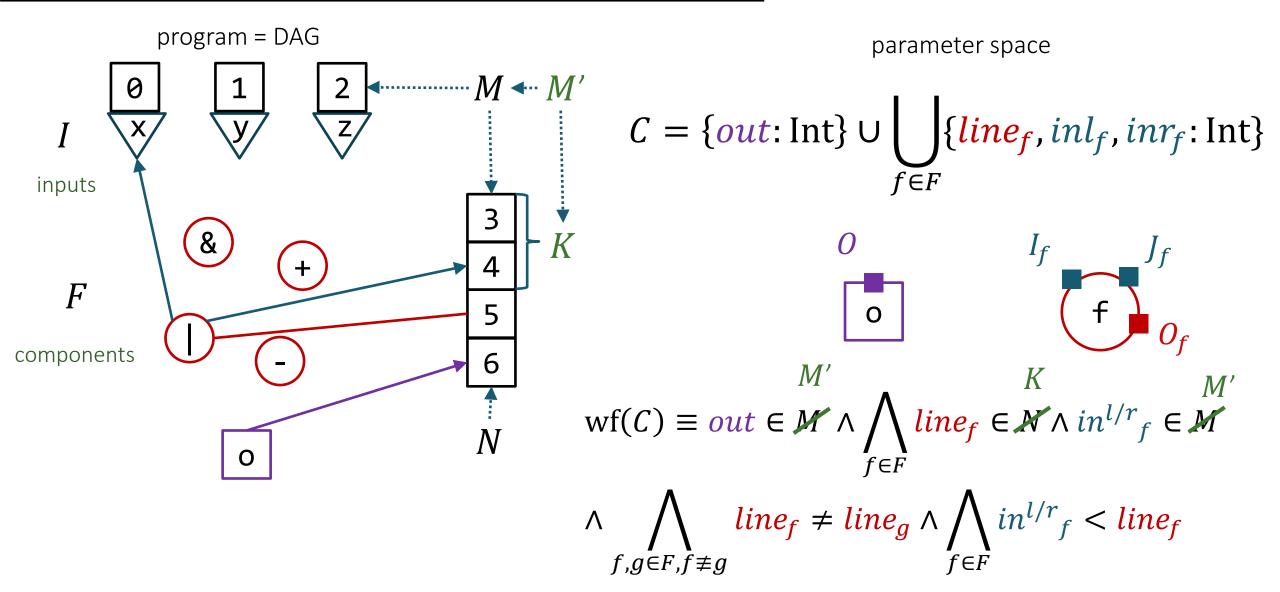
 $t_4 = F_4(inl_4, inr_4)$
 $t_5 = F_4(inl_5, inr_5)$
 $t_6 = F_6(inl_6, inr_6)$

N! assignmetns to *line* vars

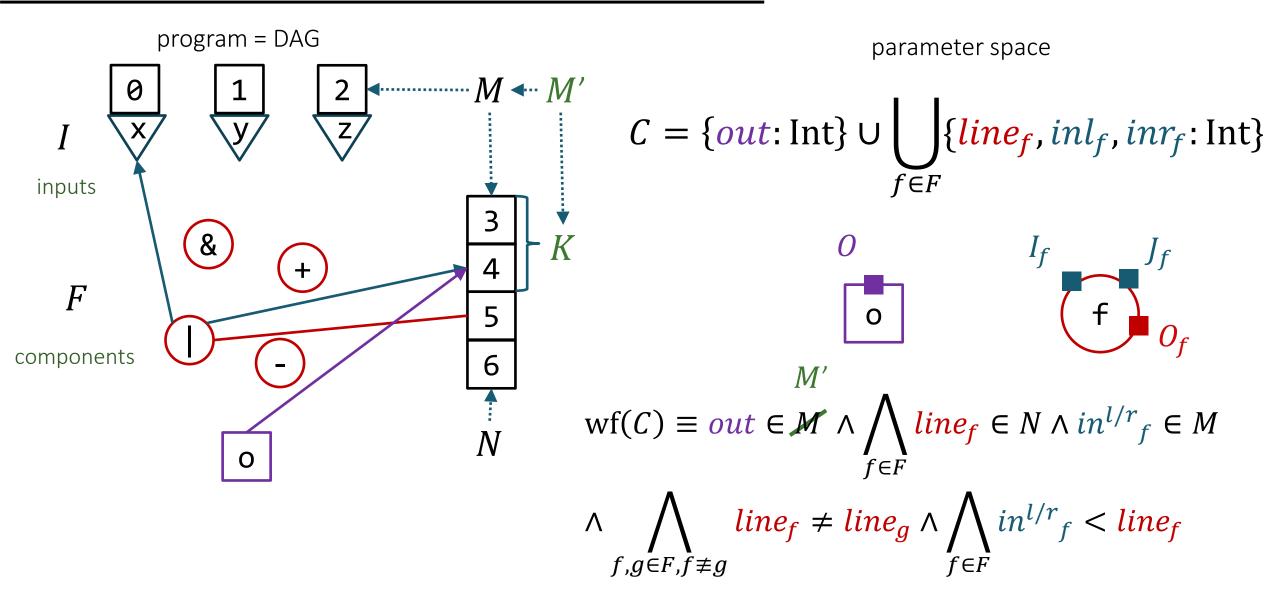
 F^N assignmetns to F vars

Brahma encoding is worse is component multiplicities are high! (N >> F)

Limit #components to K?



Limit #components to K?



Imprecise component specs

```
ExallSolver(\psi_{\text{wfp}}, \phi_{\text{lib}}, \psi_{\text{conn}}, \phi_{\text{spec}}):
 1 // \exists L \forall \vec{I}, O, T : \psi_{\text{wfp}} \land (\phi_{\text{lib}} \land \psi_{\text{conn}} \Rightarrow \phi_{\text{spec}})
                       is a synthesis constraint
 2 // Output: synthesis failed or values for L
 \mathcal{S}:=\{ec{I}_0\} // ec{I}_0 is an arbitrary input
 4 while (1) {
             model := T-SAT(\exists L, O_1, \ldots, O_n, T_1, \ldots, T_n : \psi_{\text{wfp}}(L) \land
                                              \bigwedge_{\vec{I}_i \in \mathcal{S}} (\phi_{\text{lib}}(T_i) \wedge \psi_{\text{conn}}(\vec{I}_i, O_i, T_i, L))
                                                        \wedge \phi_{\text{ener}}(\vec{I_i}, O_i)):
             if (model \neq \perp) {
                   \operatorname{curr} L := \operatorname{model}|_L
            } else {
                   return("synthesis failed");
             model := T-SAT(\exists \vec{I}, O, T : \psi_{conn}(\vec{I}, O, T, curr L) \land
                                                           \phi_{\text{lib}}(T) \wedge \neg \phi_{\text{spec}}(\vec{I}, O);
             if (model \neq \perp) {
             \vec{I}_1 := \mathtt{model}|_{\vec{I}}; \ \mathcal{S} := \mathcal{S} \cup \{\vec{I}_1\};
             } else {
                   return(currL);
10
11
```

Brahma: contributions

SMT encoding of program space

- sound? complete? solver-friendly?
- more compact than alternatives*

SMT solver can guess constants

• e.g. 0x5555555 in P23

Brahma: limitations

Requires component multiplicities

- If we didn't have multiplicities, where would their encoding break? How could we fix it?
- What happens if user provides too many? too few?
- What's the alternative to including dead code?

Requires precise SMT specs for components

What happens if we give an over-approximate spec?

No loops, no types, no ranking

Comparison of search strategies

