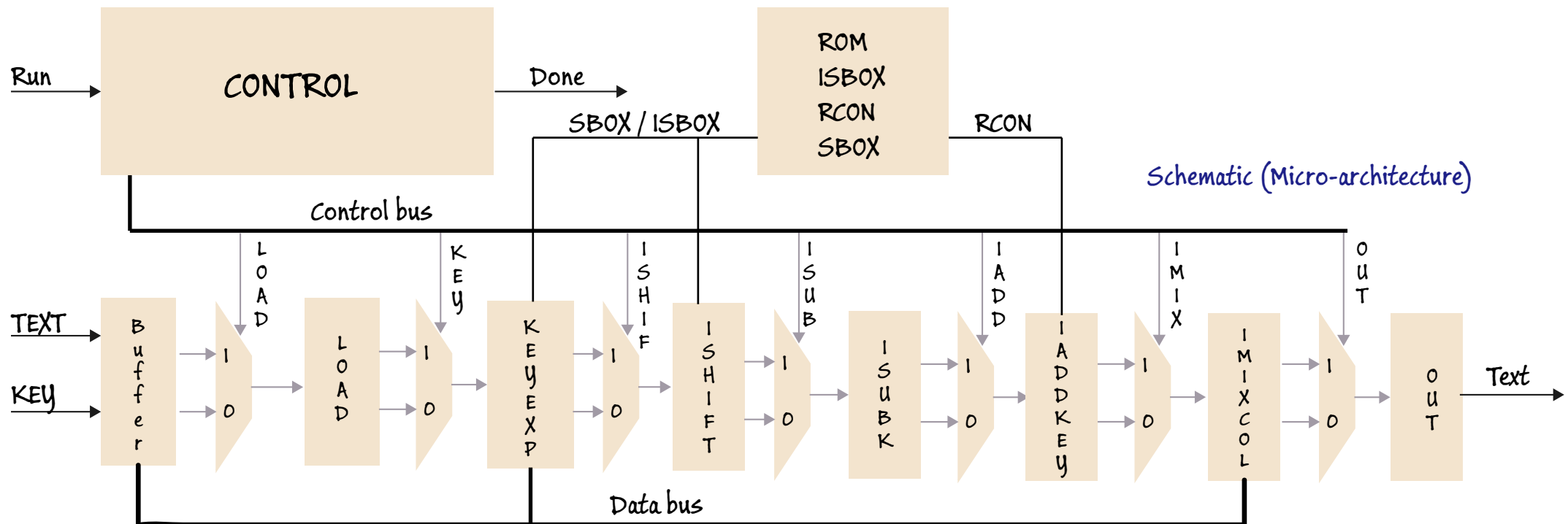


FSM (ISA architecture)

	I_shift_rows	I_sub_bytes	I_add_keys	I_mix_column
1	1	1	1	0
2	1	1	1	1
3	1	1	1	1
4	1	1	1	1
5	1	1	1	1
6	1	1	1	1
7	1	1	1	1
8	1	1	1	1
9	1	1	1	1
10	1	1	1	1
11	0	0	1	0

Fetch one instruction per round from instruction set which stored at ROM

Decryption 128 (decryption_128 architecture)



Schematic (Micro-architecture)