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         Lab 4: LED2, LED3, LED4, LED6, LED7, LED8
      //Top level module for the DE1_SoC Board
      //I/O Needed for the DE1_DoC as well as CLOCK_50
     module DE1_SoC (
output logic
output logic
10
                         [6:0] HEXO, HEX1, HEX2, HEX3, HEX4, HEX5, [9:0] LEDR, [3:0] KEY,
11
12
13
          input logic
14
                         [9:0] SW,
          input logic
15
                        CLOCK_50
          input logic
16
17
     );
18
          // Default values, turns off the HEX displays
          assign HEX1 = 7'b11111111;
19
          assign HEX2 = 7'b11111111;
20
21
          assign HEX3 = 7'b11111111;
22
23
24
          assign HEX5 = 7'b11111111;
25
          // Logic Variables
26
27
          logic L, R, L_Tug, R_Tug;
28
          // Flip Flops for Tug-of-War to handle Metastability
          metaStability L_dff (.CLOCK(CLOCK_50), .Reset(SW[9]), .KEY(\simKEY[0]), .out(R_Tug)); metaStability R_dff (.CLOCK(CLOCK_50), .Reset(SW[9]), .KEY(\simKEY[3]), .out(L_Tug));
29
30
31
32
          // User Inputs
          player player1 (.CLOCK(CLOCK_50), .Reset(SW[9]), .KEY(R_Tug), .out(R));
34
          player player2 (.CLOCK(CLOCK_50), .Reset(SW[9]), .KEY(L_Tug), .out(L));
35
36
          // Instantiate LED FSMs for LED1 to LED4
          normalLight led1(.CLOCK(CLOCK_50), .Reset(SW[9]), .L(L), .R(R), .NL(LEDR[2]), .NR(1'b0),
37
          .lightOn(LEDR[^1]); normalLight led2(.CLOCK(CLOCK_50), .Reset(SW[^9]), .L(L), .R(R), .NL(LEDR[^3]), .NR(LEDR[^1
38
      ]), .lightOn(LEDR[2]));
39
          normalLight led3(.CLOCK(CLOCK_50), .Reset(SW[9]), .L(L), .R(R), .NL(LEDR[4]), .NR(LEDR[2
     ]), .lightOn(LEDR[3]));
normalLight led4(.CLOCK(CLOCK_50), .Reset(SW[9]), .L(L), .R(R), .NL(LEDR[5]), .NR(LEDR[3
40
      ]), .lightOn(LEDR[4]));
41
42
          // Instatiate center LED FSM, LED5
43
          centerLight led5(.CLOCK(CLOCK_50), .Reset(SW[9]), .L(L), .R(R), .NL(LEDR[6]), .NR(LEDR[4
     ]), .lightOn(LEDR[5]));
44
45
          // Instantiate LED FSMs for LED1 to LED4
46
          normalLight led6(.CLOCK(CLOCK_50), .Reset(SW[9]), .L(L), .R(R), .NL(LEDR[7]), .NR(LEDR[5]
      ]), .lightOn(LEDR[6]))
47
          normalLight led7(.CLOCK(CLOCK_50), .Reset(SW[9]), .L(L), .R(R), .NL(LEDR[8]), .NR(LEDR[6
      ]), .lightOn(LEDR[7]));
48
          normalLight led8(.CLOCK(CLOCK_50), .Reset(SW[9]), .L(L), .R(R), .NL(LEDR[9]), .NR(LEDR[7
     ]), .lightOn(LEDR[8])); normalLight led9(.CLOCK(CLOCK_50), .Reset(SW[9]), .L(L), .R(R), .NL(1'b1),
49
                                                                                                    .NR(LEDR[8
      ]), .lightOn(LEDR[9]));
50
          // Instantiate victory logic to control HEXO
51
52
          victory victory_game(.CLOCK(CLOCK_50), .Reset(SW[9]), .LEDR9(LEDR[9]), .LEDR1(LEDR[1]),
      L(L), R(R), HEXO(HEXO);
53
54
55
     endmodule
56
      // Test bench for the testbench module.
57
     // Standard I/O for the DE1_SoC
58
59
     module DE1_SoC_testbench();
          // Test inputs and outputs logic [6:0] HEXO, HEX1, HEX2, HEX3, HEX4, HEX5; logic [9:0] LEDR;
60
61
                  [<mark>3:0</mark>] KEY;
62
          logic
          logic [9:0] SW;
63
64
          logic CLOCK_50;
65
66
          // Instantiate the DUT (Device Under Test)
```

```
DE1_SOC dut(HEX0, HEX1, HEX2, HEX3, HEX4, HEX5, LEDR, KEY, SW, CLOCK_50);
 68
              // Define Clock period for simulation
 69
 70
             parameter Clock_PERIOD = 100;
 71
              // Clock generation for simulation
 73
             initial begin
 74
                   CLOCK_50 \ll 0;
 75
                   forever #Clock_PERIOD CLOCK_50 <= ~CLOCK_50;</pre>
 76
77
             end
 78
                  initial begin
 79
                   // Reset the game
                   SW[9] <= 1; @(posedge CLOCK_50)
SW[9] <= 0; @(posedge CLOCK_50)
 80
 81
 82
 83
                   // Game starts, LEDs should be initialized
                   KEY[0] \leftarrow 1; KEY[3] \leftarrow 1; @(posedge\ CLOCK_50)
 84
 85
 86
                   // Game where Player1 wins right away
                   for (int i=0; i<6; i++) begin
  KEY[0] <= 0; @(posedge CLOCK_50);
  KEY[0] <= 1; @(posedge CLOCK_50);</pre>
 87
 88
 89
 90
 91
                   @(posedge CLOCK_50) //To view change in HEXO in ModelSim
 92
 93
                   @(posedge CLOCK_50)
 94
                   @(posedge CLOCK_50)
 95
                   @(posedge CLOCK_50)
 96
 97
                   // Game reset and play again
                   SW[9] <= 1; @(posedge CLOCK_50)
SW[9] <= 0; @(posedge CLOCK_50)
 98
 99
100
101
                   // Game starts, LEDs should be initialized
102
                   KEY[0] \leftarrow 1; KEY[3] \leftarrow 1; @(posedge CLOCK_50)
103
                   // Game where Player2 wins right away
104
                   for (int i=0; i<6; i++) begin
  KEY[3] <= 0; @(posedge CLOCK_50);
  KEY[3] <= 1; @(posedge CLOCK_50);</pre>
105
106
107
108
109
                   @(posedge CLOCK_50) //To view change in HEXO in ModelSim
110
                   @(posedge CLOCK_50)
@(posedge CLOCK_50)
111
112
113
                   @(posedge CLOCK_50)
114
                   // Game reset and play again
SW[9] <= 1; @(posedge CLOCK_50)
SW[9] <= 0; @(posedge CLOCK_50)</pre>
115
116
117
118
119
                   // Game starts, LEDs should be initialized
120
                   KEY[0] \leftarrow 1; KEY[3] \leftarrow 1; @(posedge CLOCK_50)
121
                   // Game where Players go back and forth, Player2 wins
                   for (int i=0; i<5; i++) begin
  KEY[3] <= 0; @(posedge CLOCK_50);</pre>
123
124
125
                       KEY[3] \leftarrow 1; @(posedge CLOCK_50);
                       KEY[0] \leftarrow 0; @(posedge CLOCK_50); // Tests long press
126
                       KEY[3] <= 0; @(posedge CLOCK_50);
KEY[3] <= 1; @(posedge CLOCK_50);
KEY[0] <= 1; @(posedge CLOCK_50);</pre>
127
128
129
130
131
                   @(posedge CLOCK_50) //To view change in HEXO in ModelSim
132
133
                   @(posedge CLOCK_50)
                   @(posedge CLOCK_50)
134
135
                   @(posedge CLOCK_50)
136
137
                $stop;
138
             end
139
140
        endmodule
```