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LAB 1	Code implementation	Video record	Brainstorm + pre-code
LAB 2	Code implementation	Brainstorm + pre-code	Video record
LAB 3	Brainstorm + pre-code	Video record	Code implementation
LAB 4	Brainstorm + pre-code	Code implementation	Video record
LAB 5	Code implementation Video record	Brainstorm + pre-code (we re-did code from 0)	Brainstorm + pre-code (we re-did code from 0)
LAB 6	Video record	Code implementation	Brainstorm + pre-code
LAB 7	Video record	Brainstorm + pre-code	Code implementation

#### Notes:

- We did code and helped each other for each lab; however, we divided tasks for efficiency. Most of the work is done in a group, but the submission is done by tasks. The way we operated was:
  - ONE person would do an initial brainstorm and pre-lab so we could later think about what would be our code pathing.
  - We meet through videocall and draft a code so ONE of us can take up that task.
  - The results (code + sims) are sent to the video person, so he reviews and test the code on the board and troubleshoots anything if needed
  - ONE person, usually the brainstorming/pre-code responsible, reviews the code one last time and submits it into github.
- Roles:
  - Brainstorm + pre-code
    - Usually contains a draft of the code, files, and target we will be aiming to complete the lab successfully.
  - Code implementation
    - Responsible to write the code into Verilog and make sure it compiles correctly. Testbench is usually required, but not necessarily if video responsible wants/needs to do it.
  - Video record
    - Make sure to review the code and pre-code so everything makes sense and lab targets have been accomplished. Might or might not need to do testbench code as well.
- Completion:
  - Every step is always shared in a discord group chat so we can help each other. Before submitting any lab, we also make sure we all in the same page.
- LAB 0 was made individually by each of us as we were not aware of the groups.