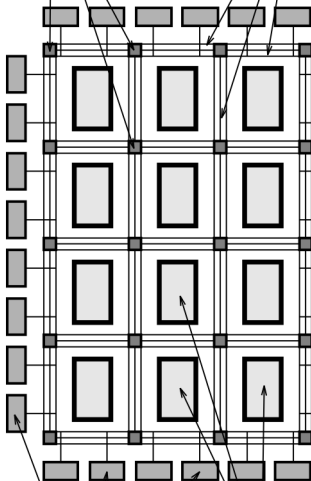


Matriz Programável de Interconexões

Canais de Roteamento



Bloco SRAM

Blocos Lógicos Configuráveis

Portas I/O