

## Logic Table and Equations:

State / Next State Table ← shows sequence of operation and ~~input~~ flip-flop inputs

Current State (flip-flop output)			Excitation (flip-flop input)			Next State (what flip-flop output will become after clock pulse)		
Q2	Q1	Q0	D2	D1	D0	Q2*	Q1*	Q0*
0	0	0	0	0	1	0	0	1
0	0	1	0	1	1	0	1	1
0	1	0	1	0	1	1	0	1
0	1	1	1	1	1	1	1	1
1	0	0	0	0	0	0	0	0
1	0	1	0	0	0	0	0	0
1	1	0	0	0	0	0	0	0
1	1	1	0	0	0	0	0	0

Logic Equations ← shows circuit connections

$D2 = Q1 \cdot \overline{Q2}$      
  $D1 = Q0 \cdot \overline{Q2}$      
  $D0 = \overline{Q2}$

## Wiring Diagram:

