

5. (a) Complete the missing steps of the fetch-execute cycle.

2

1.	
2.	The processor activates the read line on the control bus.
3.	
4.	The instruction in the instruction register is then interpreted by the decoder and carried out.

(b) State one factor that can improve the performance of the fetch-execute cycle and explain why it improves performance.

2

Factor _____

Explanation _____
