	(b)		Award 1 mark for each bullet from factor with matching explanation. Maximum 2 marks.	2	
			• Clock speed • affects the number of instructions executed/carried out per second		
			OR		
			• Data bus width • affects the number of bits/data transferred in a single operation/reduces number of fetch operations		
			OR		
			• Cache • affects the number of fetches/ accesses from slower main memory.		
			OR		
			• Number of cores • affects how many instructions can be executed concurrently		