

	(b)	<p>Matching explanation:</p> <ul style="list-style-type: none"> • multiple instructions simultaneously • more bits transferred in a single operation • reduces number of accesses to slower main memory • any other valid response 	1	<p>Award 1 mark for bullet that matches method stated in part (a).</p> <p>Do not award the mark for increasing clock speed as it is in the stem.</p>
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