

5. (a) Complete the missing steps of the fetch-execute cycle.

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1.	
2.	The processor activates the read line on the control bus.
3.	
4.	The instruction in the instruction register is then interpreted by the decoder and carried out.

- (b) State one factor that can improve the performance of the fetch-execute cycle and explain why it improves performance.

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Factor _____

Explanation _____
