# CS 744: TENSORFLOW

Shivaram Venkataraman Fall 2019

### **ADMINISTRIVIA**

- Assignment I Grading: In progress
- Assignment 2 due on Thu! Friday
  Course Project: Introduction due Oct 17th

Bismarck -> logistic Kegressian SVMs

Supervised learning, Unified Interface
Shared memory, Model fits in memory

Parameter Server Ad Chick

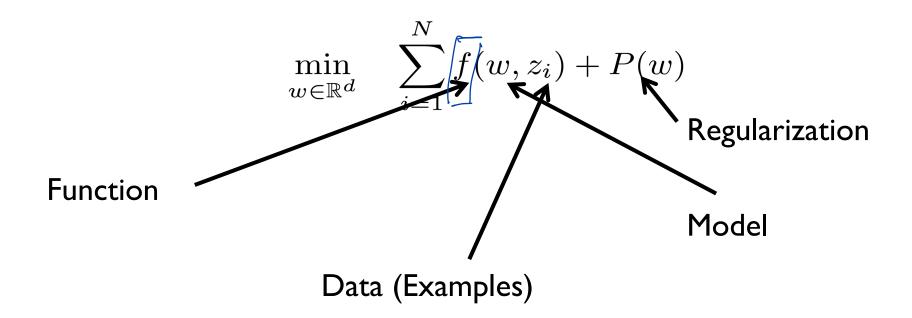
Large datasets, large models (PB scale) Shard data, parameters

Consistency model Fault tolerance

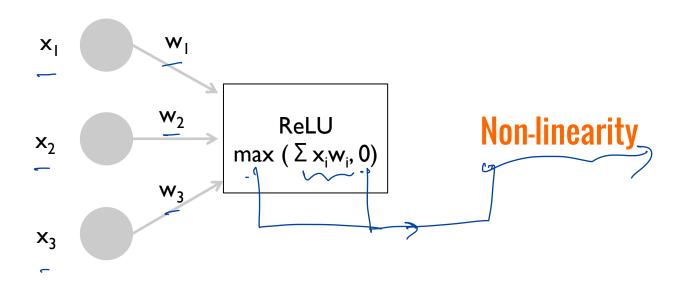
Machine Learning

WHAT IS DEEP LEARNING?

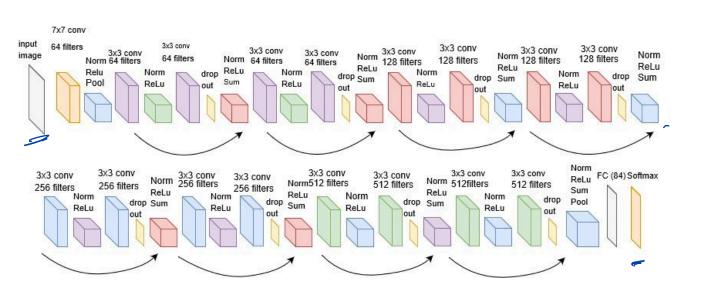
### **OPTIMIZATION**



## DEEP LEARNING



### DEEP LEARNING

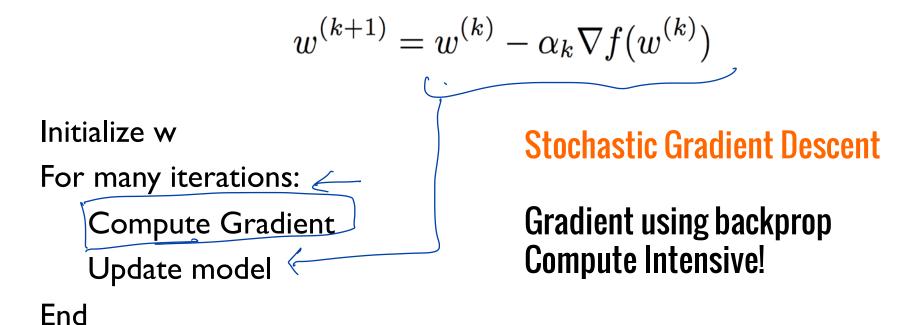


### ResNet18

Convolution / ReLU / MaxPool / Fully Connected SoftMax

. . .

### MODEL TRAINING



# DIST BELIEF SHORTCOMINGS

- Written in C++, hard to experiment - Writing new types of layers - New Optimization methods - Execution pattern fixed Lo push, Pell if lexibility" in how work was partitioned and how comm. Lappened

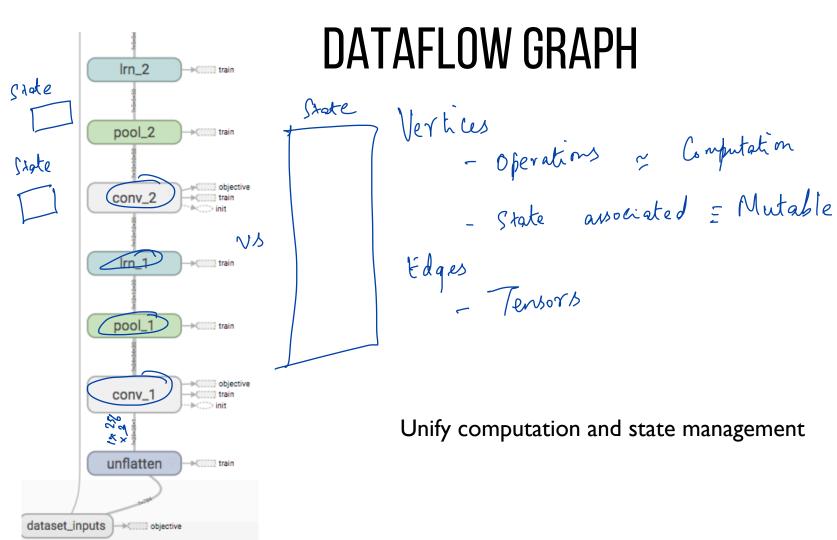
### TENSORFLOW: DESIGN PRINCIPLES

- Dataflow graphs of primitive operators

- Deferred execution: Symbolic dataflow graph

- Heterogeneous accelerators TPUS

Mobile phases



### **EXECUTION MODEL**

Multiple concurrent executions of overlapping sub-graphs

Vertices have mutable state; shared between executions

### DATAFLOW GRAPH ELEMENTS

Worker 2

Worker 1

#### **Edges: Tensors**

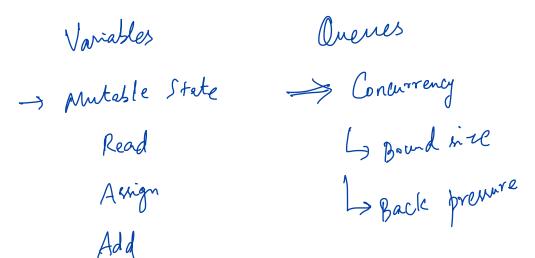
- N-dimensional arrays, dense representation by default
- Operations take in tensors and return tensors

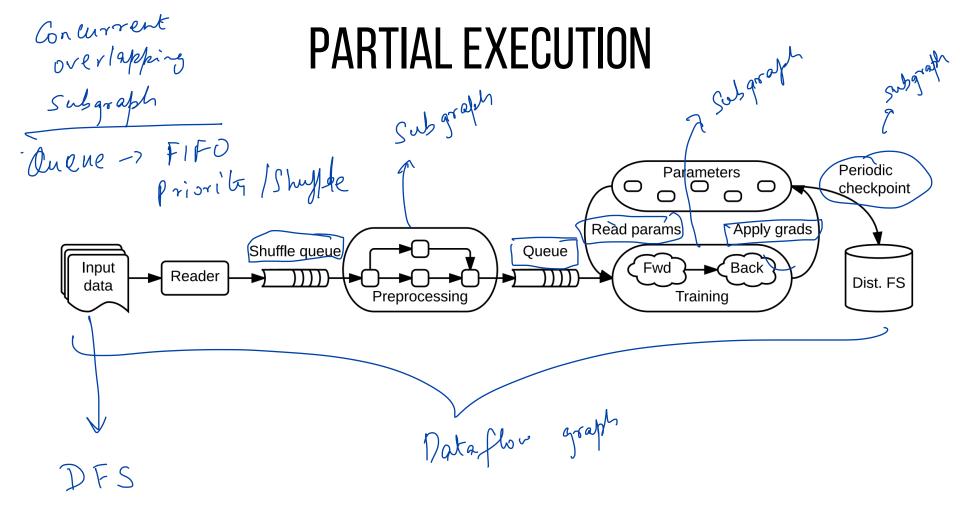
#### Vertices: Operations

- Tensor → Tensor functions

#### Stateful Operations

- Variables
- Queues





S \* X + b DISTRIBUTED EXECUTION Send, Recu operations
are inserted for Dist operation Hacement
are inserted for Dist and and RegisterGraph() Worker /job:worker/task:0 Master Client Rustep() "rendezvous- (cej" & DC send Worker RegisterGraph() /job:ps/task:0 RRC ack

### **EXECUTION MODES**

#### Partial Execution

- Input batches from queue
- Concurrent training steps
- Shared model
- "Horizontal" parallelism?

#### Distributed Execution

- Operations placed on devices
- Account for colocation
- Manual placement decisions?
- Send-Recv to stitch subgraphs

### **CONTROL FLOW**

- Support for RNNs, LSTMs
- Switch and Merge operators to support conditionals
- Enter, Exit, NextIteration to support while loops

```
input = ... # A sequence of tensors
state = 0 # Initial state
w = ... # Trainable weights

for i in range(len(input)):
   state, out[i] = f(state, w, input[i])
```

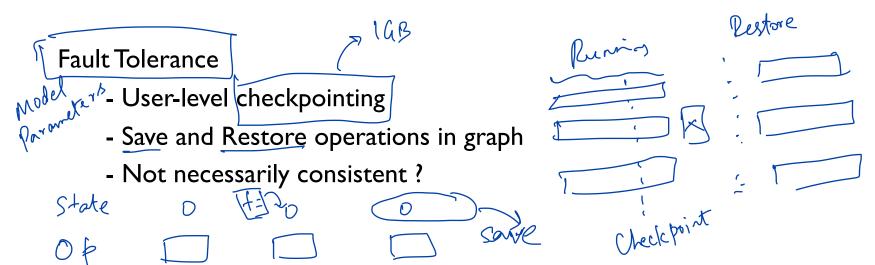
### **EXTENSIONS**

f = logistic regression

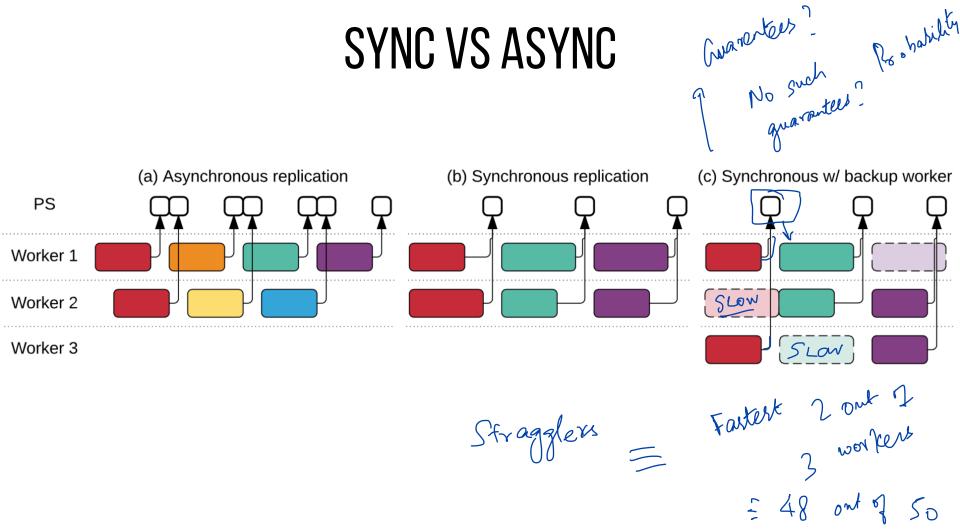
Vf: V logistic regression

Automatic Differentiation \_\_\_\_ Powerful

- Given a symbolic expression, generate its gradient
- Also extend to control flow operations



# SYNC VS ASYNC



### **IMPLEMENTATION**

Training libraries Inference libs	
Python client C++ client	
C API	
Distributed master Dataflow executor	]
Const Var MatMul Conv2D ReLU Queue  Kernel implementations	$\bigg] \bigg $
RPC RDMA CPU GPU Device layer	

Heterogeneous devices

# **DISCUSSION**

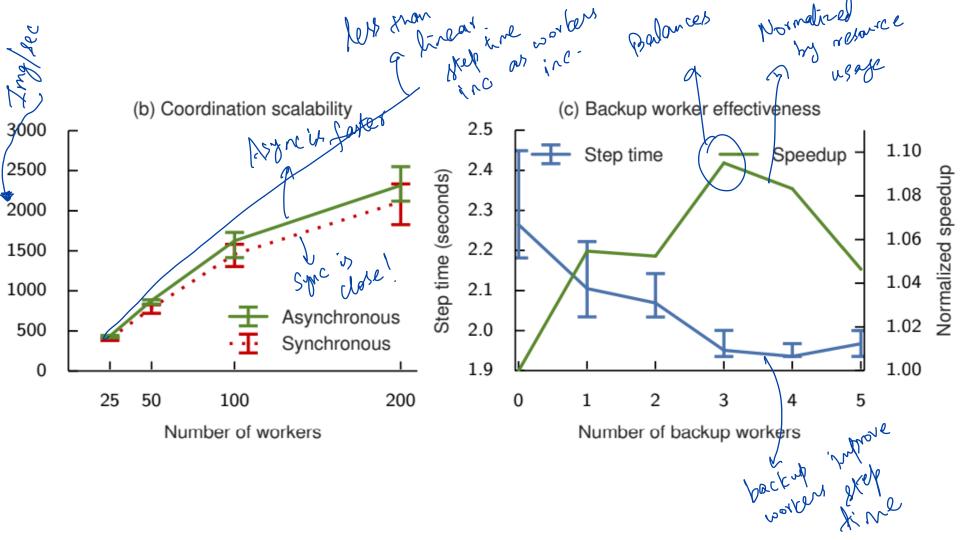
https://forms.gle/L9oA69DQe2a7yg3CA

How is the dataflow graph used in Tensorflow similar / different from Apache Spark? What are the implications of that? - Vertices: Operations, State edges: Tensors

Multiple subgraphs
E parallelism within
a step entire Muster

checkpointed RODS are partitioned, can do partial recovery

- Verter rendes in



What are some shortcomings of the programming model used in Tensorflow?

What could be some ways to improve it?

The graph is static of the programming model used in Tensorflow?

The graph is static of the programming model used in Tensorflow?

- Placement is low-level not automatic

### **NEXT STEPS**

Next class: Ray

Assignment 2 due this week!

Course project