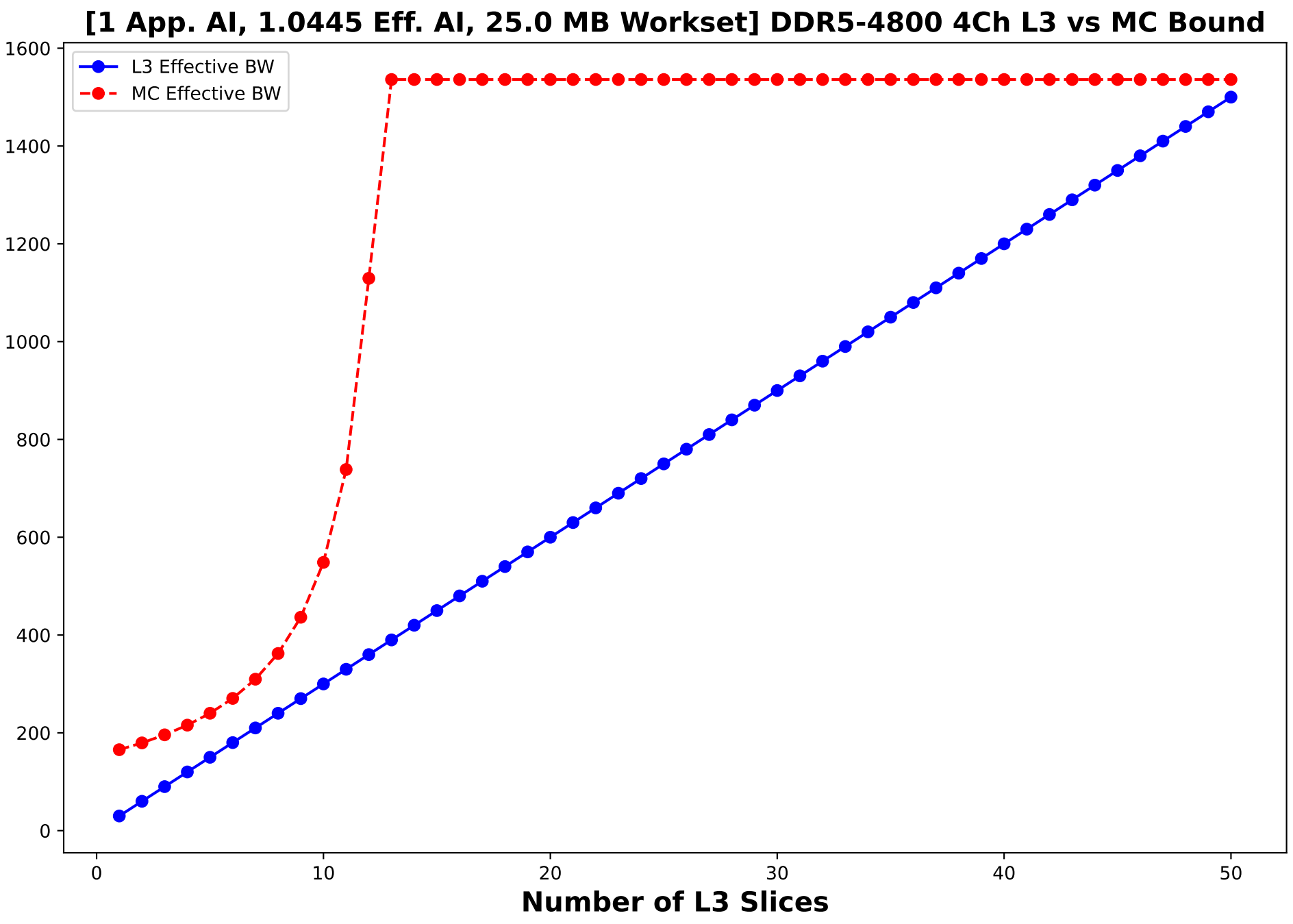
[1 App. AI, 1.0445 Eff. AI, 25.0 MB Workset] DDR5-4800 4Ch L3 vs MC Bound
L3 Effective BW
MC Effective BW
1600
1400
1200
1000
800
600
400
200
0
0
10
20
30
40
50
Number of L3 Slices