

# 4-bit kogge stone adder

- Author: Rayn Xiao
- Description: Implement a 4-bit adder for testing
- Language: Verilog

## How it works

Explain. Explain.

## How to test

Test. Test.

## External hardware

External. External.

## Pinout

#	Input	Output	Bidirectional
0	a[0]	sum[0]	0
1	a[1]	sum[1]	0
2	a[2]	sum[2]	0
3	a[3]	sum[3]	0
4	b[4]	carry_out	0
5	b[5]	3'b000	0
6	b[6]	3'b000	0
7	b[7]	3'b000	0