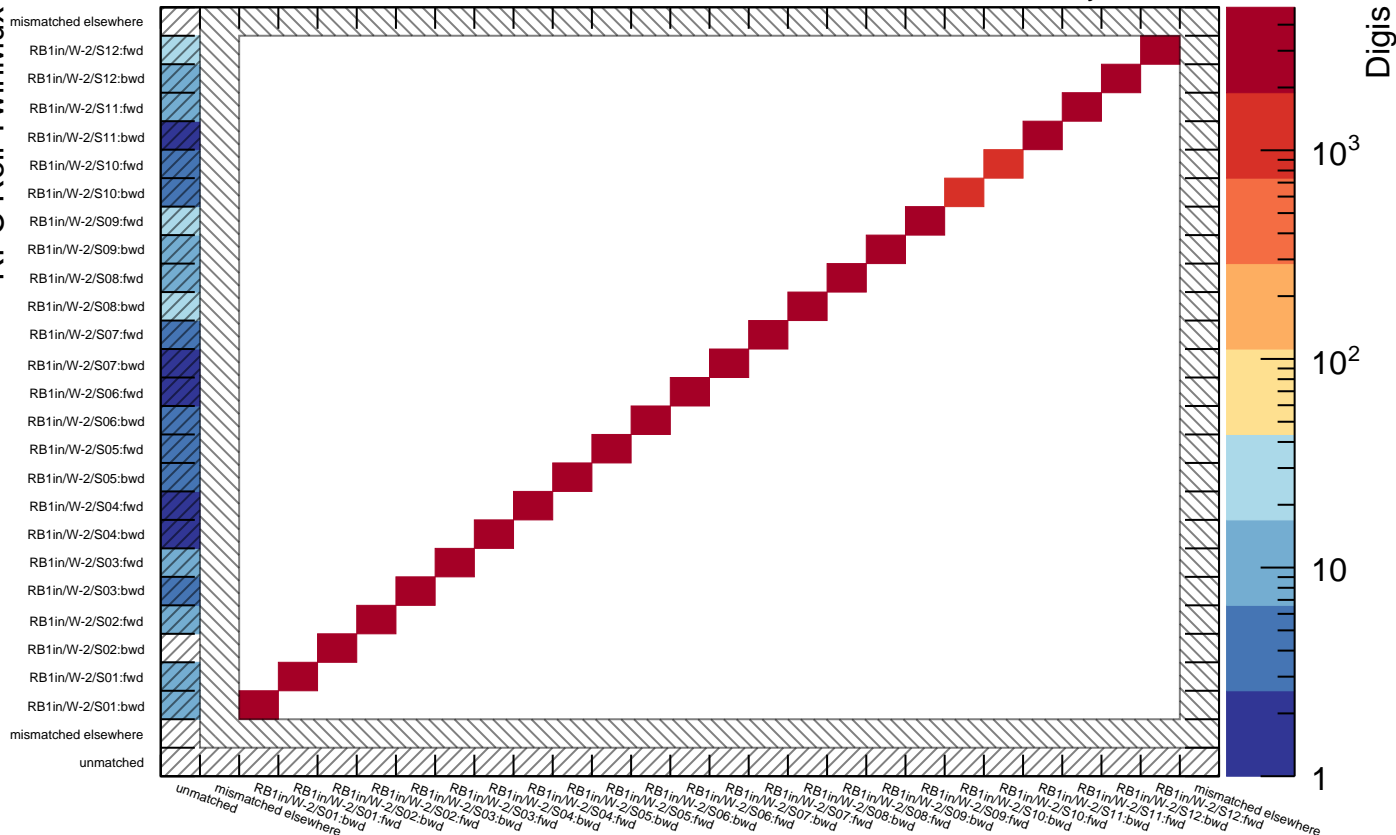


RB1in/W-2

CMS Preliminary - run 306122

RPC Roll TwinMux

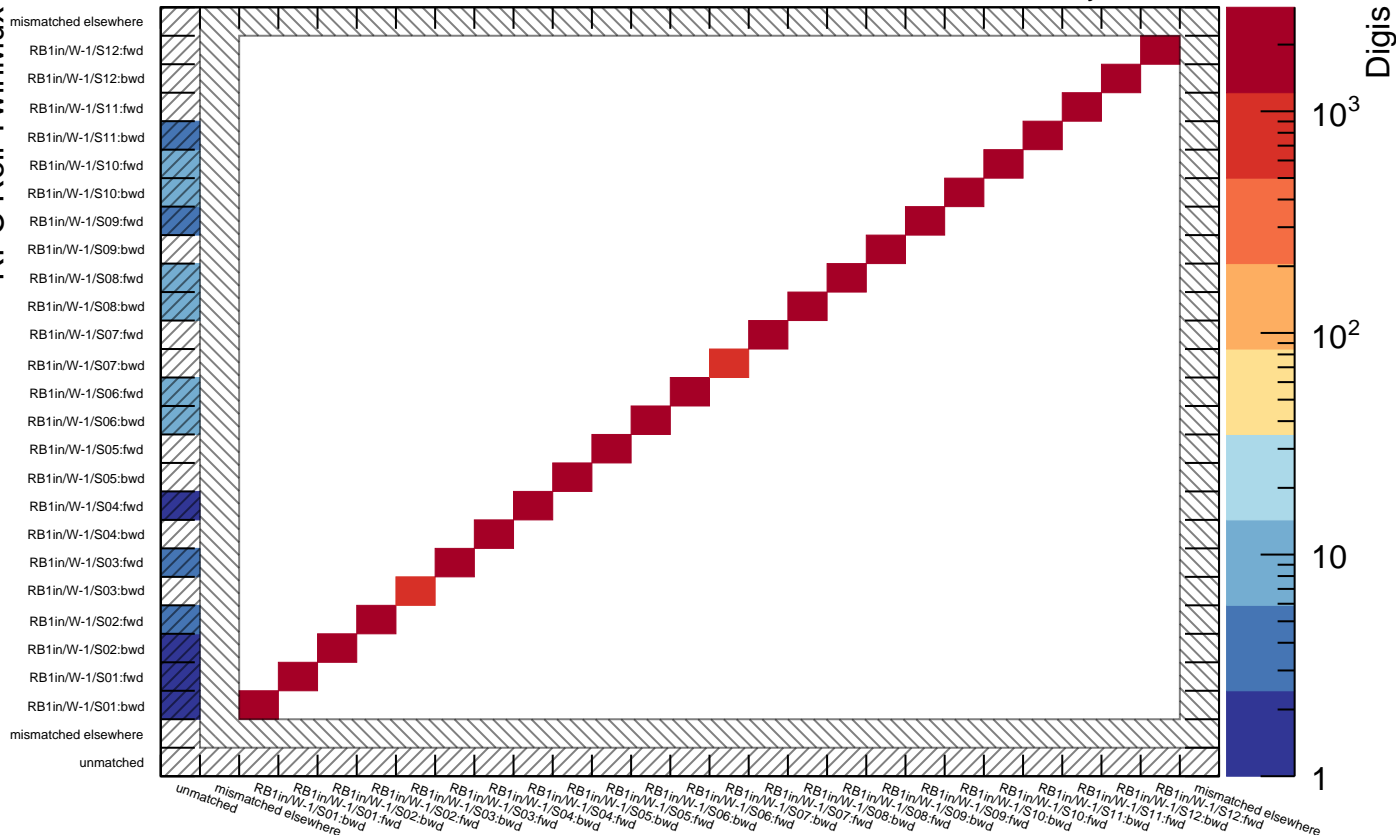


RPC Roll Legacy

RB1in/W-1

CMS Preliminary - run 306122

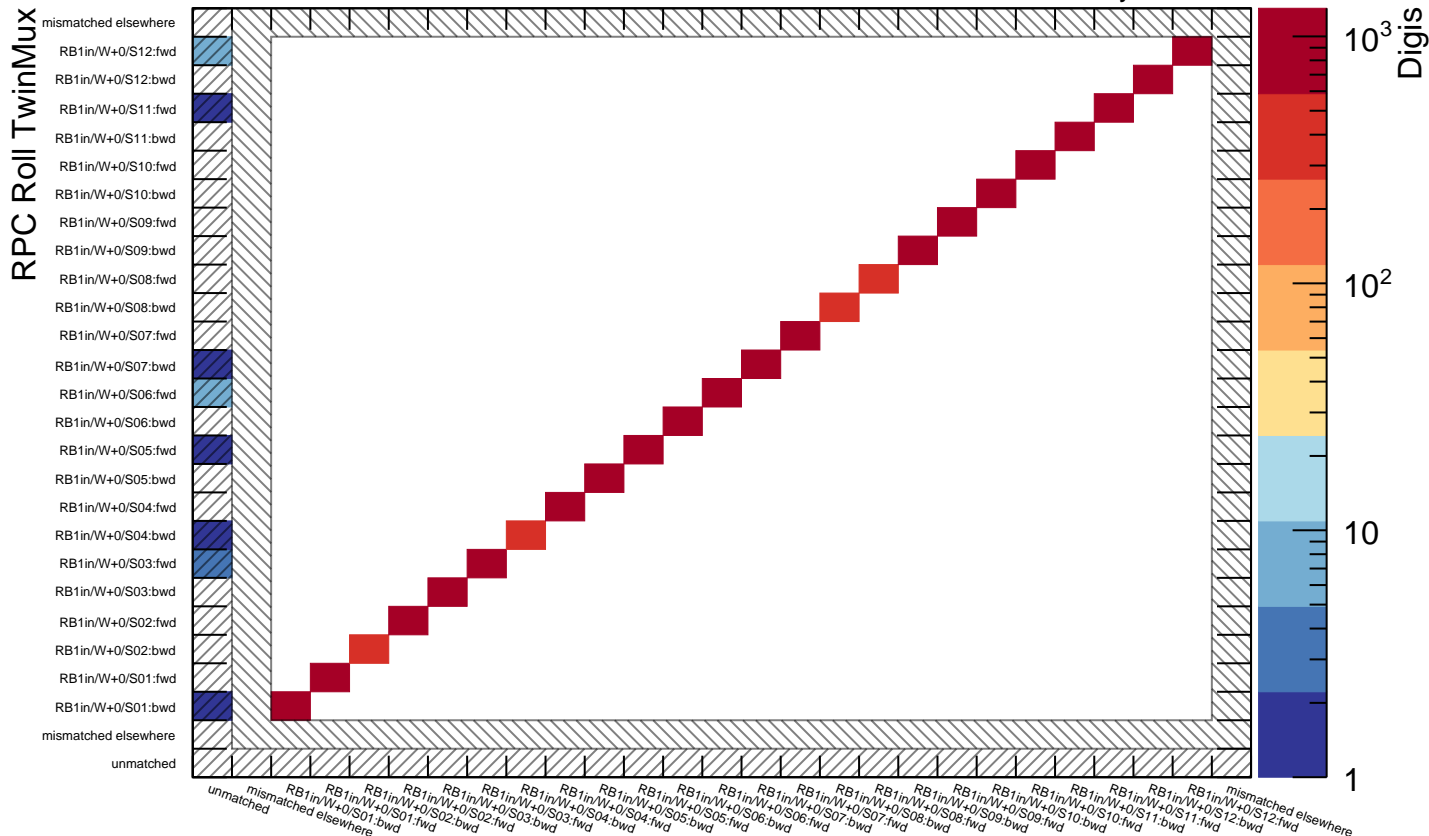
RPC Roll TwinMux



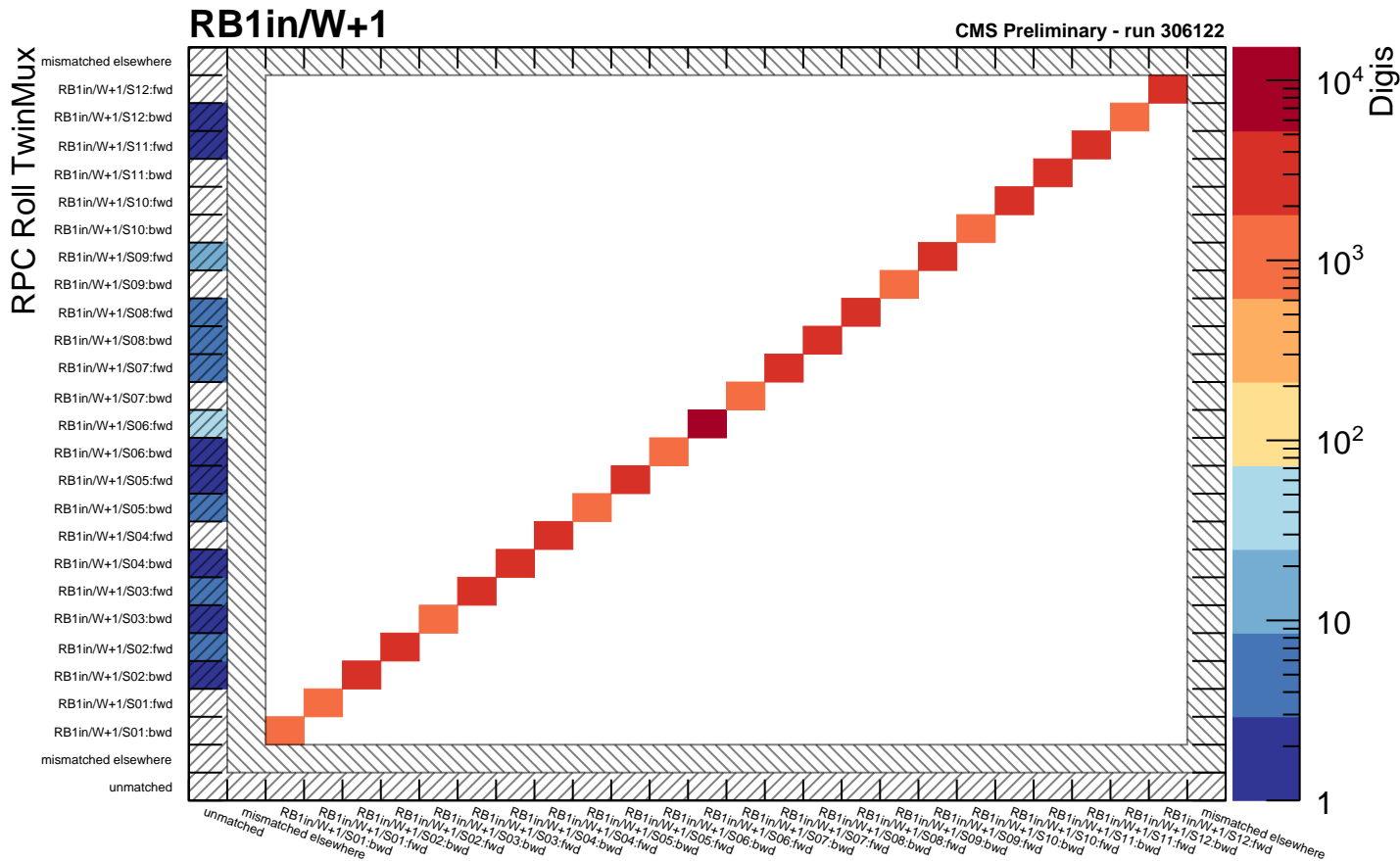
RPC Roll Legacy

RB1in/W+0

CMS Preliminary - run 306122



RPC Roll Legacy

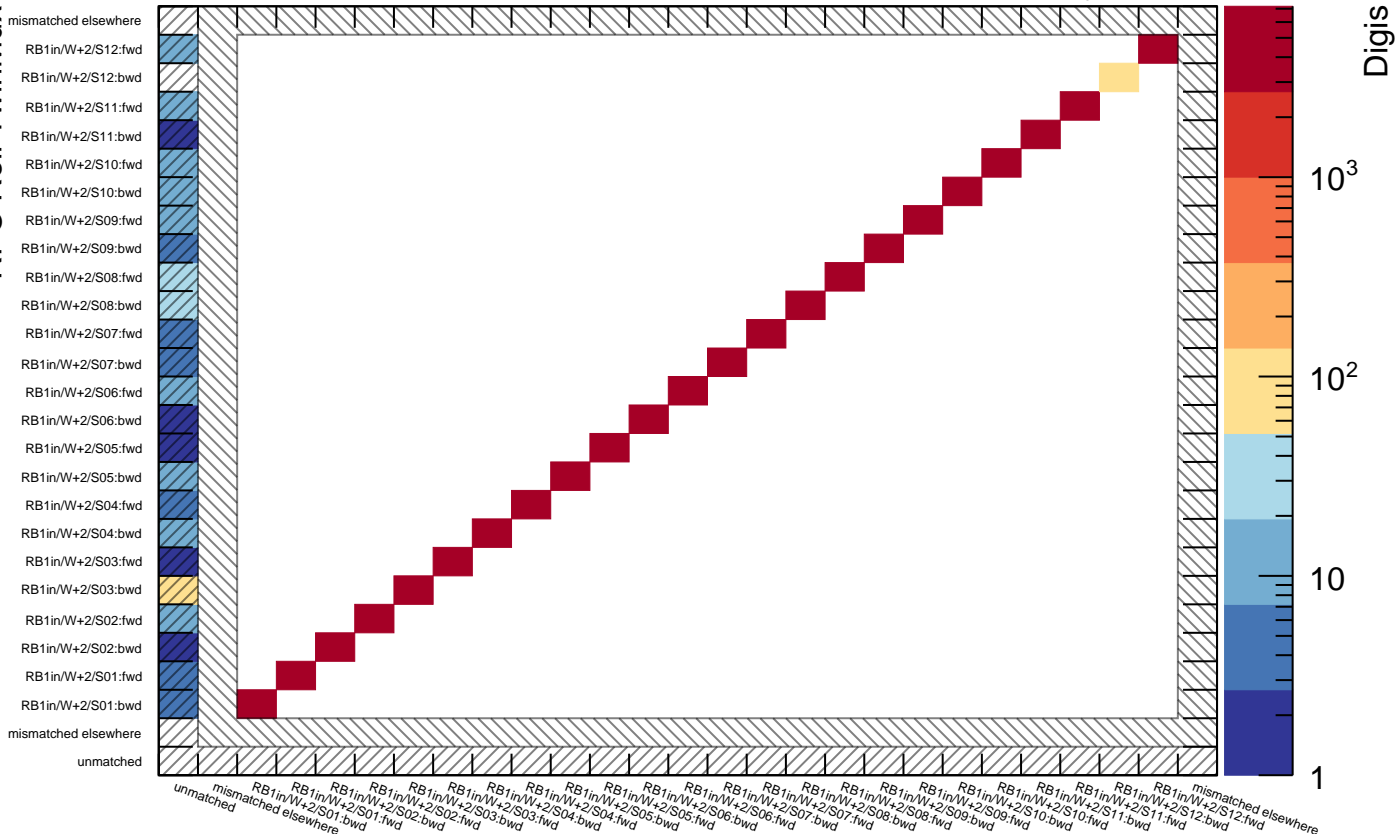


RPC Roll Legacy

RB1in/W+2

CMS Preliminary - run 306122

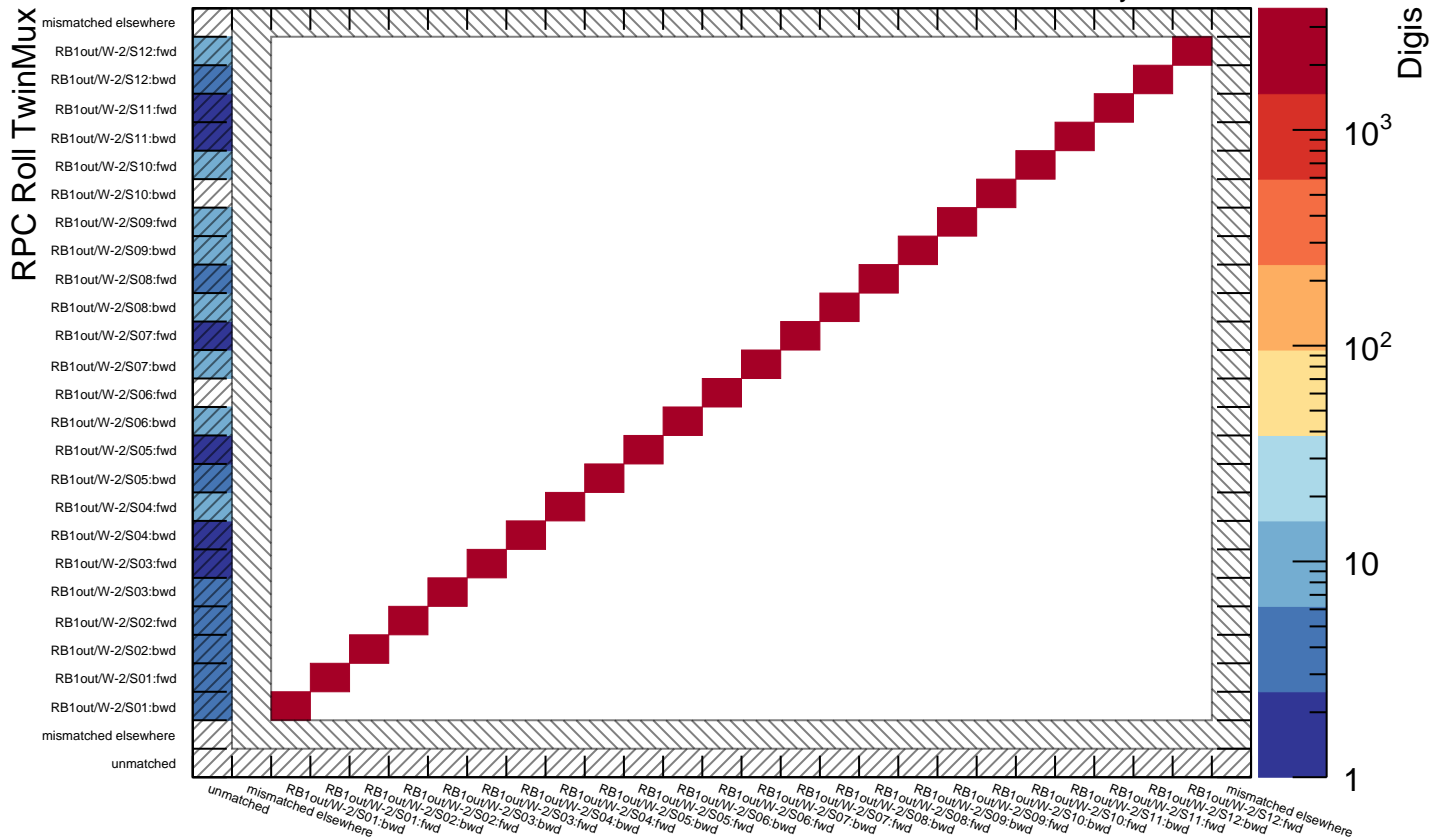
RPC Roll TwinMux



RPC Roll Legacy

RB1out/W-2

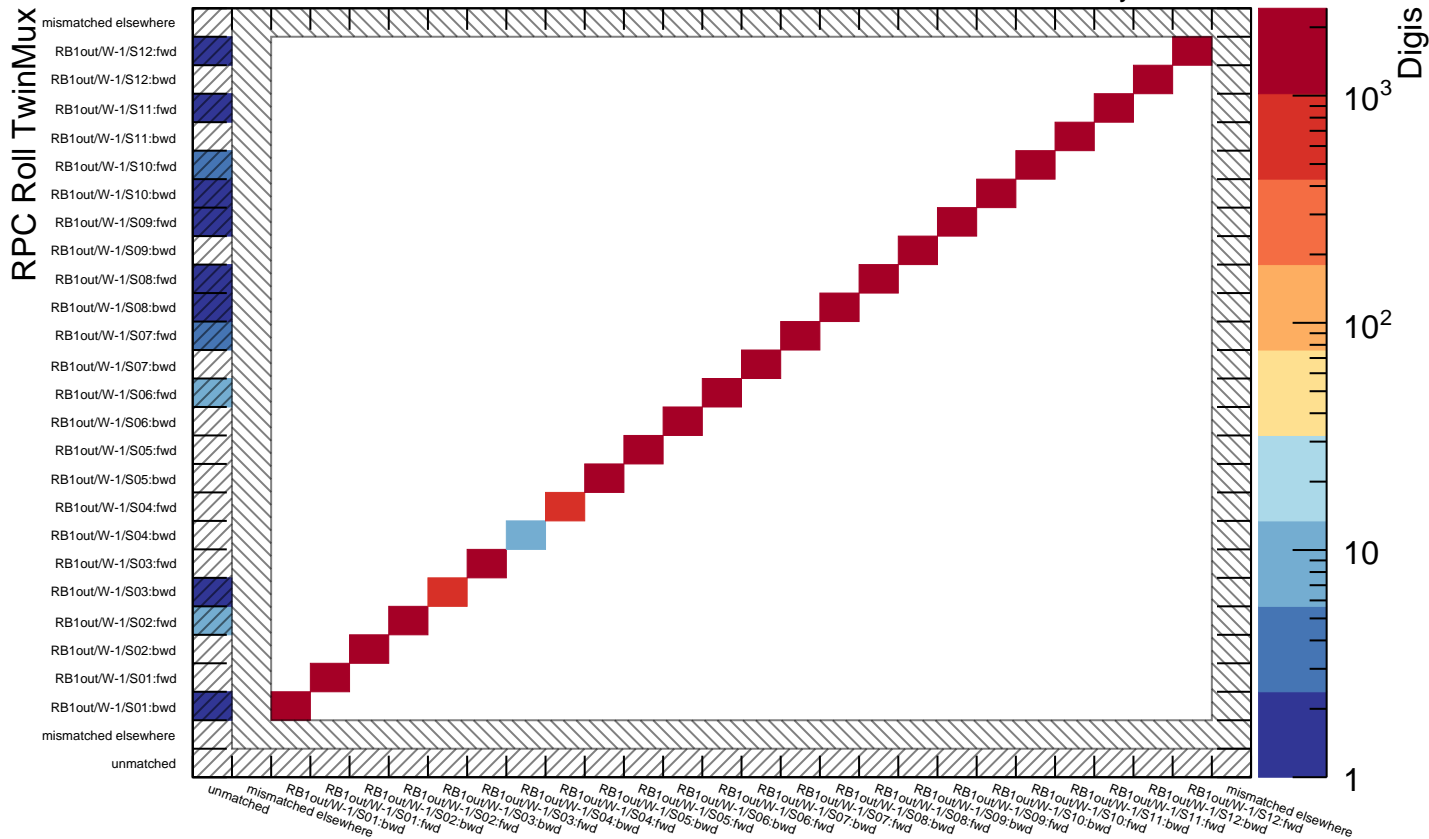
CMS Preliminary - run 306122



RPC Roll Legacy

RB1out/W-1

CMS Preliminary - run 306122

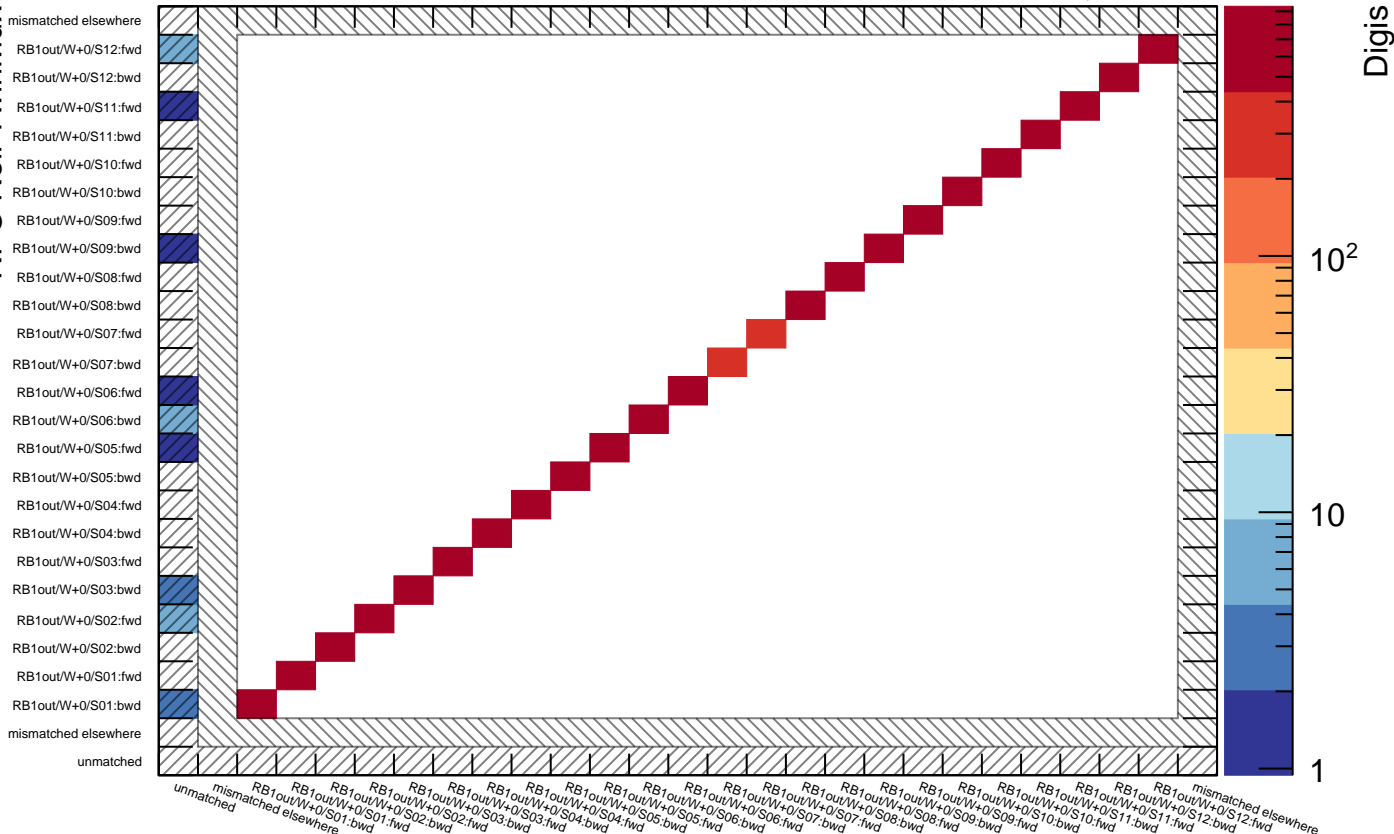


RPC Roll Legacy

RB1out/W+0

CMS Preliminary - run 306122

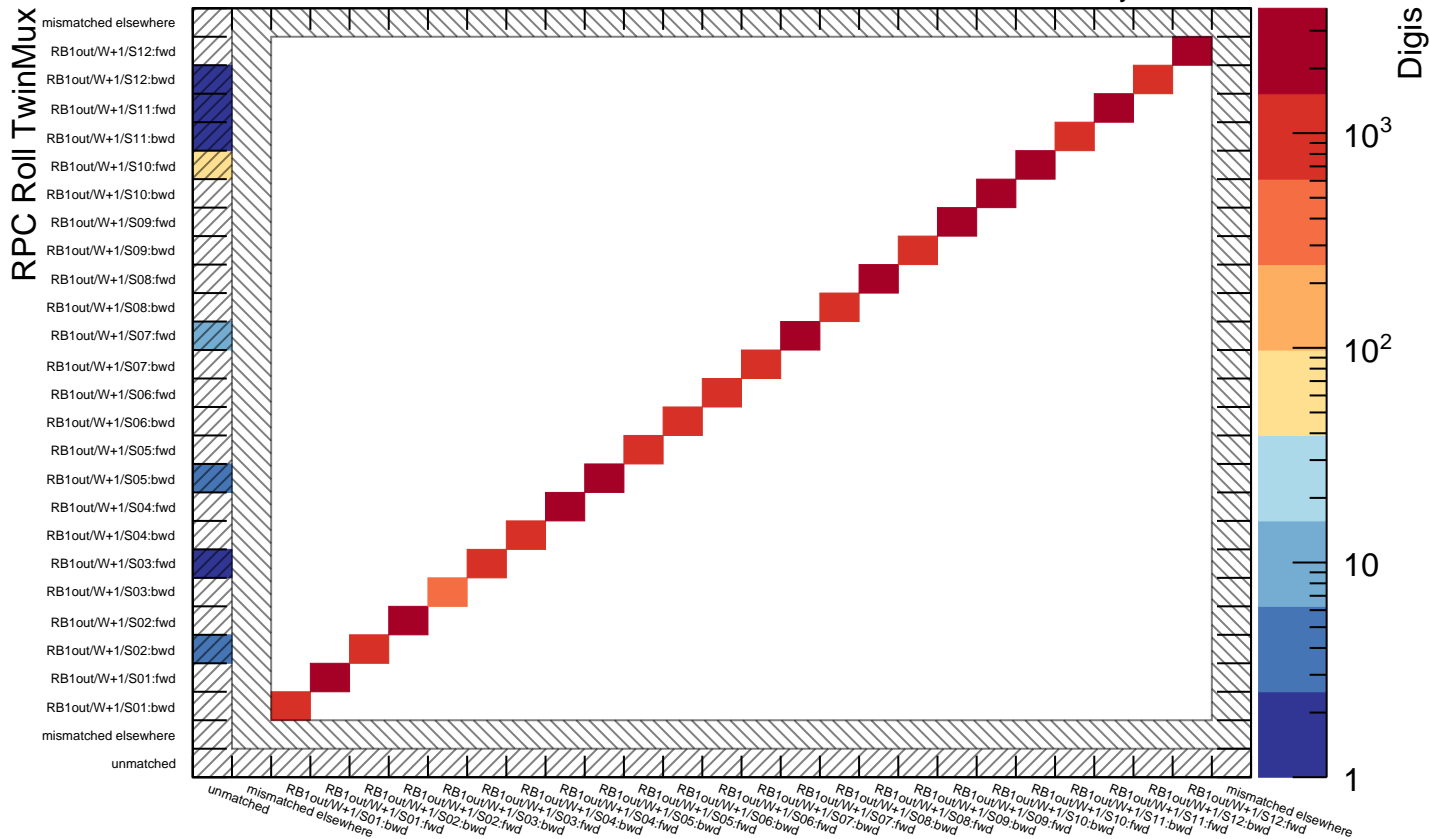
RPC Roll TwinMux



RPC Roll Legacy

RB1out/W+1

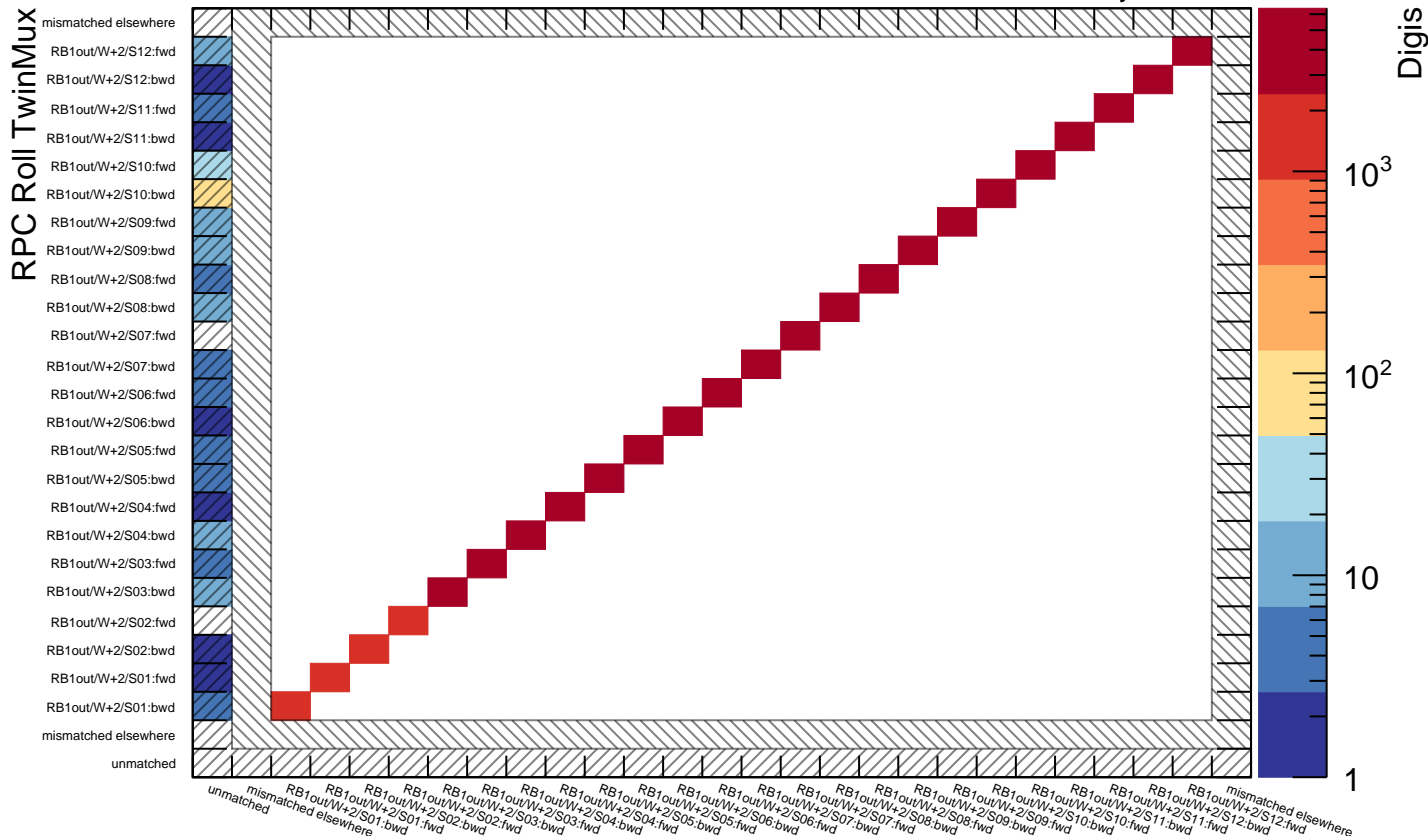
CMS Preliminary - run 306122



RPC Roll Legacy

RB1out/W+2

CMS Preliminary - run 306122

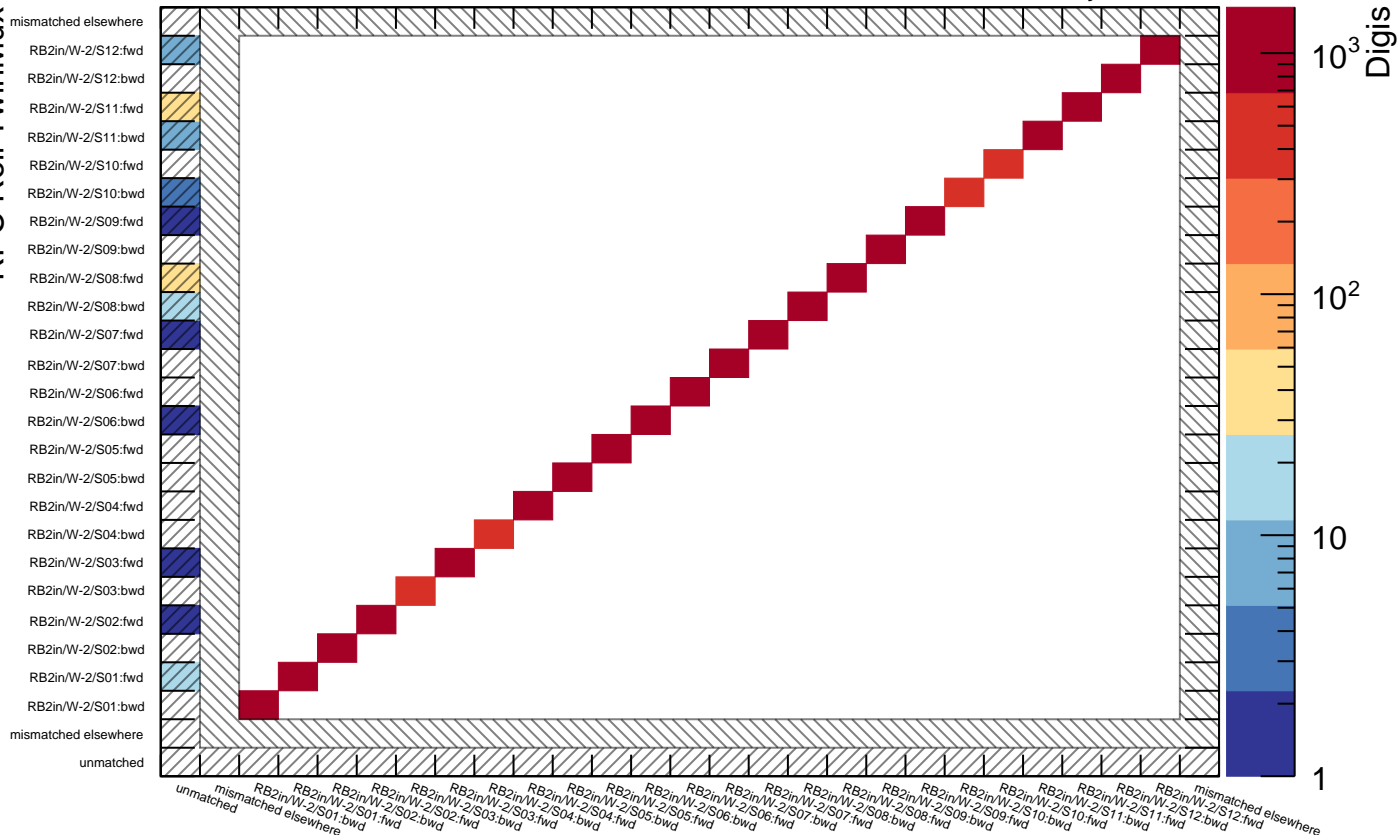


RPC Roll Legacy

RB2in/W-2

CMS Preliminary - run 306122

RPC Roll TwinMux



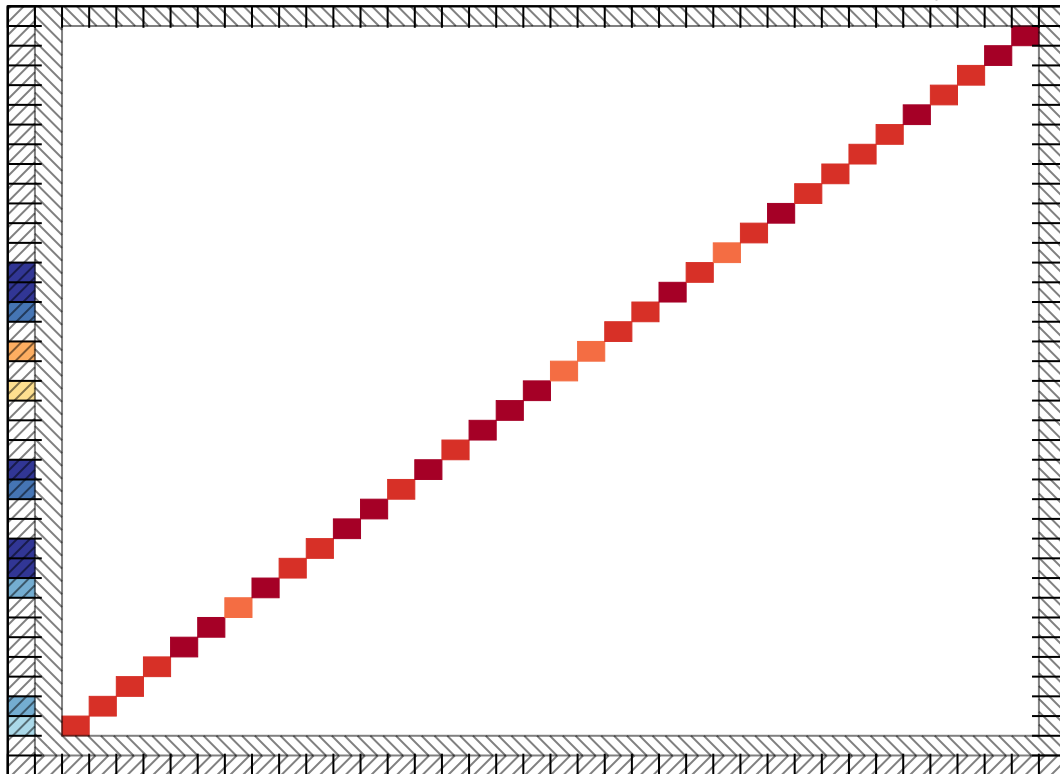
RPC Roll Legacy

RPC Roll TwinMux

RB2in/W-1

CMS Preliminary - run 306122

mismatched elsewhere
 RB2in/W-1/S12.fwd
 RB2in/W-1/S12.mid
 RB2in/W-1/S12.bwd
 RB2in/W-1/S11.fwd
 RB2in/W-1/S11.mid
 RB2in/W-1/S11.bwd
 RB2in/W-1/S10.fwd
 RB2in/W-1/S10.mid
 RB2in/W-1/S10.bwd
 RB2in/W-1/S09.fwd
 RB2in/W-1/S09.mid
 RB2in/W-1/S09.bwd
 RB2in/W-1/S08.fwd
 RB2in/W-1/S08.mid
 RB2in/W-1/S08.bwd
 RB2in/W-1/S07.fwd
 RB2in/W-1/S07.mid
 RB2in/W-1/S07.bwd
 RB2in/W-1/S06.fwd
 RB2in/W-1/S06.mid
 RB2in/W-1/S06.bwd
 RB2in/W-1/S05.fwd
 RB2in/W-1/S05.mid
 RB2in/W-1/S05.bwd
 RB2in/W-1/S04.fwd
 RB2in/W-1/S04.mid
 RB2in/W-1/S04.bwd
 RB2in/W-1/S03.fwd
 RB2in/W-1/S03.mid
 RB2in/W-1/S03.bwd
 RB2in/W-1/S02.fwd
 RB2in/W-1/S02.mid
 RB2in/W-1/S02.bwd
 RB2in/W-1/S01.fwd
 RB2in/W-1/S01.mid
 RB2in/W-1/S01.bwd
 mismatched elsewhere
 unmatched



Digits

10^2

10

1

mismatched elsewhere
 unmatched
 RB2in/W-1/S12.fwd
 RB2in/W-1/S12.mid
 RB2in/W-1/S12.bwd
 RB2in/W-1/S11.fwd
 RB2in/W-1/S11.mid
 RB2in/W-1/S11.bwd
 RB2in/W-1/S10.fwd
 RB2in/W-1/S10.mid
 RB2in/W-1/S10.bwd
 RB2in/W-1/S09.fwd
 RB2in/W-1/S09.mid
 RB2in/W-1/S09.bwd
 RB2in/W-1/S08.fwd
 RB2in/W-1/S08.mid
 RB2in/W-1/S08.bwd
 RB2in/W-1/S07.fwd
 RB2in/W-1/S07.mid
 RB2in/W-1/S07.bwd
 RB2in/W-1/S06.fwd
 RB2in/W-1/S06.mid
 RB2in/W-1/S06.bwd
 RB2in/W-1/S05.fwd
 RB2in/W-1/S05.mid
 RB2in/W-1/S05.bwd
 RB2in/W-1/S04.fwd
 RB2in/W-1/S04.mid
 RB2in/W-1/S04.bwd
 RB2in/W-1/S03.fwd
 RB2in/W-1/S03.mid
 RB2in/W-1/S03.bwd
 RB2in/W-1/S02.fwd
 RB2in/W-1/S02.mid
 RB2in/W-1/S02.bwd
 RB2in/W-1/S01.fwd
 RB2in/W-1/S01.mid
 RB2in/W-1/S01.bwd
 mismatched elsewhere
 unmatched

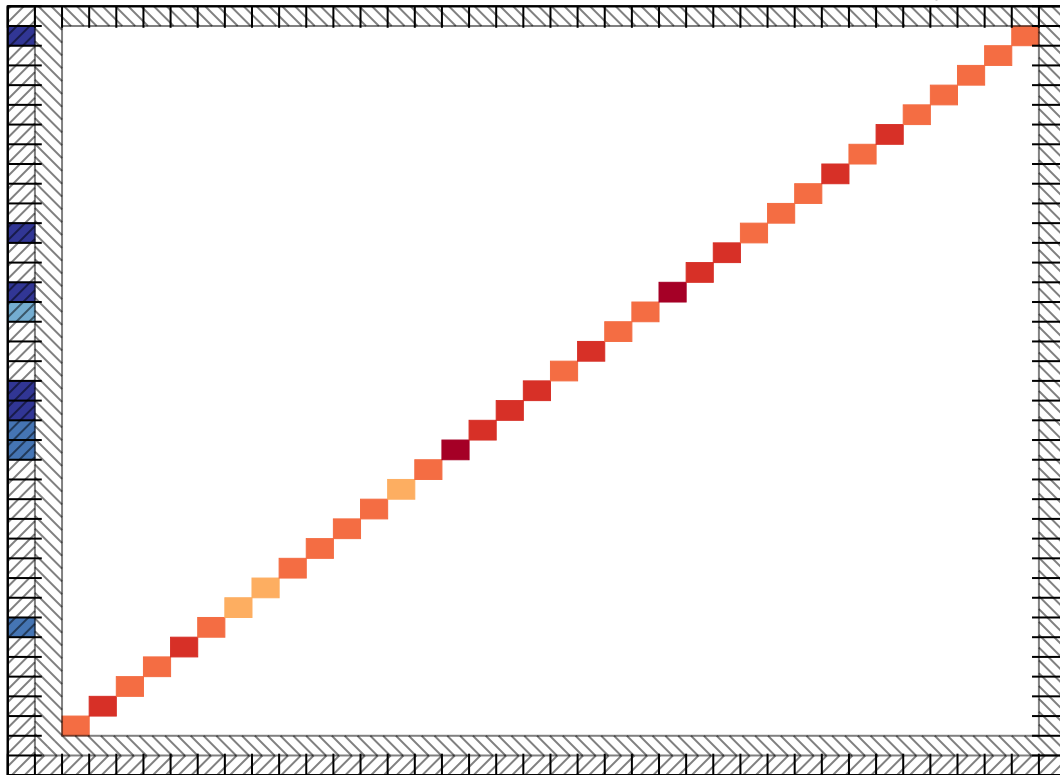
RPC Roll Legacy

RB2in/W+0

CMS Preliminary - run 306122

RPC Roll TwinMux

mismatched elsewhere
 RB2in/W+0/S12.fwd
 RB2in/W+0/S12.mid
 RB2in/W+0/S12.bwd
 RB2in/W+0/S11.fwd
 RB2in/W+0/S11.mid
 RB2in/W+0/S11.bwd
 RB2in/W+0/S10.fwd
 RB2in/W+0/S10.mid
 RB2in/W+0/S10.bwd
 RB2in/W+0/S09.fwd
 RB2in/W+0/S09.mid
 RB2in/W+0/S09.bwd
 RB2in/W+0/S08.fwd
 RB2in/W+0/S08.mid
 RB2in/W+0/S08.bwd
 RB2in/W+0/S07.fwd
 RB2in/W+0/S07.mid
 RB2in/W+0/S07.bwd
 RB2in/W+0/S06.fwd
 RB2in/W+0/S06.mid
 RB2in/W+0/S06.bwd
 RB2in/W+0/S05.fwd
 RB2in/W+0/S05.mid
 RB2in/W+0/S05.bwd
 RB2in/W+0/S04.fwd
 RB2in/W+0/S04.mid
 RB2in/W+0/S04.bwd
 RB2in/W+0/S03.fwd
 RB2in/W+0/S03.mid
 RB2in/W+0/S03.bwd
 RB2in/W+0/S02.fwd
 RB2in/W+0/S02.mid
 RB2in/W+0/S02.bwd
 RB2in/W+0/S01.fwd
 RB2in/W+0/S01.mid
 RB2in/W+0/S01.bwd
 mismatched elsewhere
 unmatched



10³
 10²
 10
 1

mismatched elsewhere
 unmatched
 RB2in/W+0/S12.fwd
 RB2in/W+0/S12.mid
 RB2in/W+0/S12.bwd
 RB2in/W+0/S11.fwd
 RB2in/W+0/S11.mid
 RB2in/W+0/S11.bwd
 RB2in/W+0/S10.fwd
 RB2in/W+0/S10.mid
 RB2in/W+0/S10.bwd
 RB2in/W+0/S09.fwd
 RB2in/W+0/S09.mid
 RB2in/W+0/S09.bwd
 RB2in/W+0/S08.fwd
 RB2in/W+0/S08.mid
 RB2in/W+0/S08.bwd
 RB2in/W+0/S07.fwd
 RB2in/W+0/S07.mid
 RB2in/W+0/S07.bwd
 RB2in/W+0/S06.fwd
 RB2in/W+0/S06.mid
 RB2in/W+0/S06.bwd
 RB2in/W+0/S05.fwd
 RB2in/W+0/S05.mid
 RB2in/W+0/S05.bwd
 RB2in/W+0/S04.fwd
 RB2in/W+0/S04.mid
 RB2in/W+0/S04.bwd
 RB2in/W+0/S03.fwd
 RB2in/W+0/S03.mid
 RB2in/W+0/S03.bwd
 RB2in/W+0/S02.fwd
 RB2in/W+0/S02.mid
 RB2in/W+0/S02.bwd
 RB2in/W+0/S01.fwd
 RB2in/W+0/S01.mid
 RB2in/W+0/S01.bwd
 mismatched elsewhere
 unmatched

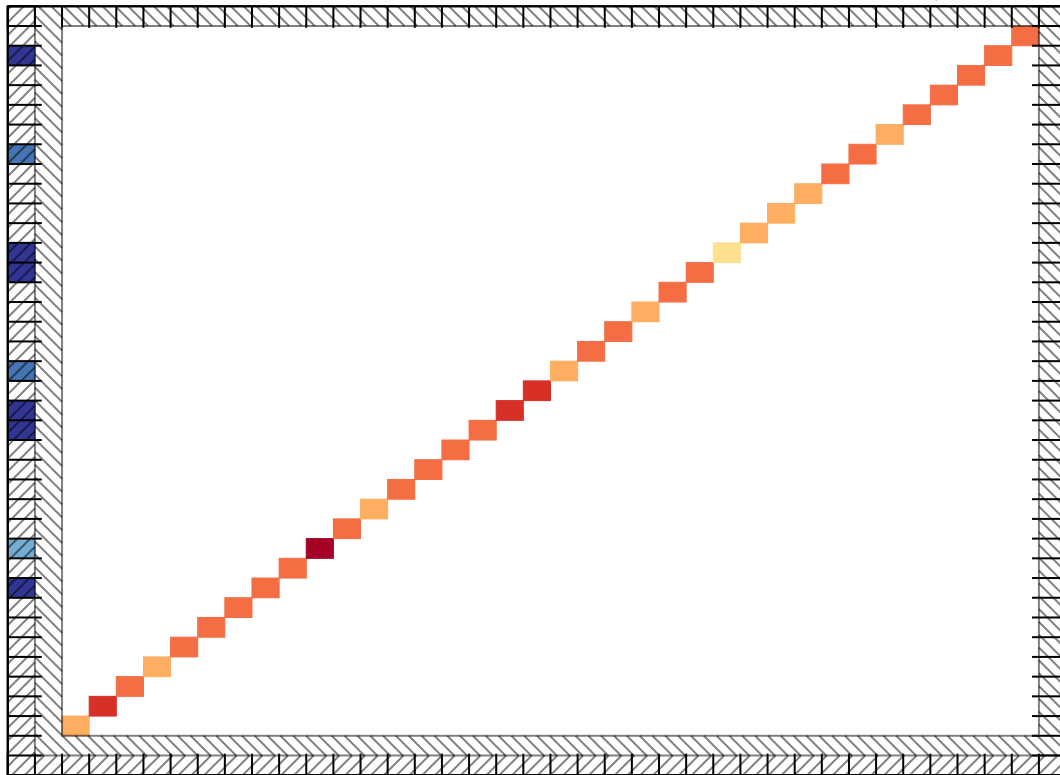
RPC Roll Legacy

RB2in/W+1

CMS Preliminary - run 306122

RPC Roll TwinMux

mismatched elsewhere
 RB2in/W+1/S12.fwd
 RB2in/W+1/S12.mid
 RB2in/W+1/S12.bwd
 RB2in/W+1/S11.fwd
 RB2in/W+1/S11.mid
 RB2in/W+1/S11.bwd
 RB2in/W+1/S10.fwd
 RB2in/W+1/S10.mid
 RB2in/W+1/S10.bwd
 RB2in/W+1/S09.fwd
 RB2in/W+1/S09.mid
 RB2in/W+1/S09.bwd
 RB2in/W+1/S08.fwd
 RB2in/W+1/S08.mid
 RB2in/W+1/S08.bwd
 RB2in/W+1/S07.fwd
 RB2in/W+1/S07.mid
 RB2in/W+1/S07.bwd
 RB2in/W+1/S06.fwd
 RB2in/W+1/S06.mid
 RB2in/W+1/S06.bwd
 RB2in/W+1/S05.fwd
 RB2in/W+1/S05.mid
 RB2in/W+1/S05.bwd
 RB2in/W+1/S04.fwd
 RB2in/W+1/S04.mid
 RB2in/W+1/S04.bwd
 RB2in/W+1/S03.fwd
 RB2in/W+1/S03.mid
 RB2in/W+1/S03.bwd
 RB2in/W+1/S02.fwd
 RB2in/W+1/S02.mid
 RB2in/W+1/S02.bwd
 RB2in/W+1/S01.fwd
 RB2in/W+1/S01.mid
 RB2in/W+1/S01.bwd
 mismatched elsewhere
 unmatched



Digs

10³

10²

10

1

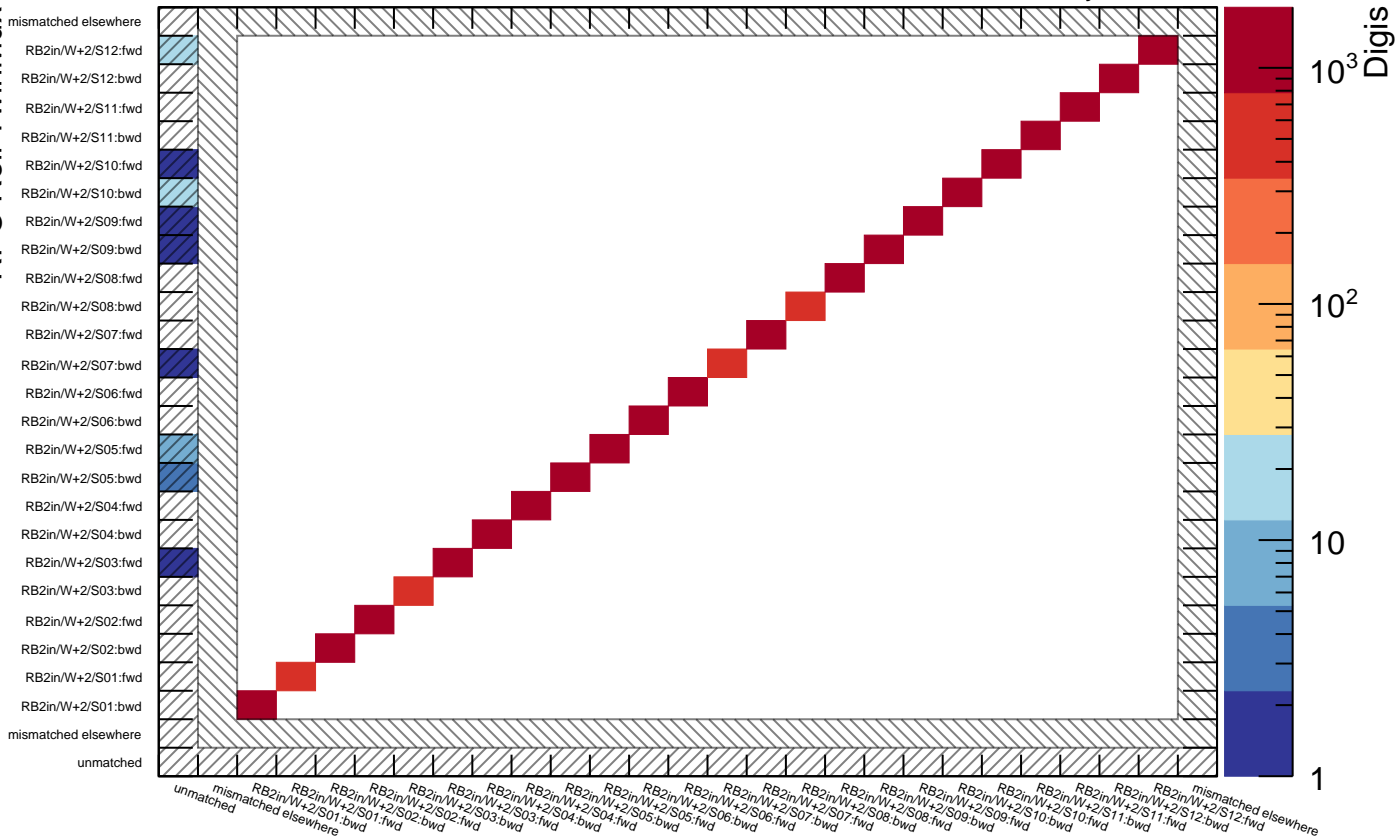
mismatched elsewhere
 unmatched
 RB2in/W+1/S12.fwd
 RB2in/W+1/S12.mid
 RB2in/W+1/S12.bwd
 RB2in/W+1/S11.fwd
 RB2in/W+1/S11.mid
 RB2in/W+1/S11.bwd
 RB2in/W+1/S10.fwd
 RB2in/W+1/S10.mid
 RB2in/W+1/S10.bwd
 RB2in/W+1/S09.fwd
 RB2in/W+1/S09.mid
 RB2in/W+1/S09.bwd
 RB2in/W+1/S08.fwd
 RB2in/W+1/S08.mid
 RB2in/W+1/S08.bwd
 RB2in/W+1/S07.fwd
 RB2in/W+1/S07.mid
 RB2in/W+1/S07.bwd
 RB2in/W+1/S06.fwd
 RB2in/W+1/S06.mid
 RB2in/W+1/S06.bwd
 RB2in/W+1/S05.fwd
 RB2in/W+1/S05.mid
 RB2in/W+1/S05.bwd
 RB2in/W+1/S04.fwd
 RB2in/W+1/S04.mid
 RB2in/W+1/S04.bwd
 RB2in/W+1/S03.fwd
 RB2in/W+1/S03.mid
 RB2in/W+1/S03.bwd
 RB2in/W+1/S02.fwd
 RB2in/W+1/S02.mid
 RB2in/W+1/S02.bwd
 RB2in/W+1/S01.fwd
 RB2in/W+1/S01.mid
 RB2in/W+1/S01.bwd
 mismatched elsewhere
 unmatched

RPC Roll Legacy

RPC Roll TwinMux

RB2in/W+2

CMS Preliminary - run 306122



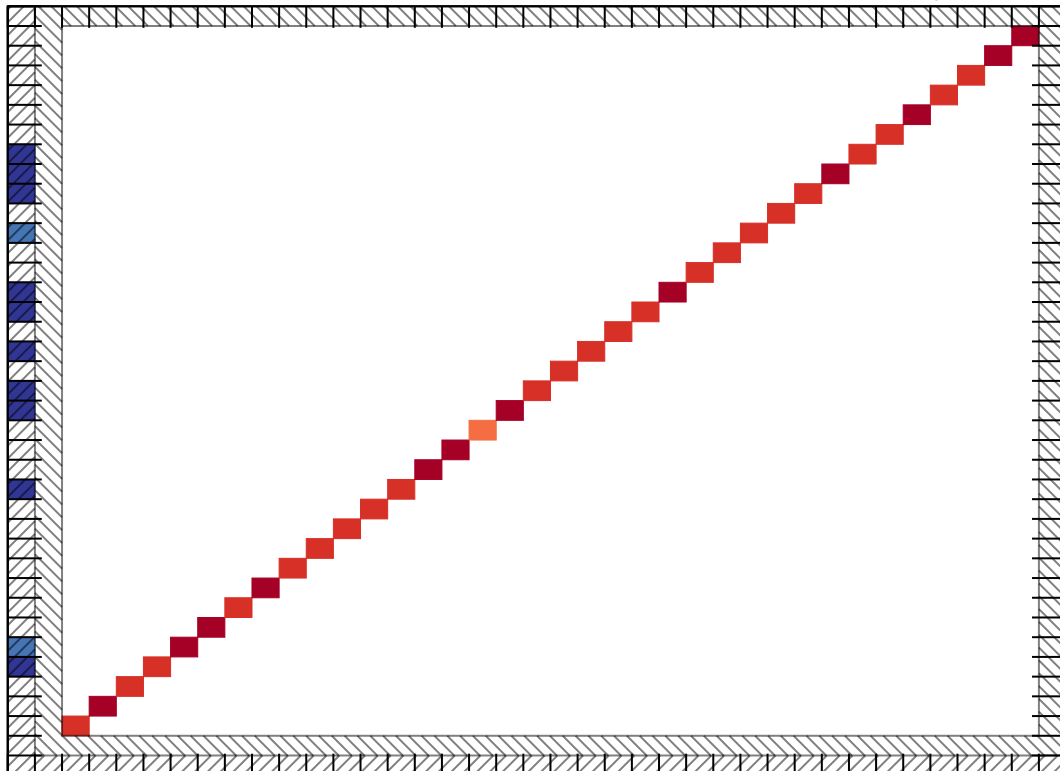
RPC Roll Legacy

RB2out/W-2

CMS Preliminary - run 306122

RPC Roll TwinMux

mismatched elsewhere
 RB2out/W-2/S12.fwd
 RB2out/W-2/S12.mid
 RB2out/W-2/S12.bwd
 RB2out/W-2/S11.fwd
 RB2out/W-2/S11.mid
 RB2out/W-2/S11.bwd
 RB2out/W-2/S10.fwd
 RB2out/W-2/S10.mid
 RB2out/W-2/S10.bwd
 RB2out/W-2/S09.fwd
 RB2out/W-2/S09.mid
 RB2out/W-2/S09.bwd
 RB2out/W-2/S08.fwd
 RB2out/W-2/S08.mid
 RB2out/W-2/S08.bwd
 RB2out/W-2/S07.fwd
 RB2out/W-2/S07.mid
 RB2out/W-2/S07.bwd
 RB2out/W-2/S06.fwd
 RB2out/W-2/S06.mid
 RB2out/W-2/S06.bwd
 RB2out/W-2/S05.fwd
 RB2out/W-2/S05.mid
 RB2out/W-2/S05.bwd
 RB2out/W-2/S04.fwd
 RB2out/W-2/S04.mid
 RB2out/W-2/S04.bwd
 RB2out/W-2/S03.fwd
 RB2out/W-2/S03.mid
 RB2out/W-2/S03.bwd
 RB2out/W-2/S02.fwd
 RB2out/W-2/S02.mid
 RB2out/W-2/S02.bwd
 RB2out/W-2/S01.fwd
 RB2out/W-2/S01.mid
 RB2out/W-2/S01.bwd
 mismatched elsewhere
 unmatched



10³
 10²
 10
 1

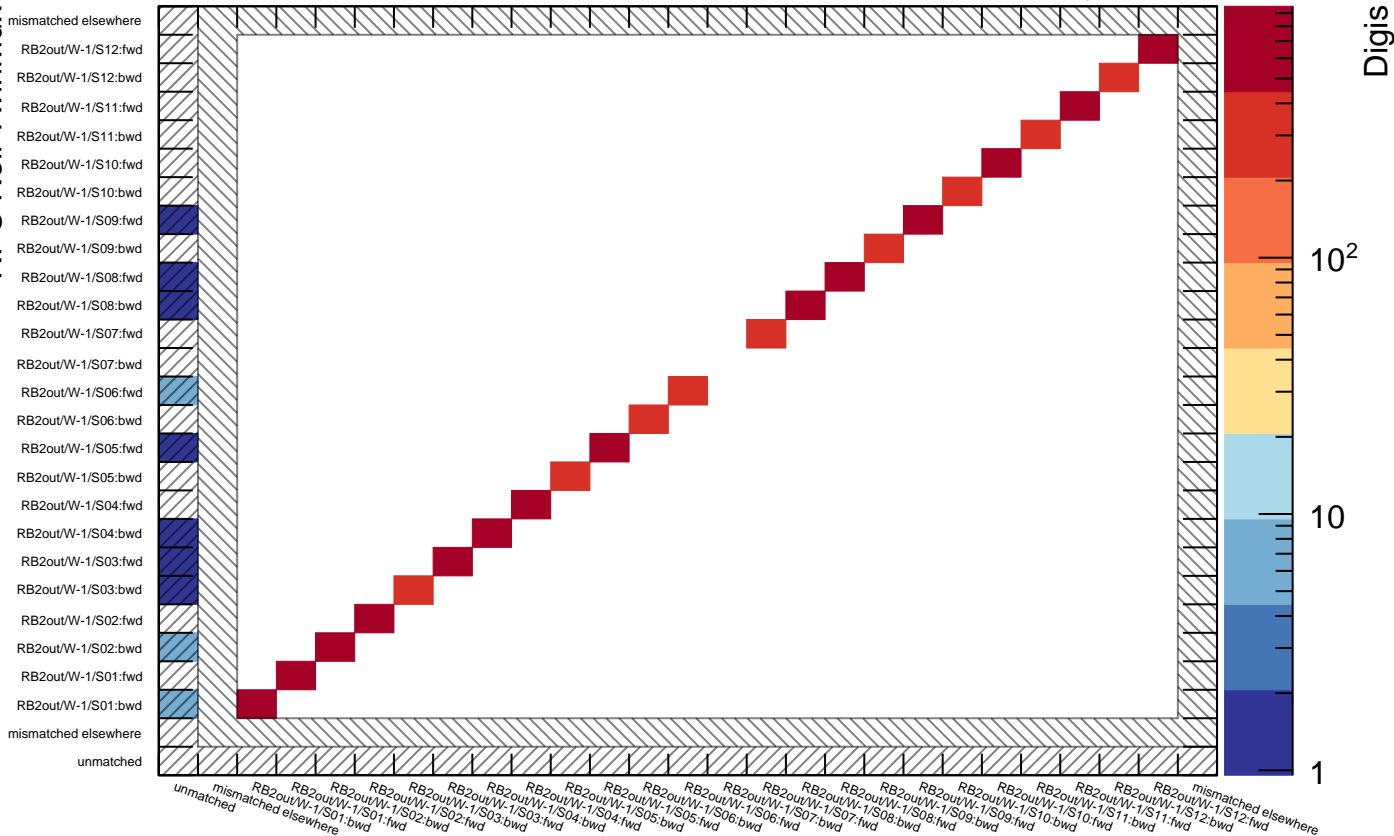
mismatched elsewhere
 unmatched
 RB2out/W-2/S12.fwd
 RB2out/W-2/S12.mid
 RB2out/W-2/S12.bwd
 RB2out/W-2/S11.fwd
 RB2out/W-2/S11.mid
 RB2out/W-2/S11.bwd
 RB2out/W-2/S10.fwd
 RB2out/W-2/S10.mid
 RB2out/W-2/S10.bwd
 RB2out/W-2/S09.fwd
 RB2out/W-2/S09.mid
 RB2out/W-2/S09.bwd
 RB2out/W-2/S08.fwd
 RB2out/W-2/S08.mid
 RB2out/W-2/S08.bwd
 RB2out/W-2/S07.fwd
 RB2out/W-2/S07.mid
 RB2out/W-2/S07.bwd
 RB2out/W-2/S06.fwd
 RB2out/W-2/S06.mid
 RB2out/W-2/S06.bwd
 RB2out/W-2/S05.fwd
 RB2out/W-2/S05.mid
 RB2out/W-2/S05.bwd
 RB2out/W-2/S04.fwd
 RB2out/W-2/S04.mid
 RB2out/W-2/S04.bwd
 RB2out/W-2/S03.fwd
 RB2out/W-2/S03.mid
 RB2out/W-2/S03.bwd
 RB2out/W-2/S02.fwd
 RB2out/W-2/S02.mid
 RB2out/W-2/S02.bwd
 RB2out/W-2/S01.fwd
 RB2out/W-2/S01.mid
 RB2out/W-2/S01.bwd
 mismatched elsewhere
 unmatched

RPC Roll Legacy

RB2out/W-1

CMS Preliminary - run 306122

RPC Roll TwinMux

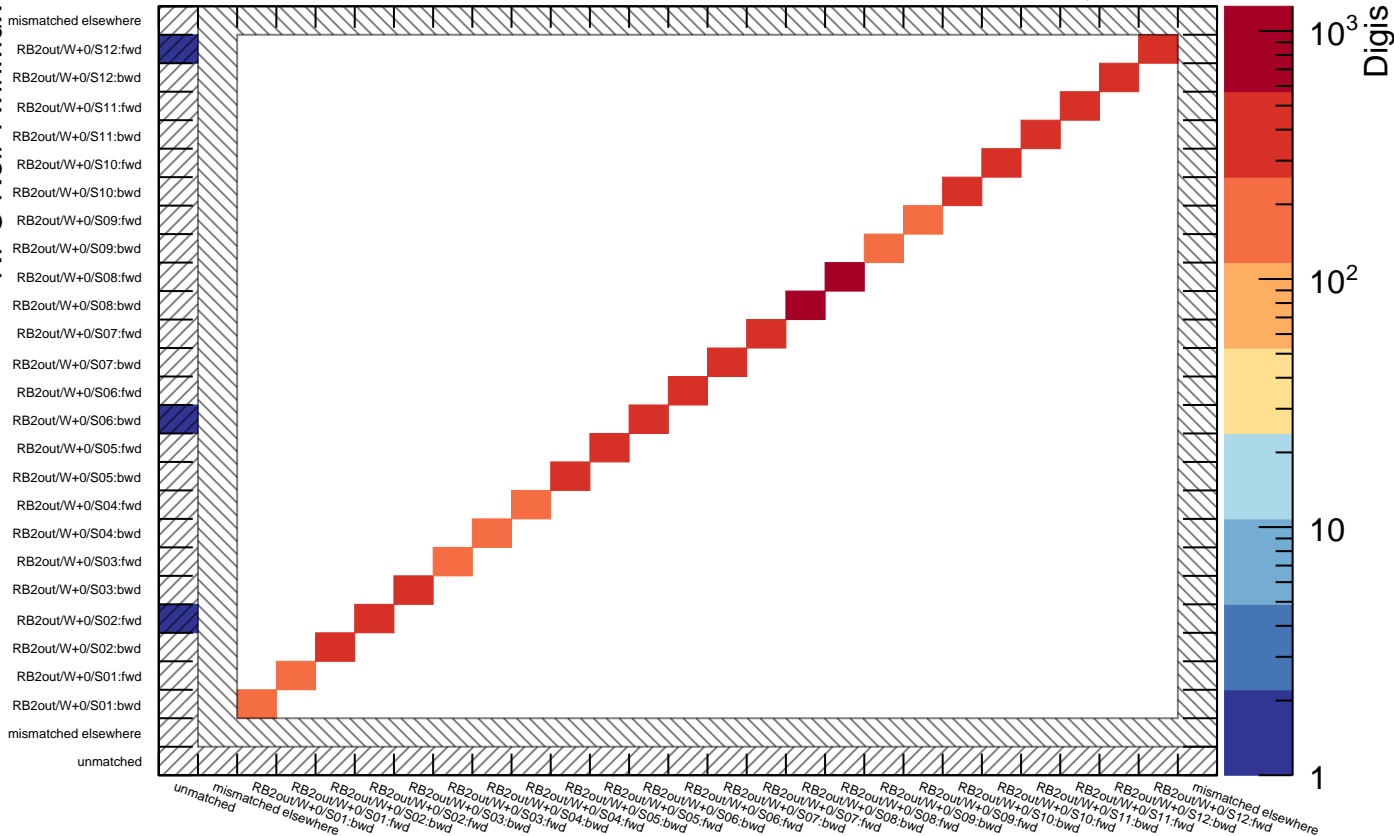


RPC Roll Legacy

RB2out/W+0

CMS Preliminary - run 306122

RPC Roll TwinMux

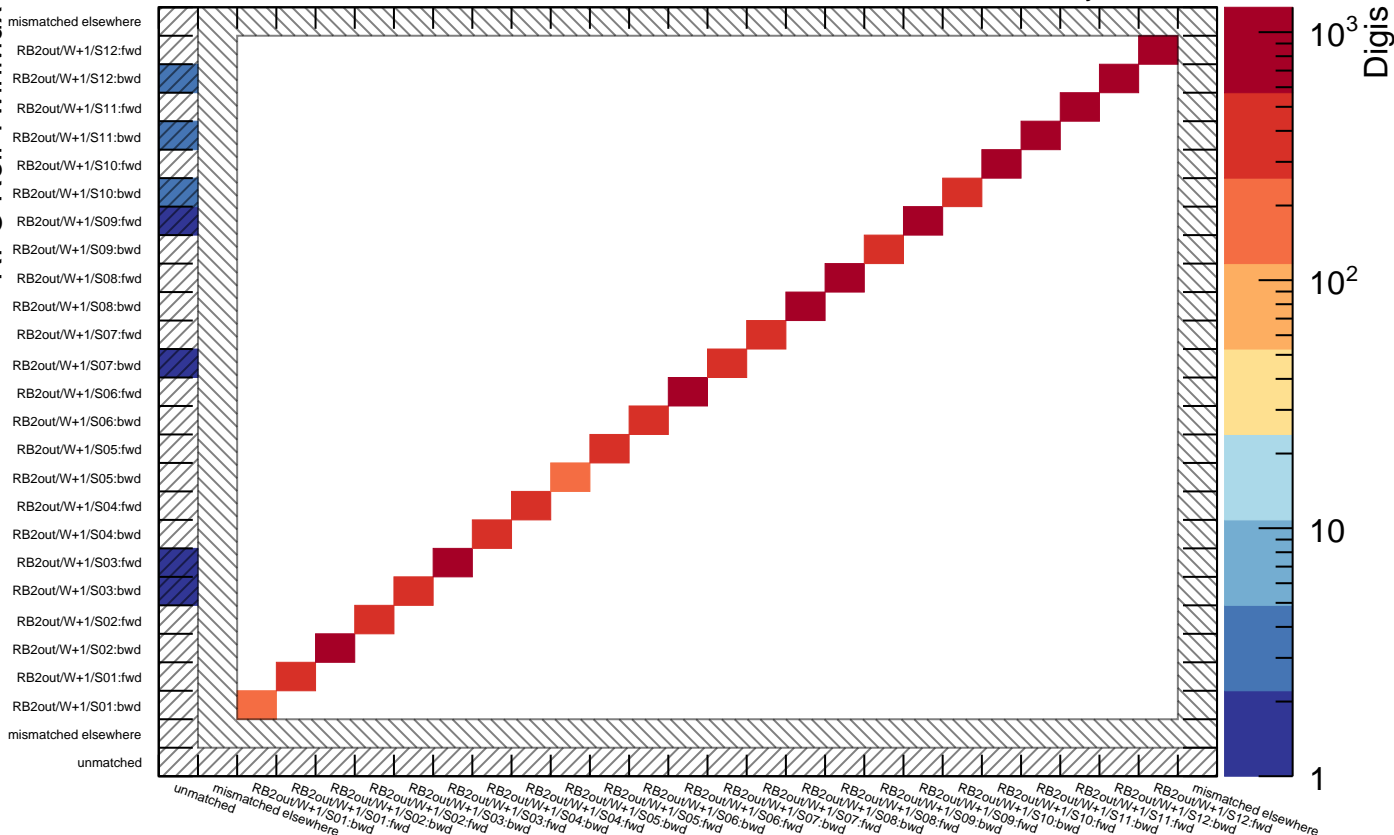


RPC Roll Legacy

RB2out/W+1

CMS Preliminary - run 306122

RPC Roll TwinMux



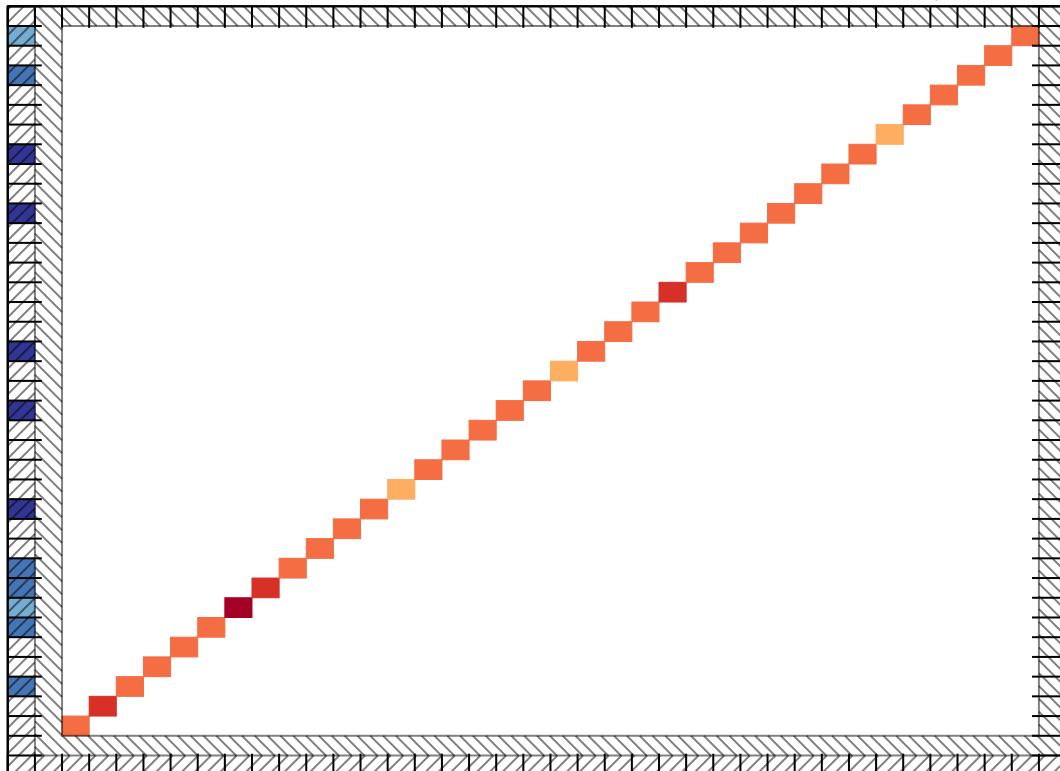
RPC Roll Legacy

RB2out/W+2

CMS Preliminary - run 306122

RPC Roll TwinMux

mismatched elsewhere
 RB2out/W+2/S12.fwd
 RB2out/W+2/S12.mid
 RB2out/W+2/S12.bwd
 RB2out/W+2/S11.fwd
 RB2out/W+2/S11.mid
 RB2out/W+2/S11.bwd
 RB2out/W+2/S10.fwd
 RB2out/W+2/S10.mid
 RB2out/W+2/S10.bwd
 RB2out/W+2/S09.fwd
 RB2out/W+2/S09.mid
 RB2out/W+2/S09.bwd
 RB2out/W+2/S08.fwd
 RB2out/W+2/S08.mid
 RB2out/W+2/S08.bwd
 RB2out/W+2/S07.fwd
 RB2out/W+2/S07.mid
 RB2out/W+2/S07.bwd
 RB2out/W+2/S06.fwd
 RB2out/W+2/S06.mid
 RB2out/W+2/S06.bwd
 RB2out/W+2/S05.fwd
 RB2out/W+2/S05.mid
 RB2out/W+2/S05.bwd
 RB2out/W+2/S04.fwd
 RB2out/W+2/S04.mid
 RB2out/W+2/S04.bwd
 RB2out/W+2/S03.fwd
 RB2out/W+2/S03.mid
 RB2out/W+2/S03.bwd
 RB2out/W+2/S02.fwd
 RB2out/W+2/S02.mid
 RB2out/W+2/S02.bwd
 RB2out/W+2/S01.fwd
 RB2out/W+2/S01.mid
 RB2out/W+2/S01.bwd
 mismatched elsewhere
 unmatched



Digits

10³

10²

10

1

mismatched elsewhere
 unmatched
 RB2out/W+2/S12.fwd
 RB2out/W+2/S12.mid
 RB2out/W+2/S12.bwd
 RB2out/W+2/S11.fwd
 RB2out/W+2/S11.mid
 RB2out/W+2/S11.bwd
 RB2out/W+2/S10.fwd
 RB2out/W+2/S10.mid
 RB2out/W+2/S10.bwd
 RB2out/W+2/S09.fwd
 RB2out/W+2/S09.mid
 RB2out/W+2/S09.bwd
 RB2out/W+2/S08.fwd
 RB2out/W+2/S08.mid
 RB2out/W+2/S08.bwd
 RB2out/W+2/S07.fwd
 RB2out/W+2/S07.mid
 RB2out/W+2/S07.bwd
 RB2out/W+2/S06.fwd
 RB2out/W+2/S06.mid
 RB2out/W+2/S06.bwd
 RB2out/W+2/S05.fwd
 RB2out/W+2/S05.mid
 RB2out/W+2/S05.bwd
 RB2out/W+2/S04.fwd
 RB2out/W+2/S04.mid
 RB2out/W+2/S04.bwd
 RB2out/W+2/S03.fwd
 RB2out/W+2/S03.mid
 RB2out/W+2/S03.bwd
 RB2out/W+2/S02.fwd
 RB2out/W+2/S02.mid
 RB2out/W+2/S02.bwd
 RB2out/W+2/S01.fwd
 RB2out/W+2/S01.mid
 RB2out/W+2/S01.bwd
 mismatched elsewhere
 unmatched

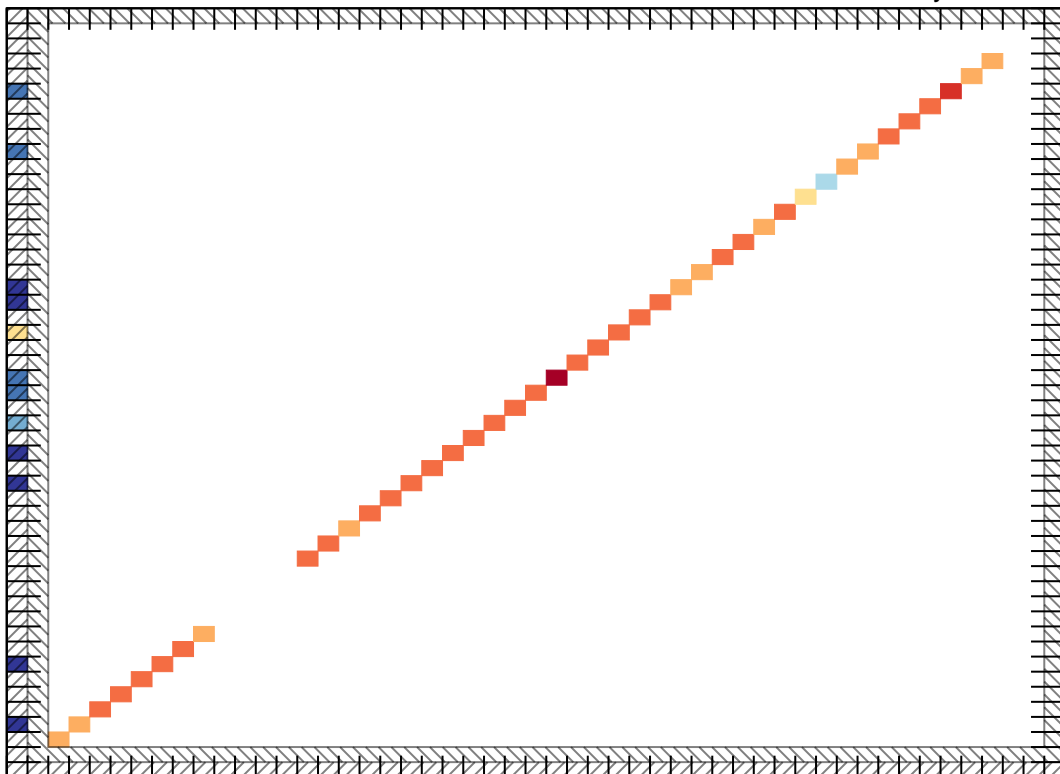
RPC Roll Legacy

RPC Roll TwinMux

RB3/W-2

CMS Preliminary - run 306122

mismatched elsewhere
 RB3/W-2/S12+ fwd
 RB3/W-2/S12+ bwd
 RB3/W-2/S12- fwd
 RB3/W-2/S12- bwd
 RB3/W-2/S11+ fwd
 RB3/W-2/S11+ bwd
 RB3/W-2/S11- fwd
 RB3/W-2/S11- bwd
 RB3/W-2/S10+ fwd
 RB3/W-2/S10+ bwd
 RB3/W-2/S10- fwd
 RB3/W-2/S10- bwd
 RB3/W-2/S09+ fwd
 RB3/W-2/S09+ bwd
 RB3/W-2/S09- fwd
 RB3/W-2/S09- bwd
 RB3/W-2/S08+ fwd
 RB3/W-2/S08+ bwd
 RB3/W-2/S08- fwd
 RB3/W-2/S08- bwd
 RB3/W-2/S07+ fwd
 RB3/W-2/S07+ bwd
 RB3/W-2/S07- fwd
 RB3/W-2/S07- bwd
 RB3/W-2/S06+ fwd
 RB3/W-2/S06+ bwd
 RB3/W-2/S06- fwd
 RB3/W-2/S06- bwd
 RB3/W-2/S05+ fwd
 RB3/W-2/S05+ bwd
 RB3/W-2/S05- fwd
 RB3/W-2/S05- bwd
 RB3/W-2/S04+ fwd
 RB3/W-2/S04+ bwd
 RB3/W-2/S04- fwd
 RB3/W-2/S04- bwd
 RB3/W-2/S03+ fwd
 RB3/W-2/S03+ bwd
 RB3/W-2/S03- fwd
 RB3/W-2/S03- bwd
 RB3/W-2/S02+ fwd
 RB3/W-2/S02+ bwd
 RB3/W-2/S02- fwd
 RB3/W-2/S02- bwd
 RB3/W-2/S01+ fwd
 RB3/W-2/S01+ bwd
 RB3/W-2/S01- fwd
 RB3/W-2/S01- bwd
 mismatched elsewhere
 unmatched



Digis

10²

10

1

unfulfilled elsewhere
 RB3/W-2/S12+ fwd
 RB3/W-2/S12+ bwd
 RB3/W-2/S12- fwd
 RB3/W-2/S12- bwd
 RB3/W-2/S11+ fwd
 RB3/W-2/S11+ bwd
 RB3/W-2/S11- fwd
 RB3/W-2/S11- bwd
 RB3/W-2/S10+ fwd
 RB3/W-2/S10+ bwd
 RB3/W-2/S10- fwd
 RB3/W-2/S10- bwd
 RB3/W-2/S09+ fwd
 RB3/W-2/S09+ bwd
 RB3/W-2/S09- fwd
 RB3/W-2/S09- bwd
 RB3/W-2/S08+ fwd
 RB3/W-2/S08+ bwd
 RB3/W-2/S08- fwd
 RB3/W-2/S08- bwd
 RB3/W-2/S07+ fwd
 RB3/W-2/S07+ bwd
 RB3/W-2/S07- fwd
 RB3/W-2/S07- bwd
 RB3/W-2/S06+ fwd
 RB3/W-2/S06+ bwd
 RB3/W-2/S06- fwd
 RB3/W-2/S06- bwd
 RB3/W-2/S05+ fwd
 RB3/W-2/S05+ bwd
 RB3/W-2/S05- fwd
 RB3/W-2/S05- bwd
 RB3/W-2/S04+ fwd
 RB3/W-2/S04+ bwd
 RB3/W-2/S04- fwd
 RB3/W-2/S04- bwd
 RB3/W-2/S03+ fwd
 RB3/W-2/S03+ bwd
 RB3/W-2/S03- fwd
 RB3/W-2/S03- bwd
 RB3/W-2/S02+ fwd
 RB3/W-2/S02+ bwd
 RB3/W-2/S02- fwd
 RB3/W-2/S02- bwd
 RB3/W-2/S01+ fwd
 RB3/W-2/S01+ bwd
 RB3/W-2/S01- fwd
 RB3/W-2/S01- bwd
 mismatched elsewhere

RPC Roll Legacy

RB3/W-1

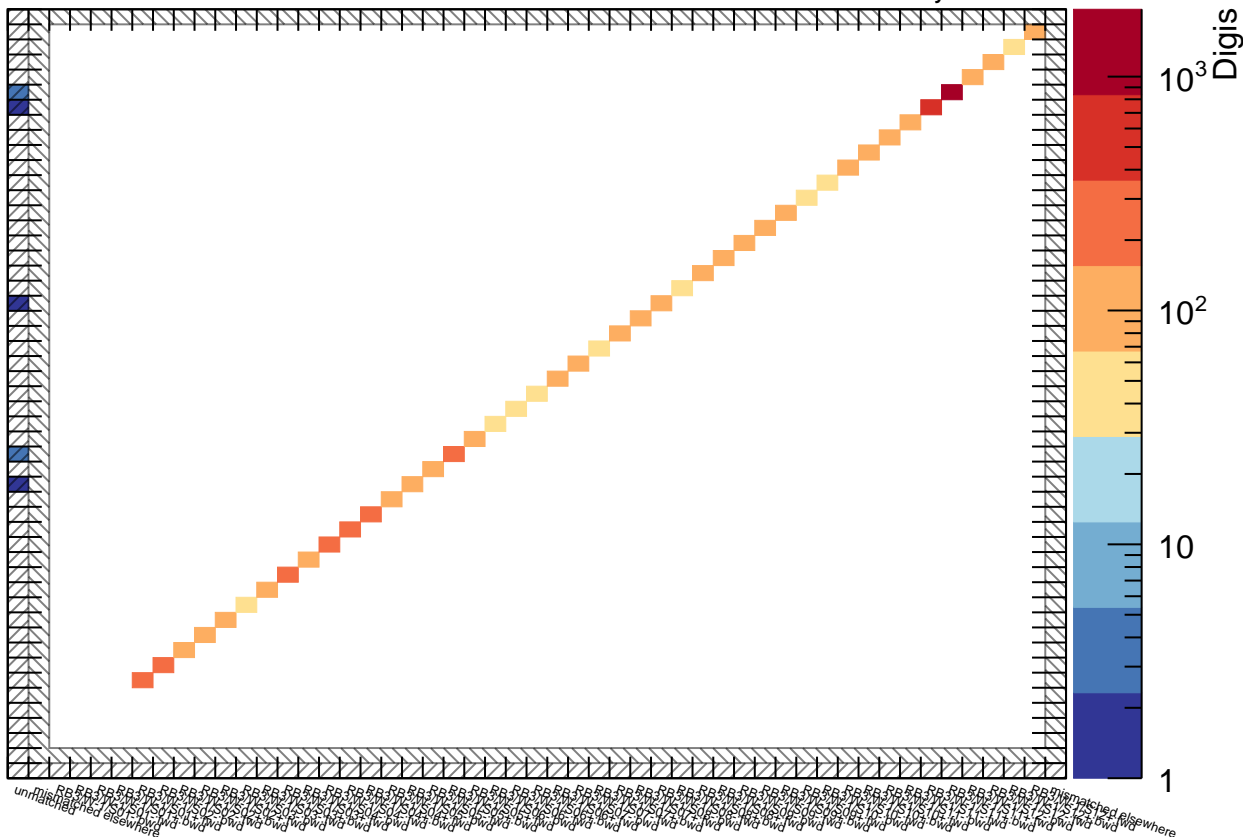
CMS Preliminary - run 306122

mismatched elsewhere

- RB3/W-1/S12+ fwd
- RB3/W-1/S12+ bwd
- RB3/W-1/S12- fwd
- RB3/W-1/S12- bwd
- RB3/W-1/S11+ fwd
- RB3/W-1/S11+ bwd
- RB3/W-1/S11- fwd
- RB3/W-1/S11- bwd
- RB3/W-1/S10+ fwd
- RB3/W-1/S10+ bwd
- RB3/W-1/S10- fwd
- RB3/W-1/S10- bwd
- RB3/W-1/S09+ fwd
- RB3/W-1/S09+ bwd
- RB3/W-1/S09- fwd
- RB3/W-1/S09- bwd
- RB3/W-1/S08+ fwd
- RB3/W-1/S08+ bwd
- RB3/W-1/S08- fwd
- RB3/W-1/S08- bwd
- RB3/W-1/S07+ fwd
- RB3/W-1/S07+ bwd
- RB3/W-1/S07- fwd
- RB3/W-1/S07- bwd
- RB3/W-1/S06+ fwd
- RB3/W-1/S06+ bwd
- RB3/W-1/S06- fwd
- RB3/W-1/S06- bwd
- RB3/W-1/S05+ fwd
- RB3/W-1/S05+ bwd
- RB3/W-1/S05- fwd
- RB3/W-1/S05- bwd
- RB3/W-1/S04+ fwd
- RB3/W-1/S04+ bwd
- RB3/W-1/S04- fwd
- RB3/W-1/S04- bwd
- RB3/W-1/S03+ fwd
- RB3/W-1/S03+ bwd
- RB3/W-1/S03- fwd
- RB3/W-1/S03- bwd
- RB3/W-1/S02+ fwd
- RB3/W-1/S02+ bwd
- RB3/W-1/S02- fwd
- RB3/W-1/S02- bwd
- RB3/W-1/S01+ fwd
- RB3/W-1/S01+ bwd
- RB3/W-1/S01- fwd
- RB3/W-1/S01- bwd

mismatched elsewhere

unmatched

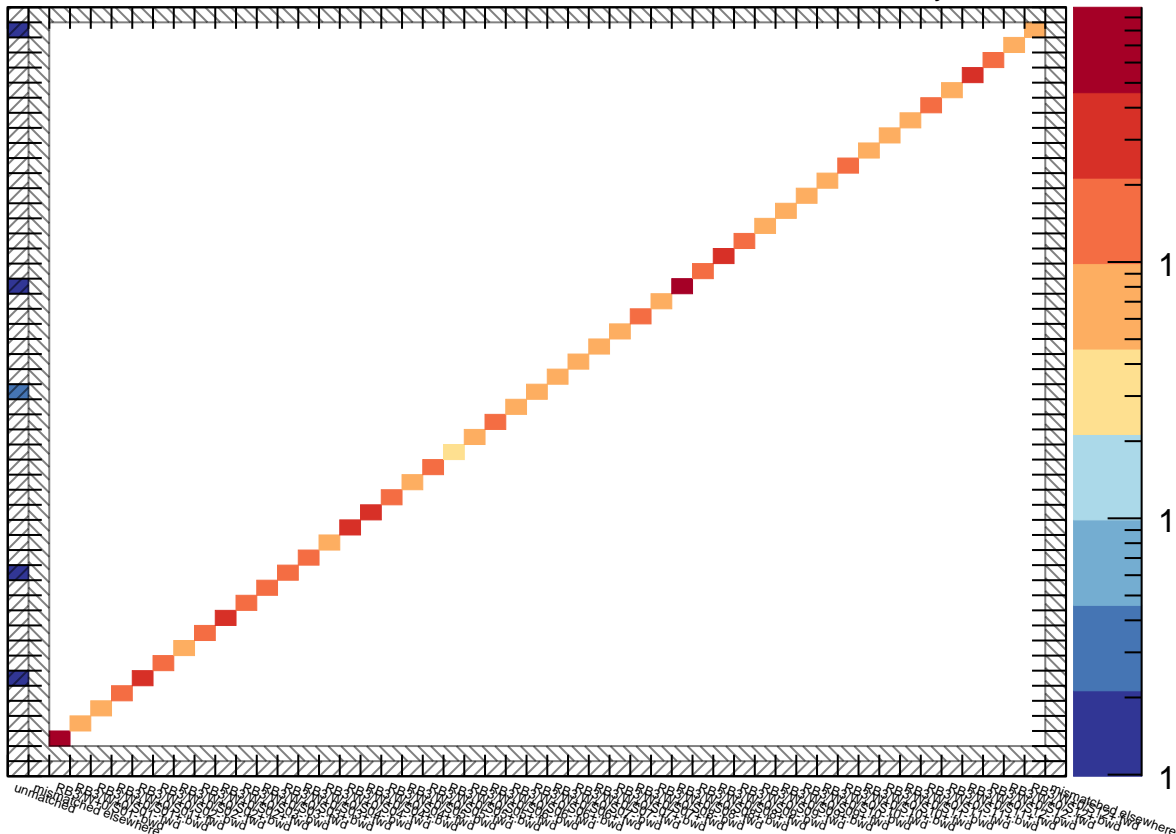


RB3/W+0

CMS Preliminary - run 306122

RPC Roll TwinMux

mismatched elsewhere
 RB3/W+0/S12+;fwd
 RB3/W+0/S12+;bwd
 RB3/W+0/S12-fwd
 RB3/W+0/S12-bwd
 RB3/W+0/S11+;fwd
 RB3/W+0/S11+;bwd
 RB3/W+0/S11-fwd
 RB3/W+0/S11-bwd
 RB3/W+0/S10+;fwd
 RB3/W+0/S10+;bwd
 RB3/W+0/S10-fwd
 RB3/W+0/S10-bwd
 RB3/W+0/S09+;fwd
 RB3/W+0/S09+;bwd
 RB3/W+0/S09-fwd
 RB3/W+0/S09-bwd
 RB3/W+0/S08+;fwd
 RB3/W+0/S08+;bwd
 RB3/W+0/S08-fwd
 RB3/W+0/S08-bwd
 RB3/W+0/S07+;fwd
 RB3/W+0/S07+;bwd
 RB3/W+0/S07-fwd
 RB3/W+0/S07-bwd
 RB3/W+0/S06+;fwd
 RB3/W+0/S06+;bwd
 RB3/W+0/S06-fwd
 RB3/W+0/S06-bwd
 RB3/W+0/S05+;fwd
 RB3/W+0/S05+;bwd
 RB3/W+0/S05-fwd
 RB3/W+0/S05-bwd
 RB3/W+0/S04+;fwd
 RB3/W+0/S04+;bwd
 RB3/W+0/S04-fwd
 RB3/W+0/S04-bwd
 RB3/W+0/S03+;fwd
 RB3/W+0/S03+;bwd
 RB3/W+0/S03-fwd
 RB3/W+0/S03-bwd
 RB3/W+0/S02+;fwd
 RB3/W+0/S02+;bwd
 RB3/W+0/S02-fwd
 RB3/W+0/S02-bwd
 RB3/W+0/S01+;fwd
 RB3/W+0/S01+;bwd
 RB3/W+0/S01-fwd
 RB3/W+0/S01-bwd
 mismatched elsewhere
 unmatched



Digis

10²

10

1

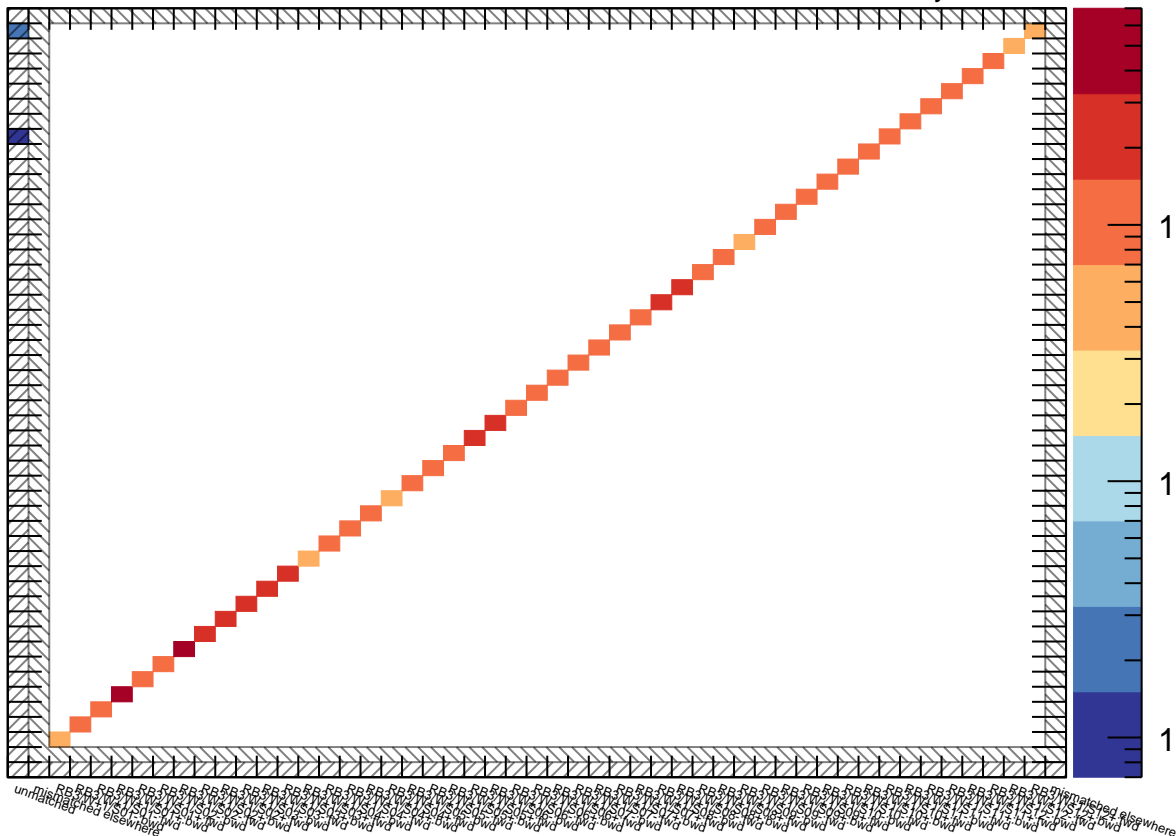
RPC Roll Legacy

RPC Roll TwinMux

RB3/W+1

CMS Preliminary - run 306122

mismatched elsewhere
 RB3/W+1/S12+;fwd
 RB3/W+1/S12+;bwd
 RB3/W+1/S12-fwd
 RB3/W+1/S12-bwd
 RB3/W+1/S11+;fwd
 RB3/W+1/S11+;bwd
 RB3/W+1/S11-fwd
 RB3/W+1/S11-bwd
 RB3/W+1/S10+;fwd
 RB3/W+1/S10+;bwd
 RB3/W+1/S10-fwd
 RB3/W+1/S10-bwd
 RB3/W+1/S09+;fwd
 RB3/W+1/S09+;bwd
 RB3/W+1/S09-fwd
 RB3/W+1/S09-bwd
 RB3/W+1/S08+;fwd
 RB3/W+1/S08+;bwd
 RB3/W+1/S08-fwd
 RB3/W+1/S08-bwd
 RB3/W+1/S07+;fwd
 RB3/W+1/S07+;bwd
 RB3/W+1/S07-fwd
 RB3/W+1/S07-bwd
 RB3/W+1/S06+;fwd
 RB3/W+1/S06+;bwd
 RB3/W+1/S06-fwd
 RB3/W+1/S06-bwd
 RB3/W+1/S05+;fwd
 RB3/W+1/S05+;bwd
 RB3/W+1/S05-fwd
 RB3/W+1/S05-bwd
 RB3/W+1/S04+;fwd
 RB3/W+1/S04+;bwd
 RB3/W+1/S04-fwd
 RB3/W+1/S04-bwd
 RB3/W+1/S03+;fwd
 RB3/W+1/S03+;bwd
 RB3/W+1/S03-fwd
 RB3/W+1/S03-bwd
 RB3/W+1/S02+;fwd
 RB3/W+1/S02+;bwd
 RB3/W+1/S02-fwd
 RB3/W+1/S02-bwd
 RB3/W+1/S01+;fwd
 RB3/W+1/S01+;bwd
 RB3/W+1/S01-fwd
 RB3/W+1/S01-bwd
 mismatched elsewhere
 unmatched



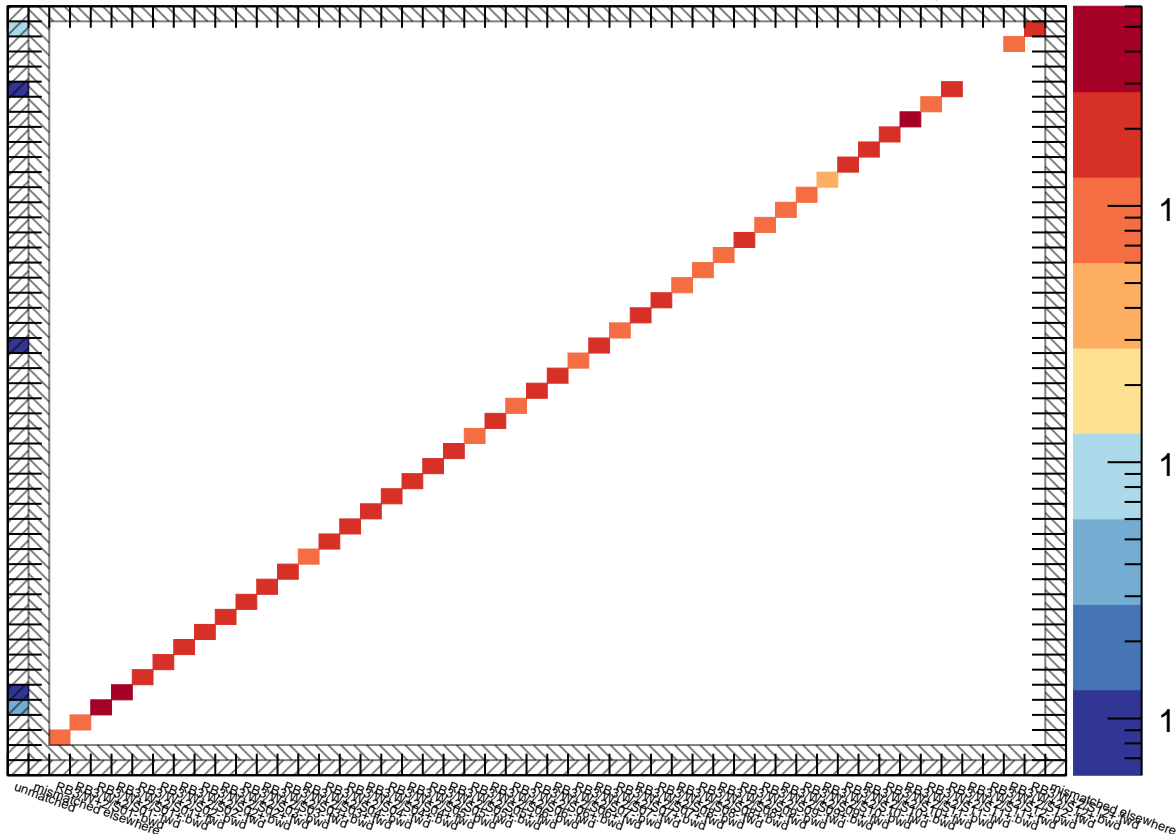
RPC Roll Legacy

RPC Roll TwinMux

RB3/W+2

CMS Preliminary - run 306122

mismatched elsewhere
 RB3/W+2/S12+;fwd
 RB3/W+2/S12+;bwd
 RB3/W+2/S12-;fwd
 RB3/W+2/S12-;bwd
 RB3/W+2/S11+;fwd
 RB3/W+2/S11+;bwd
 RB3/W+2/S11-;fwd
 RB3/W+2/S11-;bwd
 RB3/W+2/S10+;fwd
 RB3/W+2/S10+;bwd
 RB3/W+2/S10-;fwd
 RB3/W+2/S10-;bwd
 RB3/W+2/S09+;fwd
 RB3/W+2/S09+;bwd
 RB3/W+2/S09-;fwd
 RB3/W+2/S09-;bwd
 RB3/W+2/S08+;fwd
 RB3/W+2/S08+;bwd
 RB3/W+2/S08-;fwd
 RB3/W+2/S08-;bwd
 RB3/W+2/S07+;fwd
 RB3/W+2/S07+;bwd
 RB3/W+2/S07-;fwd
 RB3/W+2/S07-;bwd
 RB3/W+2/S06+;fwd
 RB3/W+2/S06+;bwd
 RB3/W+2/S06-;fwd
 RB3/W+2/S06-;bwd
 RB3/W+2/S05+;fwd
 RB3/W+2/S05+;bwd
 RB3/W+2/S05-;fwd
 RB3/W+2/S05-;bwd
 RB3/W+2/S04+;fwd
 RB3/W+2/S04+;bwd
 RB3/W+2/S04-;fwd
 RB3/W+2/S04-;bwd
 RB3/W+2/S03+;fwd
 RB3/W+2/S03+;bwd
 RB3/W+2/S03-;fwd
 RB3/W+2/S03-;bwd
 RB3/W+2/S02+;fwd
 RB3/W+2/S02+;bwd
 RB3/W+2/S02-;fwd
 RB3/W+2/S02-;bwd
 RB3/W+2/S01+;fwd
 RB3/W+2/S01+;bwd
 RB3/W+2/S01-;fwd
 RB3/W+2/S01-;bwd
 mismatched elsewhere
 unmatched



Digis

10^2

10

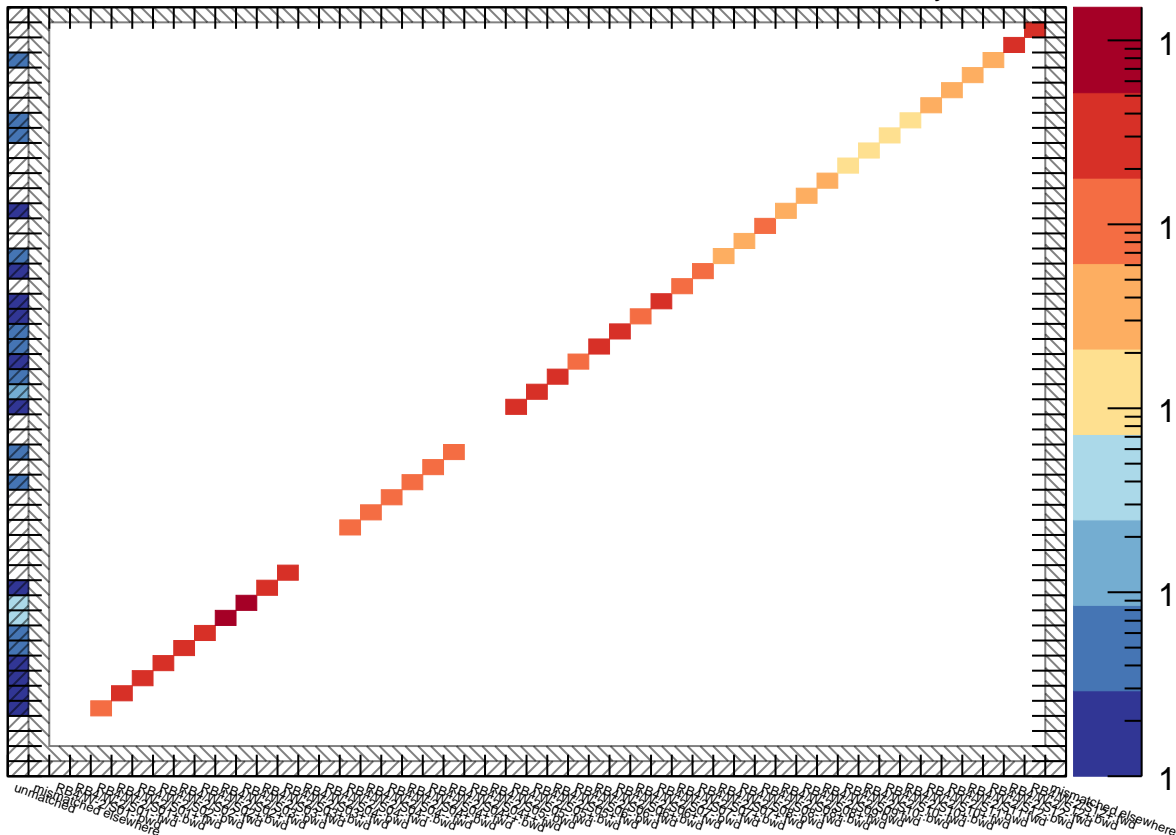
1

RPC Roll Legacy

RB4/W-2

CMS Preliminary - run 306122

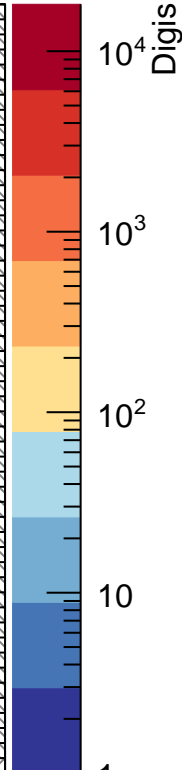
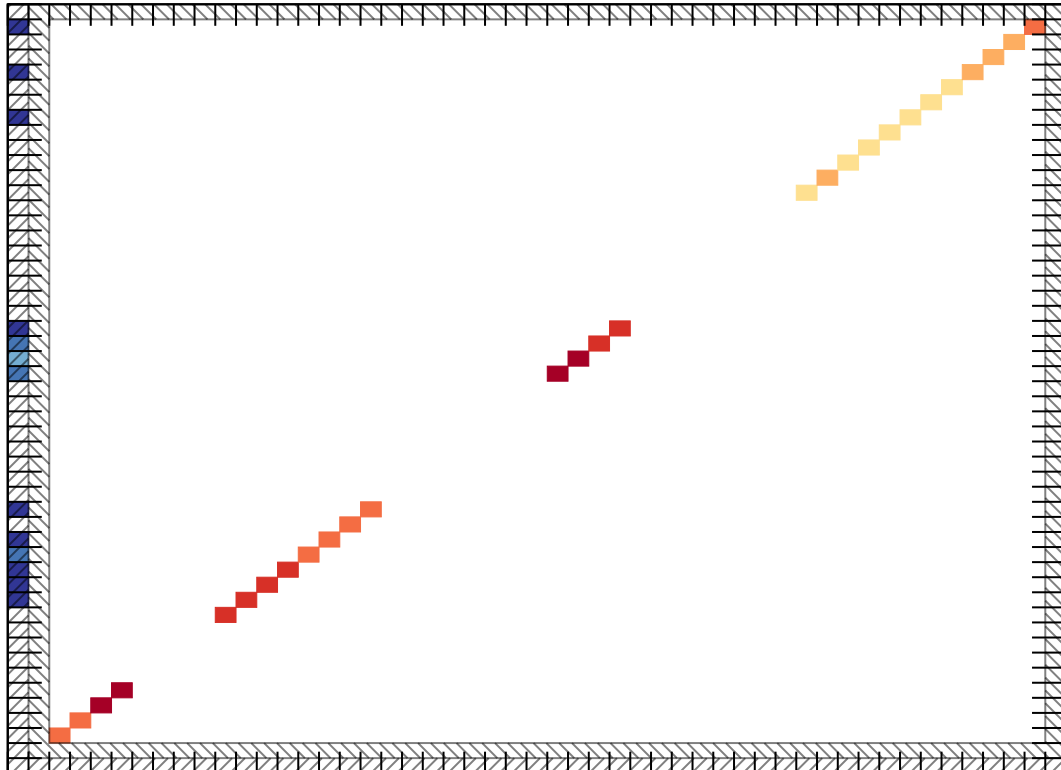
mismatched elsewhere
 RB4/W-2/S12+.fwd
 RB4/W-2/S12+.bwd
 RB4/W-2/S12-.fwd
 RB4/W-2/S12-.bwd
 RB4/W-2/S11.fwd
 RB4/W-2/S11.bwd
 RB4/W-2/S10+.fwd
 RB4/W-2/S10+.bwd
 RB4/W-2/S10-.fwd
 RB4/W-2/S10-.bwd
 RB4/W-2/S09.fwd
 RB4/W-2/S09.bwd
 RB4/W-2/S08+.fwd
 RB4/W-2/S08+.bwd
 RB4/W-2/S08-.fwd
 RB4/W-2/S08-.bwd
 RB4/W-2/S07+.fwd
 RB4/W-2/S07+.bwd
 RB4/W-2/S07-.fwd
 RB4/W-2/S07-.bwd
 RB4/W-2/S06+.fwd
 RB4/W-2/S06+.bwd
 RB4/W-2/S06-.fwd
 RB4/W-2/S06-.bwd
 RB4/W-2/S05+.fwd
 RB4/W-2/S05+.bwd
 RB4/W-2/S05-.fwd
 RB4/W-2/S05-.bwd
 RB4/W-2/S04++.fwd
 RB4/W-2/S04++.bwd
 RB4/W-2/S04+.fwd
 RB4/W-2/S04+.bwd
 RB4/W-2/S04-.fwd
 RB4/W-2/S04-.bwd
 RB4/W-2/S04-.fwd
 RB4/W-2/S04-.bwd
 RB4/W-2/S03+.fwd
 RB4/W-2/S03+.bwd
 RB4/W-2/S03-.fwd
 RB4/W-2/S03-.bwd
 RB4/W-2/S02+.fwd
 RB4/W-2/S02+.bwd
 RB4/W-2/S02-.fwd
 RB4/W-2/S02-.bwd
 RB4/W-2/S01+.fwd
 RB4/W-2/S01+.bwd
 RB4/W-2/S01-.fwd
 RB4/W-2/S01-.bwd
 mismatched elsewhere
 unmatched



RB4/W-1

CMS Preliminary - run 306122

mismatched elsewhere
 RB4/W-1/S12+ fwd
 RB4/W-1/S12+ bwd
 RB4/W-1/S12- fwd
 RB4/W-1/S12- bwd
 RB4/W-1/S11 fwd
 RB4/W-1/S11 bwd
 RB4/W-1/S10+ fwd
 RB4/W-1/S10+ bwd
 RB4/W-1/S10- fwd
 RB4/W-1/S10- bwd
 RB4/W-1/S09 fwd
 RB4/W-1/S09 bwd
 RB4/W-1/S08+ fwd
 RB4/W-1/S08+ bwd
 RB4/W-1/S08- fwd
 RB4/W-1/S08- bwd
 RB4/W-1/S07+ fwd
 RB4/W-1/S07+ bwd
 RB4/W-1/S07- fwd
 RB4/W-1/S07- bwd
 RB4/W-1/S06+ fwd
 RB4/W-1/S06+ bwd
 RB4/W-1/S06- fwd
 RB4/W-1/S06- bwd
 RB4/W-1/S05+ fwd
 RB4/W-1/S05+ bwd
 RB4/W-1/S05- fwd
 RB4/W-1/S05- bwd
 RB4/W-1/S04++ fwd
 RB4/W-1/S04++ bwd
 RB4/W-1/S04+ fwd
 RB4/W-1/S04+ bwd
 RB4/W-1/S04- fwd
 RB4/W-1/S04- bwd
 RB4/W-1/S04- fwd
 RB4/W-1/S04- bwd
 RB4/W-1/S03+ fwd
 RB4/W-1/S03+ bwd
 RB4/W-1/S03- fwd
 RB4/W-1/S03- bwd
 RB4/W-1/S02+ fwd
 RB4/W-1/S02+ bwd
 RB4/W-1/S02- fwd
 RB4/W-1/S02- bwd
 RB4/W-1/S01+ fwd
 RB4/W-1/S01+ bwd
 RB4/W-1/S01- fwd
 RB4/W-1/S01- bwd
 mismatched elsewhere
 unmatched



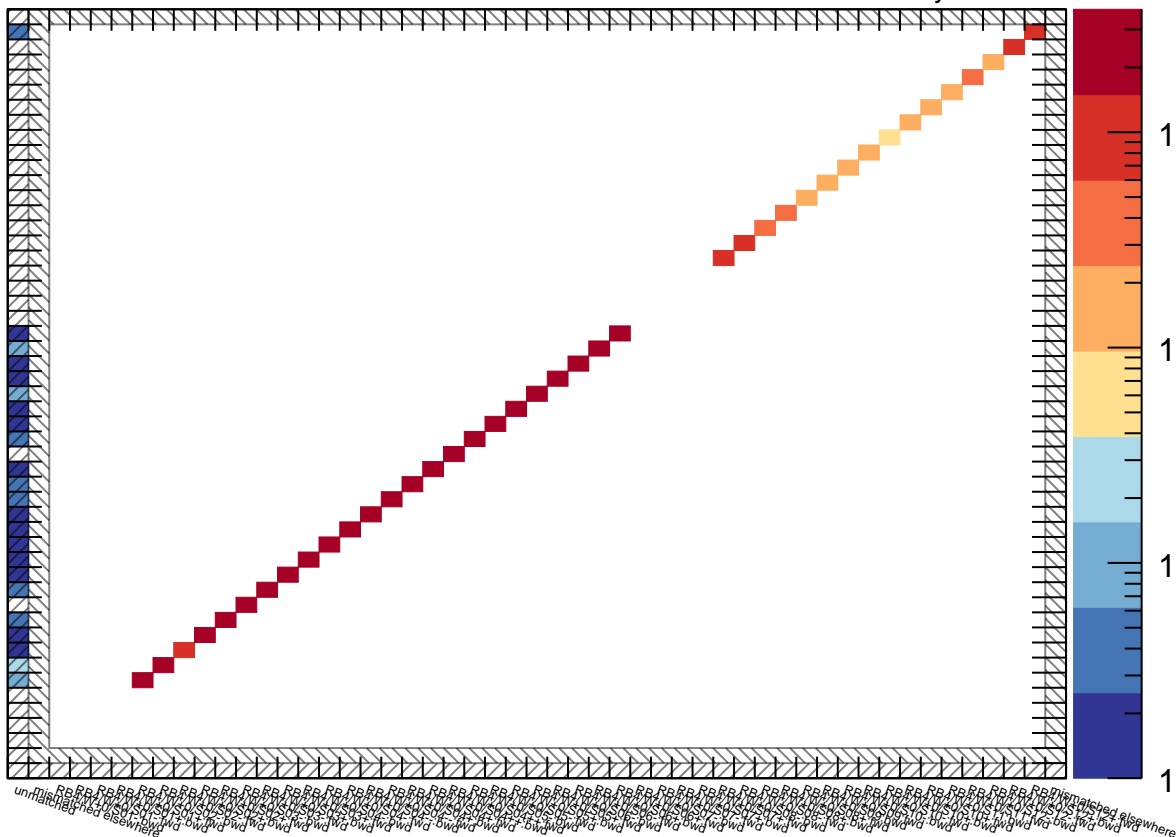
unfulfilled elsewhere
 RB4/W-1/S12+ fwd
 RB4/W-1/S12+ bwd
 RB4/W-1/S12- fwd
 RB4/W-1/S12- bwd
 RB4/W-1/S11 fwd
 RB4/W-1/S11 bwd
 RB4/W-1/S10+ fwd
 RB4/W-1/S10+ bwd
 RB4/W-1/S10- fwd
 RB4/W-1/S10- bwd
 RB4/W-1/S09 fwd
 RB4/W-1/S09 bwd
 RB4/W-1/S08+ fwd
 RB4/W-1/S08+ bwd
 RB4/W-1/S08- fwd
 RB4/W-1/S08- bwd
 RB4/W-1/S07+ fwd
 RB4/W-1/S07+ bwd
 RB4/W-1/S07- fwd
 RB4/W-1/S07- bwd
 RB4/W-1/S06+ fwd
 RB4/W-1/S06+ bwd
 RB4/W-1/S06- fwd
 RB4/W-1/S06- bwd
 RB4/W-1/S05+ fwd
 RB4/W-1/S05+ bwd
 RB4/W-1/S05- fwd
 RB4/W-1/S05- bwd
 RB4/W-1/S04++ fwd
 RB4/W-1/S04++ bwd
 RB4/W-1/S04+ fwd
 RB4/W-1/S04+ bwd
 RB4/W-1/S04- fwd
 RB4/W-1/S04- bwd
 RB4/W-1/S04- fwd
 RB4/W-1/S04- bwd
 RB4/W-1/S03+ fwd
 RB4/W-1/S03+ bwd
 RB4/W-1/S03- fwd
 RB4/W-1/S03- bwd
 RB4/W-1/S02+ fwd
 RB4/W-1/S02+ bwd
 RB4/W-1/S02- fwd
 RB4/W-1/S02- bwd
 RB4/W-1/S01+ fwd
 RB4/W-1/S01+ bwd
 RB4/W-1/S01- fwd
 RB4/W-1/S01- bwd
 mismatched elsewhere
 unmatched

RPC Roll TwinMux

RB4/W+0

CMS Preliminary - run 306122

mismatched elsewhere
 RB4/W+0/S12+.fwd
 RB4/W+0/S12+.bwd
 RB4/W+0/S12-.fwd
 RB4/W+0/S12-.bwd
 RB4/W+0/S11.fwd
 RB4/W+0/S11.bwd
 RB4/W+0/S10+.fwd
 RB4/W+0/S10+.bwd
 RB4/W+0/S10-.fwd
 RB4/W+0/S10-.bwd
 RB4/W+0/S09.fwd
 RB4/W+0/S09.bwd
 RB4/W+0/S08+.fwd
 RB4/W+0/S08+.bwd
 RB4/W+0/S08-.fwd
 RB4/W+0/S08-.bwd
 RB4/W+0/S07+.fwd
 RB4/W+0/S07+.bwd
 RB4/W+0/S07-.fwd
 RB4/W+0/S07-.bwd
 RB4/W+0/S06+.fwd
 RB4/W+0/S06+.bwd
 RB4/W+0/S06-.fwd
 RB4/W+0/S06-.bwd
 RB4/W+0/S05+.fwd
 RB4/W+0/S05+.bwd
 RB4/W+0/S05-.fwd
 RB4/W+0/S05-.bwd
 RB4/W+0/S04+.fwd
 RB4/W+0/S04+.bwd
 RB4/W+0/S04-.fwd
 RB4/W+0/S04-.bwd
 RB4/W+0/S03+.fwd
 RB4/W+0/S03+.bwd
 RB4/W+0/S03-.fwd
 RB4/W+0/S03-.bwd
 RB4/W+0/S02+.fwd
 RB4/W+0/S02+.bwd
 RB4/W+0/S02-.fwd
 RB4/W+0/S02-.bwd
 RB4/W+0/S01+.fwd
 RB4/W+0/S01+.bwd
 RB4/W+0/S01-.fwd
 RB4/W+0/S01-.bwd
 mismatched elsewhere
 unmatched



RPC Roll Legacy

RB4/W+1

CMS Preliminary - run 306122

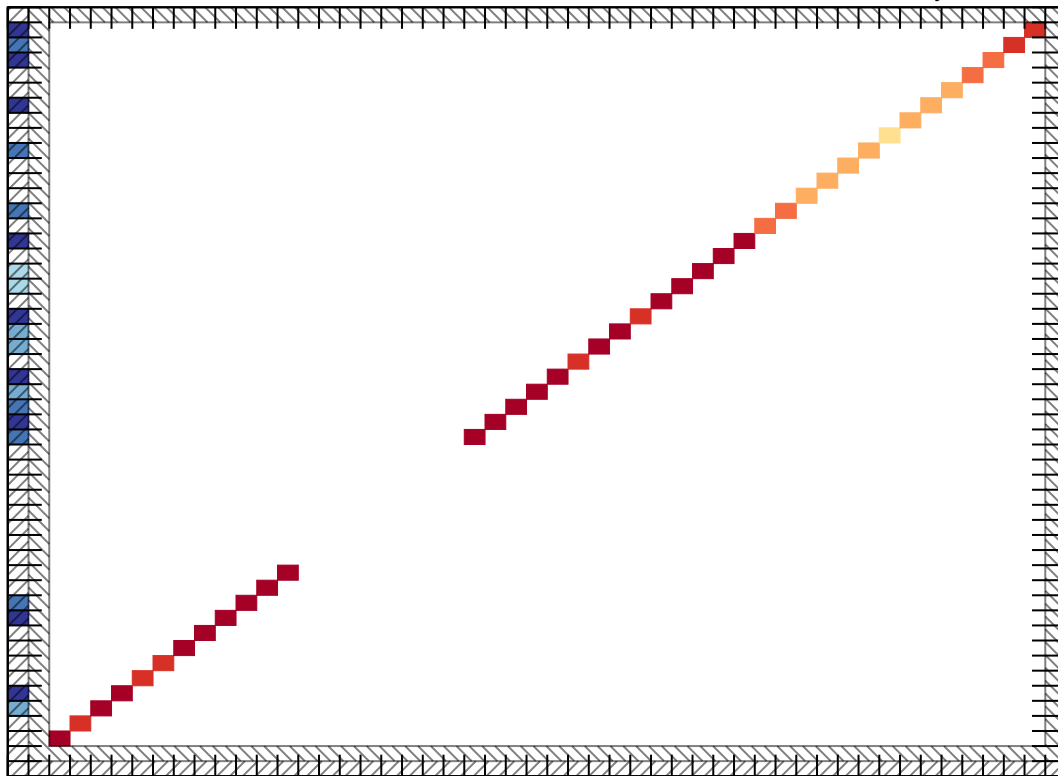
RPC Roll TwinMux

mismatched elsewhere

RB4/W+1/S12+-fwd
RB4/W+1/S12+-bwd
RB4/W+1/S12--fwd
RB4/W+1/S11-fwd
RB4/W+1/S11-bwd
RB4/W+1/S10+-fwd
RB4/W+1/S10+-bwd
RB4/W+1/S10--fwd
RB4/W+1/S09-fwd
RB4/W+1/S09-bwd
RB4/W+1/S08+-fwd
RB4/W+1/S08+-bwd
RB4/W+1/S08--fwd
RB4/W+1/S07+-fwd
RB4/W+1/S07+-bwd
RB4/W+1/S07--fwd
RB4/W+1/S06+-fwd
RB4/W+1/S06+-bwd
RB4/W+1/S06--fwd
RB4/W+1/S05+-fwd
RB4/W+1/S05+-bwd
RB4/W+1/S05--fwd
RB4/W+1/S04+-fwd
RB4/W+1/S04+-bwd
RB4/W+1/S04--fwd
RB4/W+1/S04--bwd
RB4/W+1/S03+-fwd
RB4/W+1/S03+-bwd
RB4/W+1/S03--fwd
RB4/W+1/S02+-fwd
RB4/W+1/S02+-bwd
RB4/W+1/S02--fwd
RB4/W+1/S01+-fwd
RB4/W+1/S01+-bwd
RB4/W+1/S01--fwd

mismatched elsewhere

unmatched



Digis

10^3

10^2

10

1

unfulfilled elsewhere
RB4/W+1/S12+-fwd
RB4/W+1/S12+-bwd
RB4/W+1/S12--fwd
RB4/W+1/S11-fwd
RB4/W+1/S11-bwd
RB4/W+1/S10+-fwd
RB4/W+1/S10+-bwd
RB4/W+1/S10--fwd
RB4/W+1/S09-fwd
RB4/W+1/S09-bwd
RB4/W+1/S08+-fwd
RB4/W+1/S08+-bwd
RB4/W+1/S08--fwd
RB4/W+1/S07+-fwd
RB4/W+1/S07+-bwd
RB4/W+1/S07--fwd
RB4/W+1/S06+-fwd
RB4/W+1/S06+-bwd
RB4/W+1/S06--fwd
RB4/W+1/S05+-fwd
RB4/W+1/S05+-bwd
RB4/W+1/S05--fwd
RB4/W+1/S04+-fwd
RB4/W+1/S04+-bwd
RB4/W+1/S04--fwd
RB4/W+1/S04--bwd
RB4/W+1/S03+-fwd
RB4/W+1/S03+-bwd
RB4/W+1/S03--fwd
RB4/W+1/S02+-fwd
RB4/W+1/S02+-bwd
RB4/W+1/S02--fwd
RB4/W+1/S01+-fwd
RB4/W+1/S01+-bwd
RB4/W+1/S01--fwd
unfulfilled elsewhere
RB4/W+1/S12+-fwd
RB4/W+1/S12+-bwd
RB4/W+1/S12--fwd
RB4/W+1/S11-fwd
RB4/W+1/S11-bwd
RB4/W+1/S10+-fwd
RB4/W+1/S10+-bwd
RB4/W+1/S10--fwd
RB4/W+1/S09-fwd
RB4/W+1/S09-bwd
RB4/W+1/S08+-fwd
RB4/W+1/S08+-bwd
RB4/W+1/S08--fwd
RB4/W+1/S07+-fwd
RB4/W+1/S07+-bwd
RB4/W+1/S07--fwd
RB4/W+1/S06+-fwd
RB4/W+1/S06+-bwd
RB4/W+1/S06--fwd
RB4/W+1/S05+-fwd
RB4/W+1/S05+-bwd
RB4/W+1/S05--fwd
RB4/W+1/S04+-fwd
RB4/W+1/S04+-bwd
RB4/W+1/S04--fwd
RB4/W+1/S04--bwd
RB4/W+1/S03+-fwd
RB4/W+1/S03+-bwd
RB4/W+1/S03--fwd
RB4/W+1/S02+-fwd
RB4/W+1/S02+-bwd
RB4/W+1/S02--fwd
RB4/W+1/S01+-fwd
RB4/W+1/S01+-bwd
RB4/W+1/S01--fwd
unfulfilled elsewhere

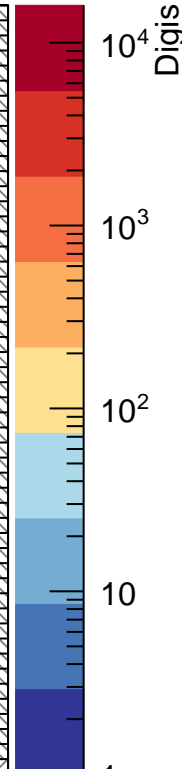
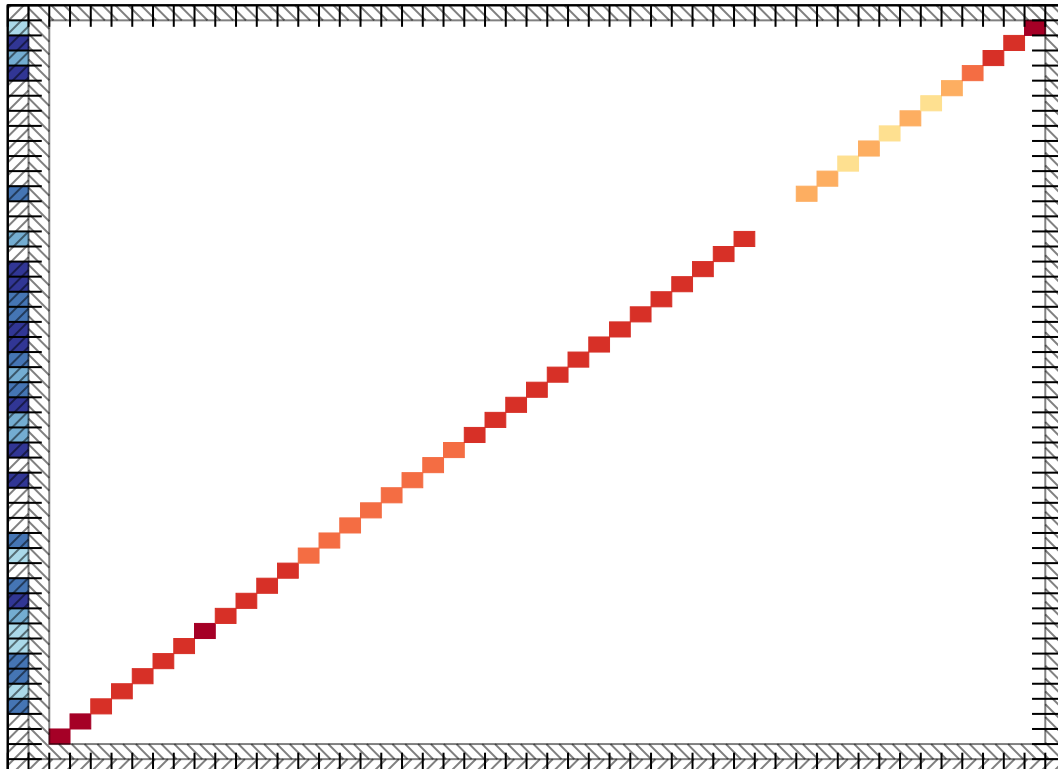
RPC Roll Legacy

RPC Roll TwinMux

RB4/W+2

CMS Preliminary - run 306122

mismatched elsewhere
 RB4/W+2/S12+.fwd
 RB4/W+2/S12+.bwd
 RB4/W+2/S12-.fwd
 RB4/W+2/S12-.bwd
 RB4/W+2/S11.fwd
 RB4/W+2/S11.bwd
 RB4/W+2/S10+.fwd
 RB4/W+2/S10+.bwd
 RB4/W+2/S10-.fwd
 RB4/W+2/S10-.bwd
 RB4/W+2/S09.fwd
 RB4/W+2/S09.bwd
 RB4/W+2/S08+.fwd
 RB4/W+2/S08+.bwd
 RB4/W+2/S08-.fwd
 RB4/W+2/S08-.bwd
 RB4/W+2/S07+.fwd
 RB4/W+2/S07+.bwd
 RB4/W+2/S07-.fwd
 RB4/W+2/S07-.bwd
 RB4/W+2/S06+.fwd
 RB4/W+2/S06+.bwd
 RB4/W+2/S06-.fwd
 RB4/W+2/S06-.bwd
 RB4/W+2/S05+.fwd
 RB4/W+2/S05+.bwd
 RB4/W+2/S05-.fwd
 RB4/W+2/S05-.bwd
 RB4/W+2/S04+.fwd
 RB4/W+2/S04+.bwd
 RB4/W+2/S04-.fwd
 RB4/W+2/S04-.bwd
 RB4/W+2/S04+.fwd
 RB4/W+2/S04+.bwd
 RB4/W+2/S03+.fwd
 RB4/W+2/S03+.bwd
 RB4/W+2/S03-.fwd
 RB4/W+2/S03-.bwd
 RB4/W+2/S02+.fwd
 RB4/W+2/S02+.bwd
 RB4/W+2/S02-.fwd
 RB4/W+2/S02-.bwd
 RB4/W+2/S01+.fwd
 RB4/W+2/S01+.bwd
 RB4/W+2/S01-.fwd
 RB4/W+2/S01-.bwd
 mismatched elsewhere
 unmatched



unfulfilled elsewhere
 RB4/W+2/S12+.fwd
 RB4/W+2/S12+.bwd
 RB4/W+2/S12-.fwd
 RB4/W+2/S12-.bwd
 RB4/W+2/S11.fwd
 RB4/W+2/S11.bwd
 RB4/W+2/S10+.fwd
 RB4/W+2/S10+.bwd
 RB4/W+2/S10-.fwd
 RB4/W+2/S10-.bwd
 RB4/W+2/S09.fwd
 RB4/W+2/S09.bwd
 RB4/W+2/S08+.fwd
 RB4/W+2/S08+.bwd
 RB4/W+2/S08-.fwd
 RB4/W+2/S08-.bwd
 RB4/W+2/S07+.fwd
 RB4/W+2/S07+.bwd
 RB4/W+2/S07-.fwd
 RB4/W+2/S07-.bwd
 RB4/W+2/S06+.fwd
 RB4/W+2/S06+.bwd
 RB4/W+2/S06-.fwd
 RB4/W+2/S06-.bwd
 RB4/W+2/S05+.fwd
 RB4/W+2/S05+.bwd
 RB4/W+2/S05-.fwd
 RB4/W+2/S05-.bwd
 RB4/W+2/S04+.fwd
 RB4/W+2/S04+.bwd
 RB4/W+2/S04-.fwd
 RB4/W+2/S04-.bwd
 RB4/W+2/S04+.fwd
 RB4/W+2/S04+.bwd
 RB4/W+2/S03+.fwd
 RB4/W+2/S03+.bwd
 RB4/W+2/S03-.fwd
 RB4/W+2/S03-.bwd
 RB4/W+2/S02+.fwd
 RB4/W+2/S02+.bwd
 RB4/W+2/S02-.fwd
 RB4/W+2/S02-.bwd
 RB4/W+2/S01+.fwd
 RB4/W+2/S01+.bwd
 RB4/W+2/S01-.fwd
 RB4/W+2/S01-.bwd
 mismatched elsewhere
 unmatched

RPC Roll Legacy

RB1in/W-2

CMS Preliminary - run 306122

Strip TwinMux

120
100
80
60
40
20
0

0

20

40

60

80

100

120

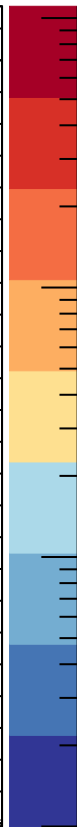
Strip Legacy

10^3
Digs

10^2

10

1



RB1in/W-1

CMS Preliminary - run 306122

Strip TwinMux

120
100
80
60
40
20
0

0

20

40

60

80

100

120

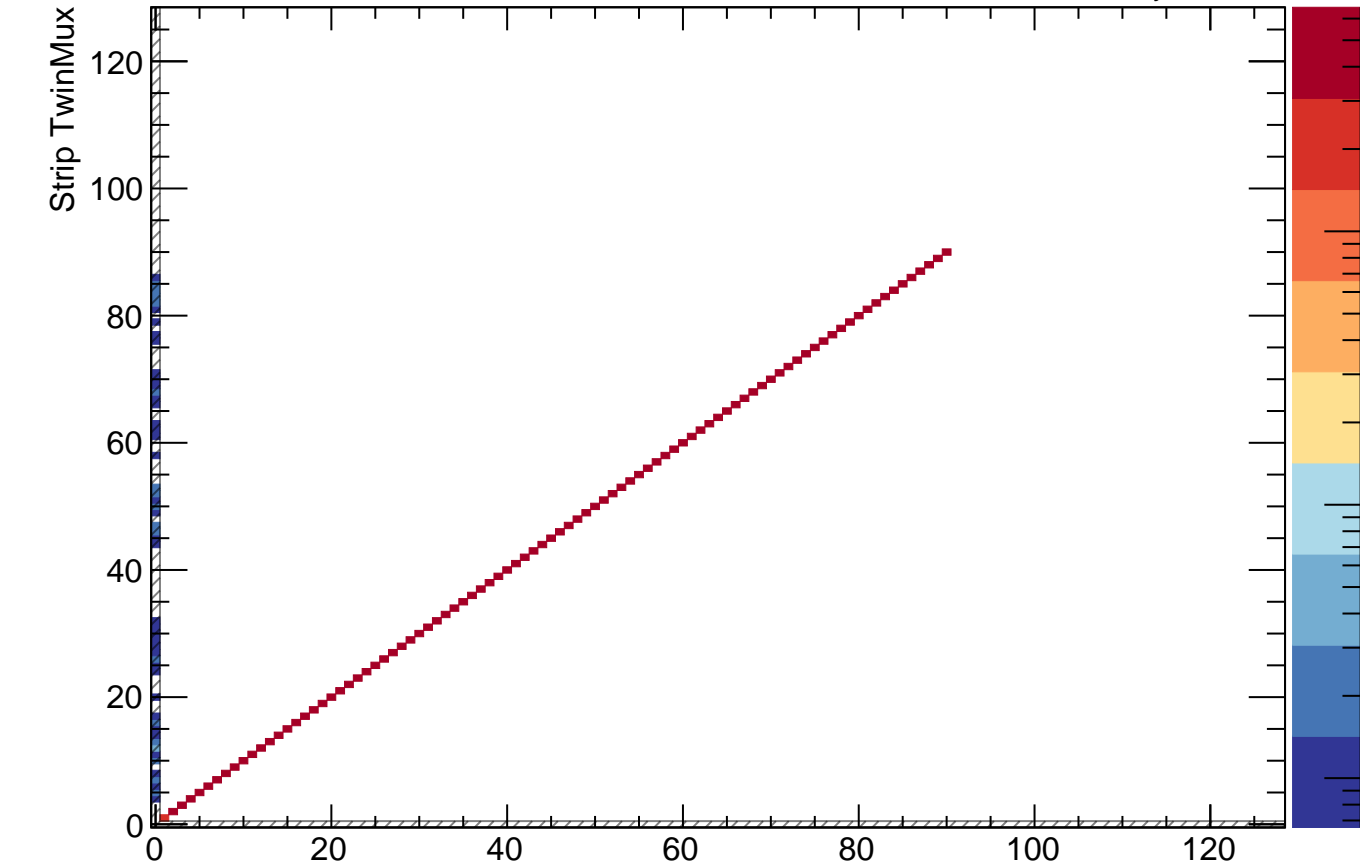
Strip Legacy

Digits

10^2

10

1



RB1in/W+0

CMS Preliminary - run 306122

Strip TwinMux

120
100
80
60
40
20
0

Digis

10^2

10

1

0

20

40

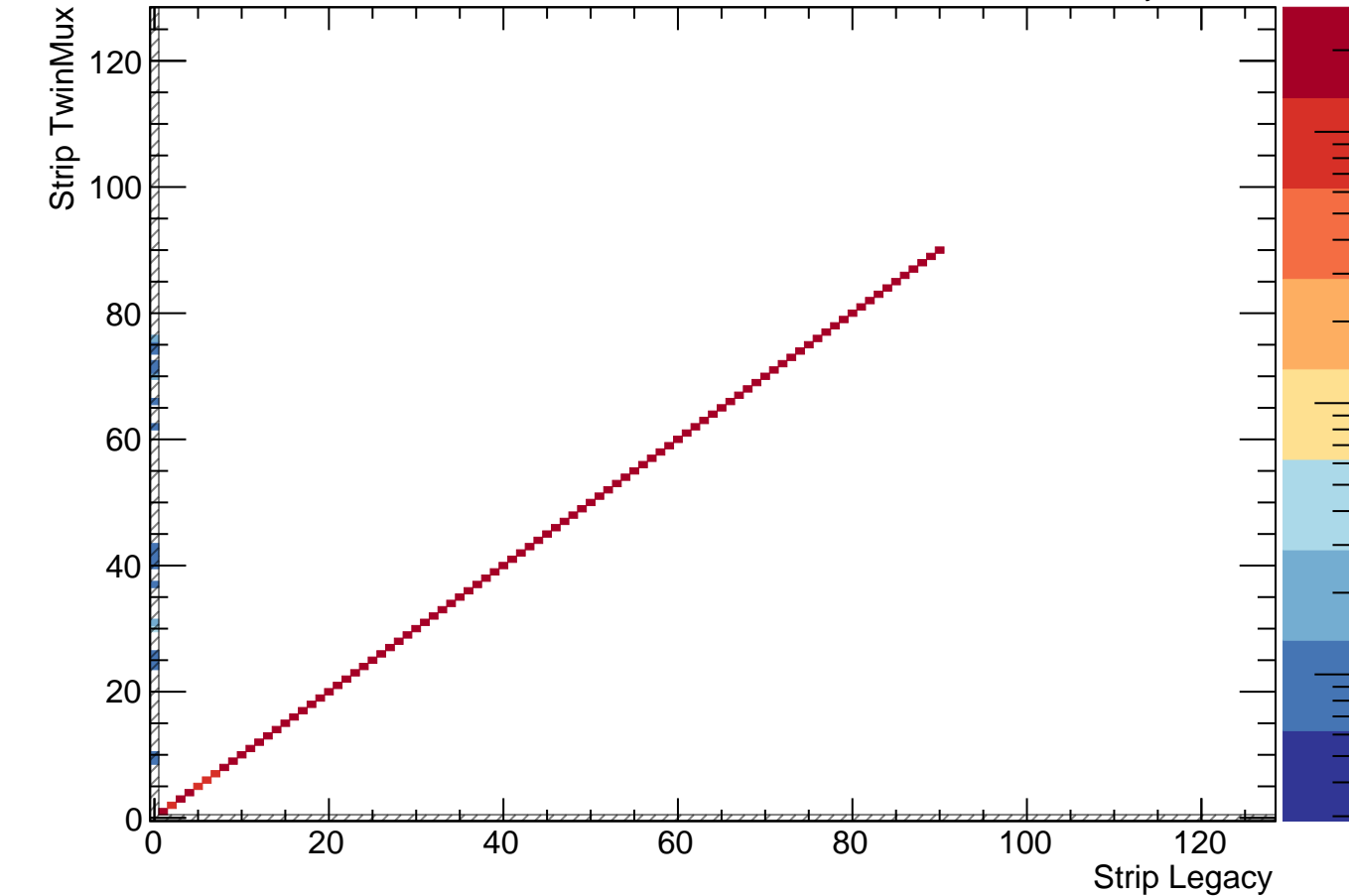
60

80

100

120

Strip Legacy



RB1in/W+1

CMS Preliminary - run 306122

Strip TwinMux

120
100
80
60
40
20
0

Digis

10^3
 10^2
10
1

Strip Legacy

0

20

40

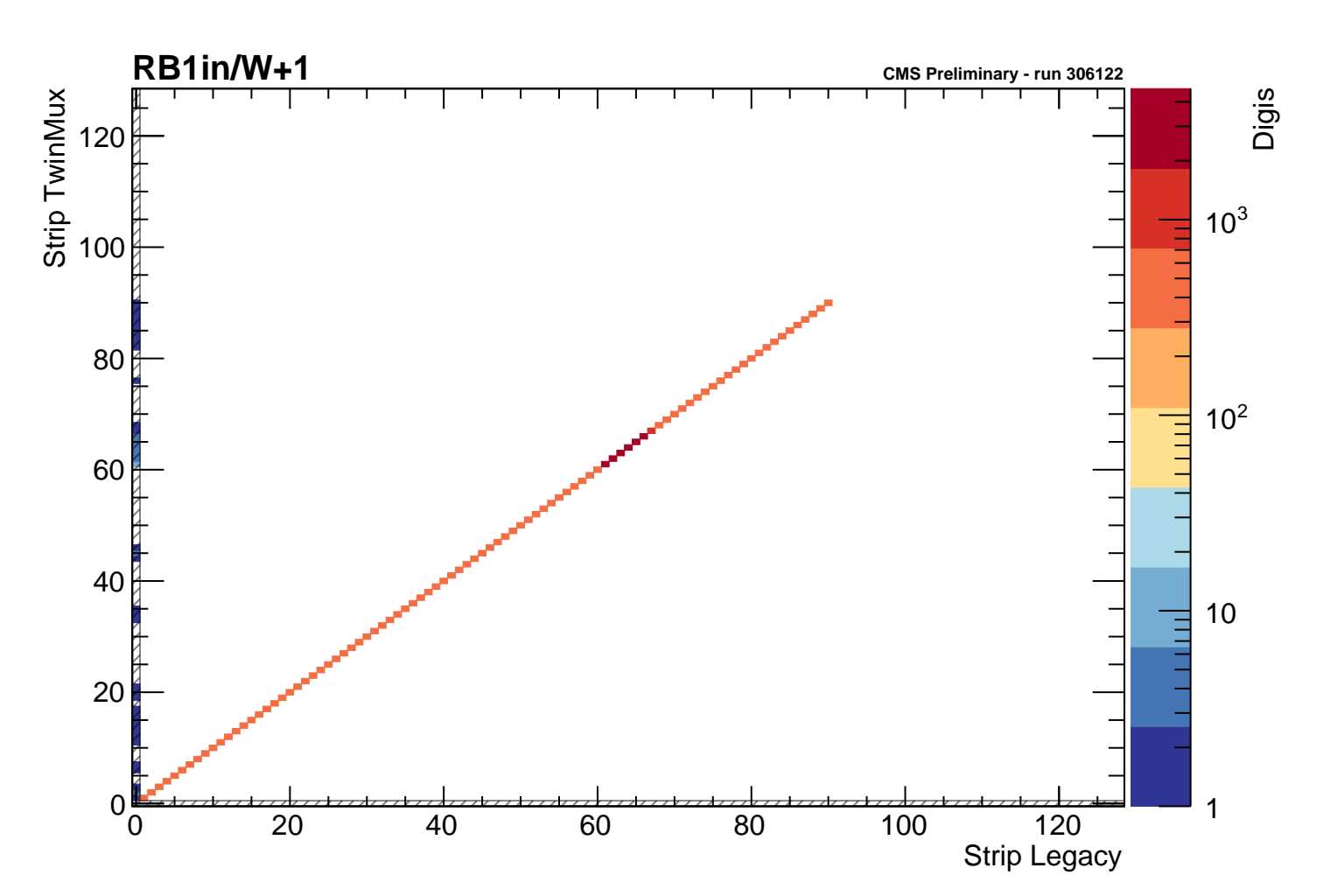
60

80

100

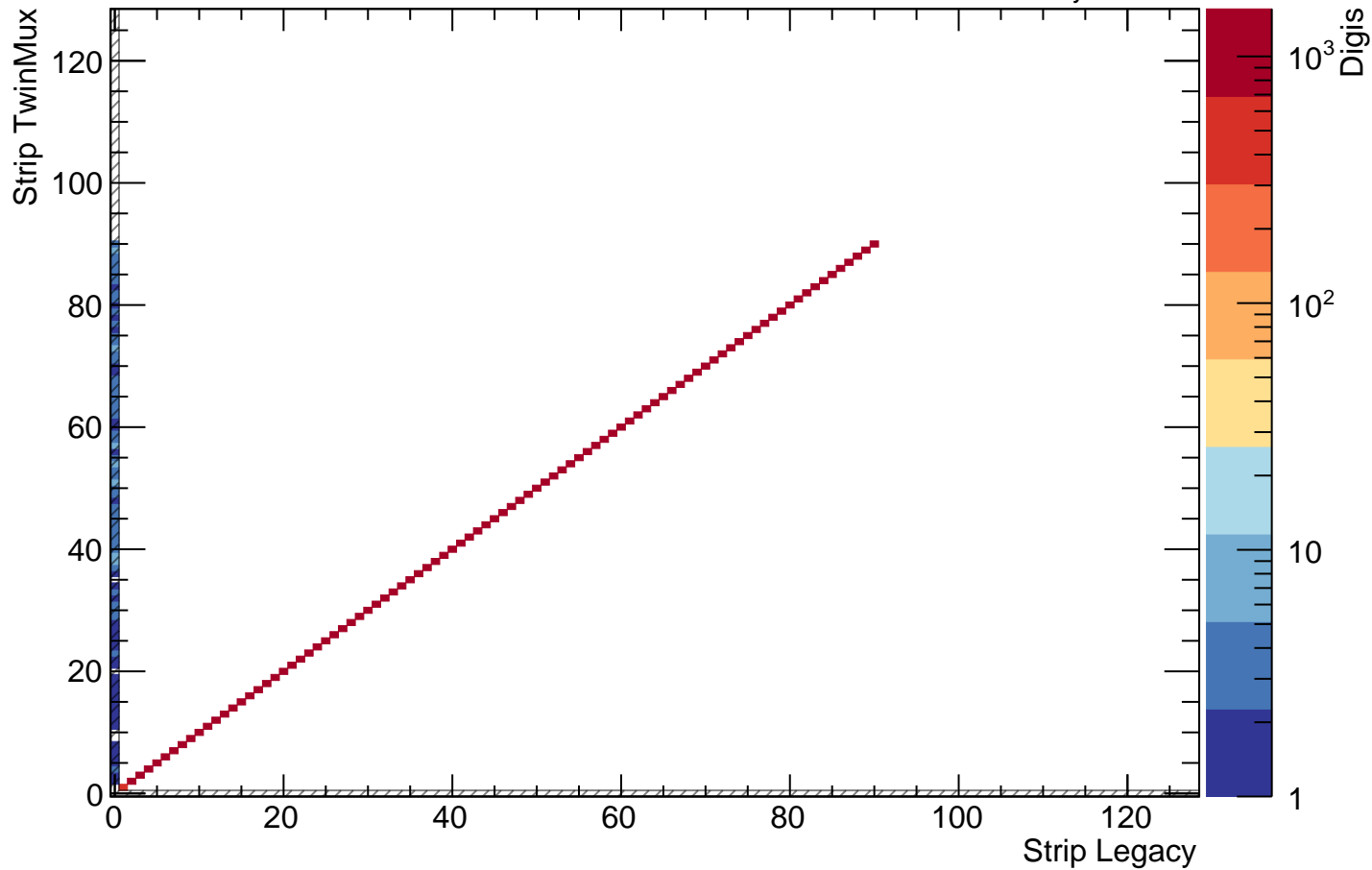
120

1



RB1in/W+2

CMS Preliminary - run 306122



RB1out/W-2

CMS Preliminary - run 306122

Strip TwinMux

120
100
80
60
40
20
0

0

20

40

60

80

100

120

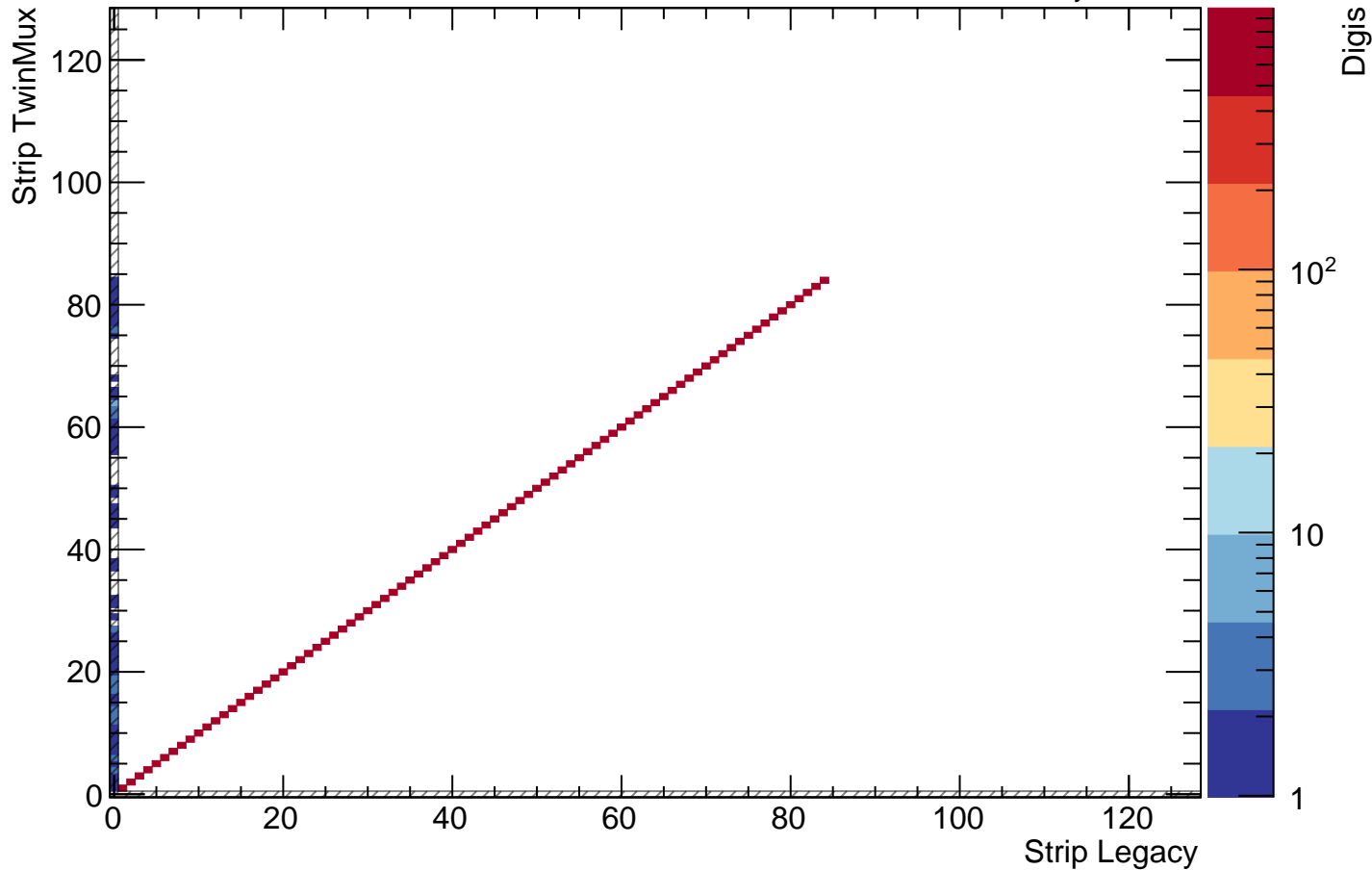
Strip Legacy

Digis

10^2

10

1



RB1out/W-1

CMS Preliminary - run 306122

Strip TwinMux

120
100
80
60
40
20
0

Digits

10^2

10

1

0

20

40

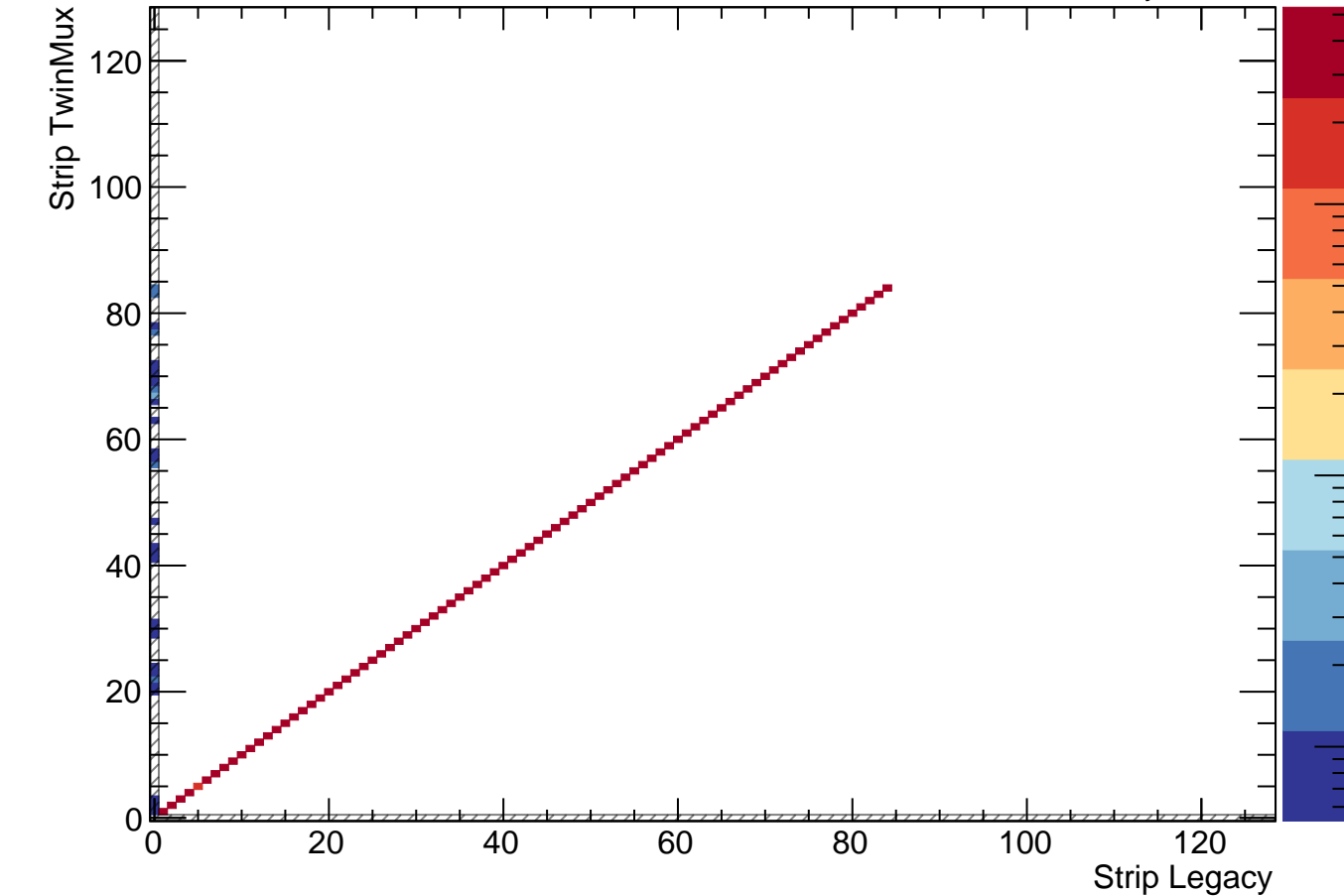
60

80

100

120

Strip Legacy



RB1out/W+0

CMS Preliminary - run 306122

Strip TwinMux

120
100
80
60
40
20
0

Digis

10^2

10

1

0

20

40

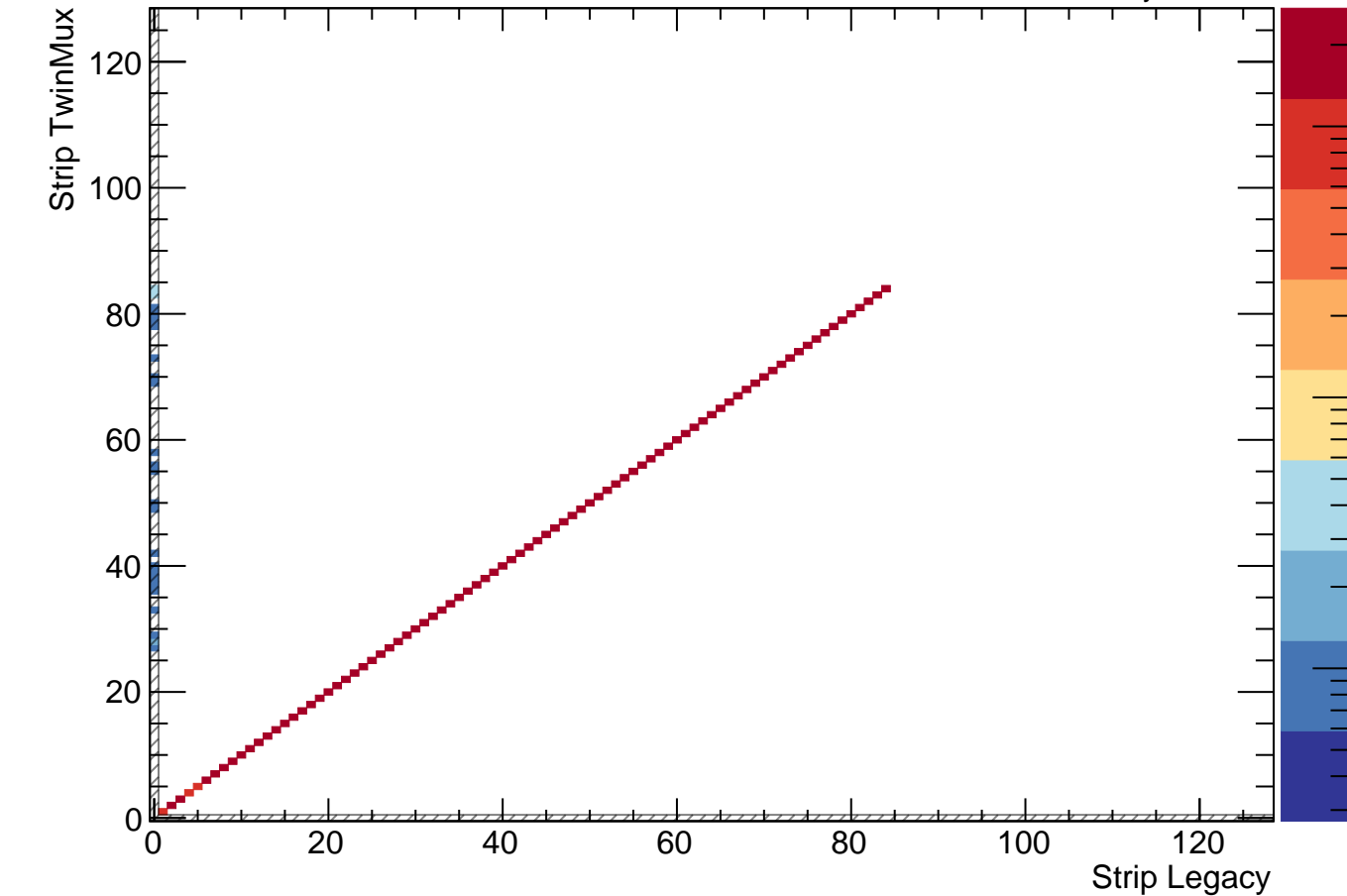
60

80

100

120

Strip Legacy



RB1out/W+1

CMS Preliminary - run 306122

Strip TwinMux

120
100
80
60
40
20
0

0

20

40

60

80

100

120

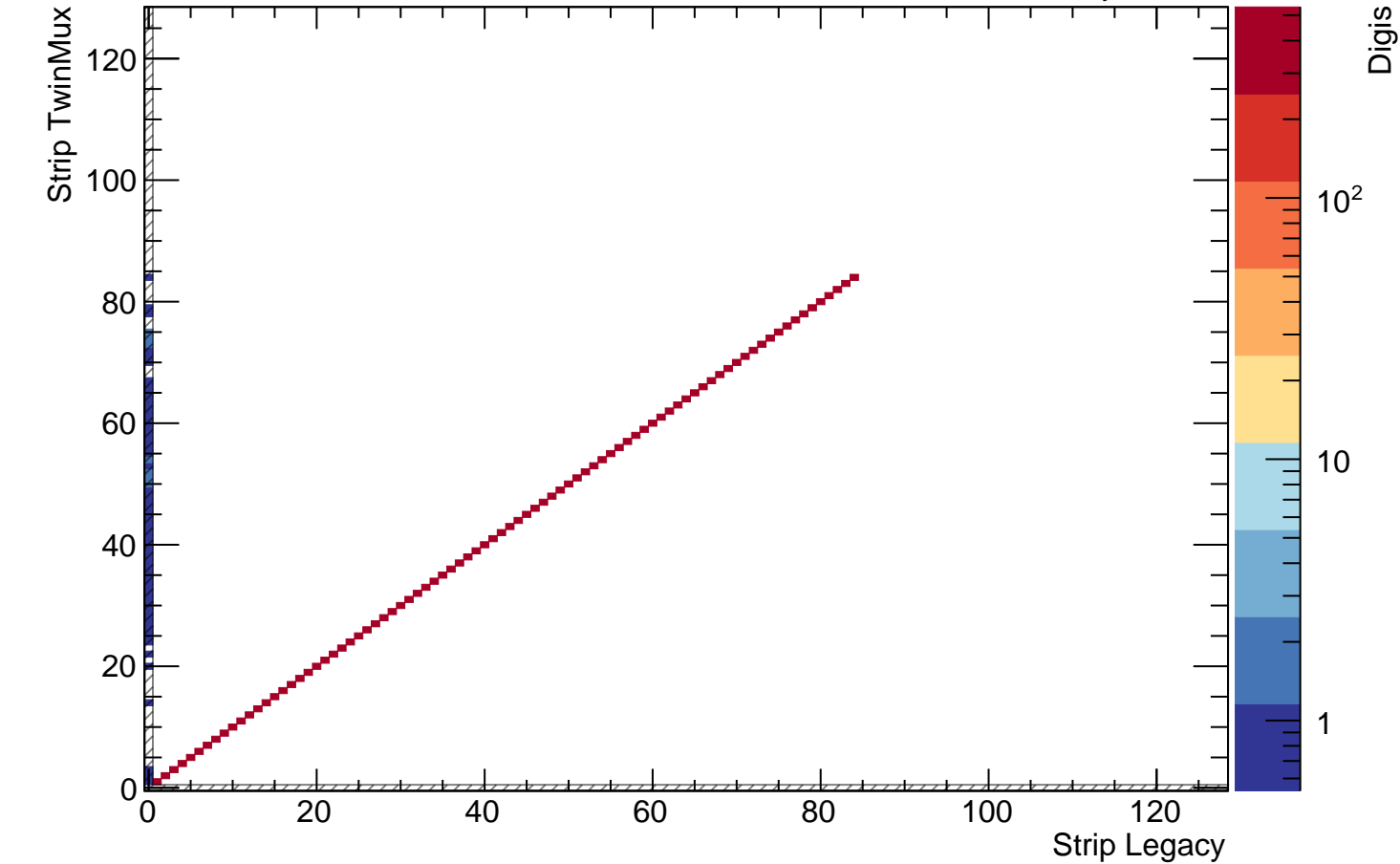
Strip Legacy

Digis

10^2

10

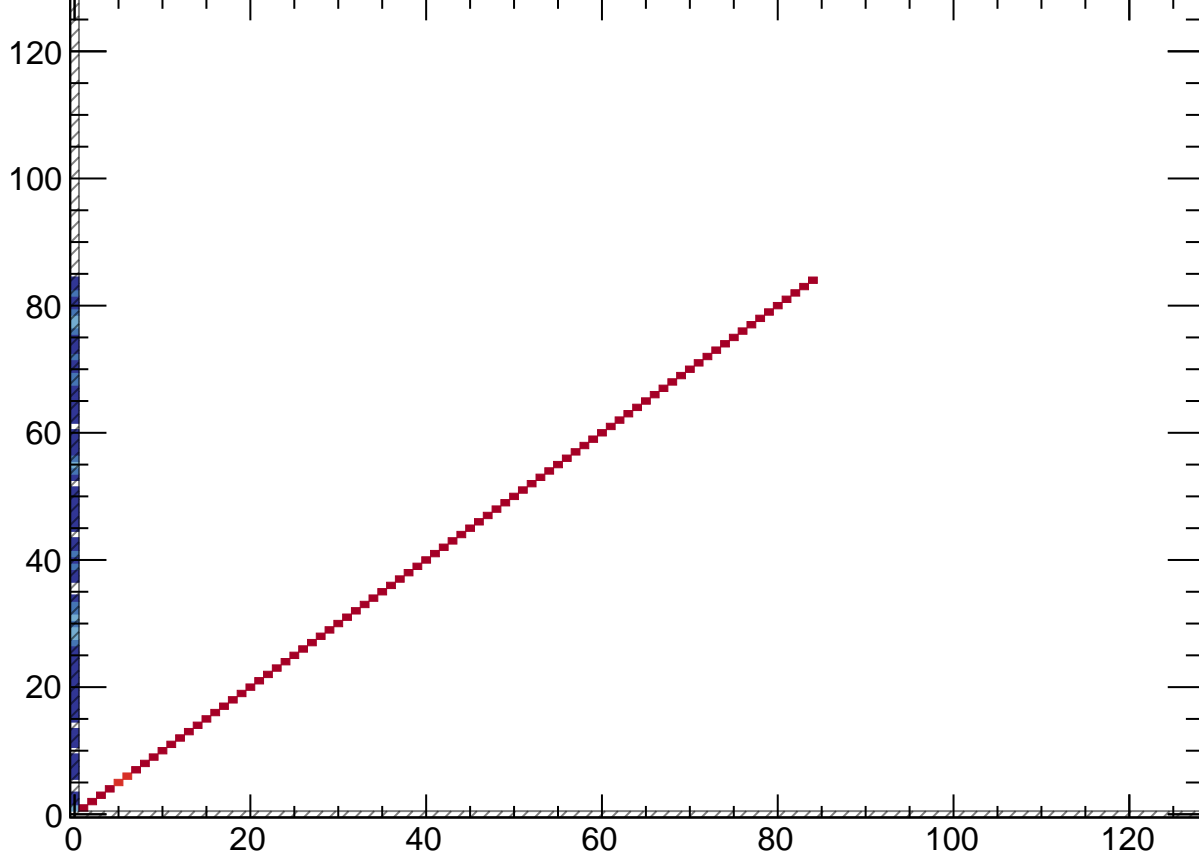
1



RB1out/W+2

CMS Preliminary - run 306122

Strip TwinMux



Digs



Strip Legacy

RB2in/W-2

CMS Preliminary - run 306122

Strip TwinMux

120
100
80
60
40
20
0

0

20

40

60

80

100

120

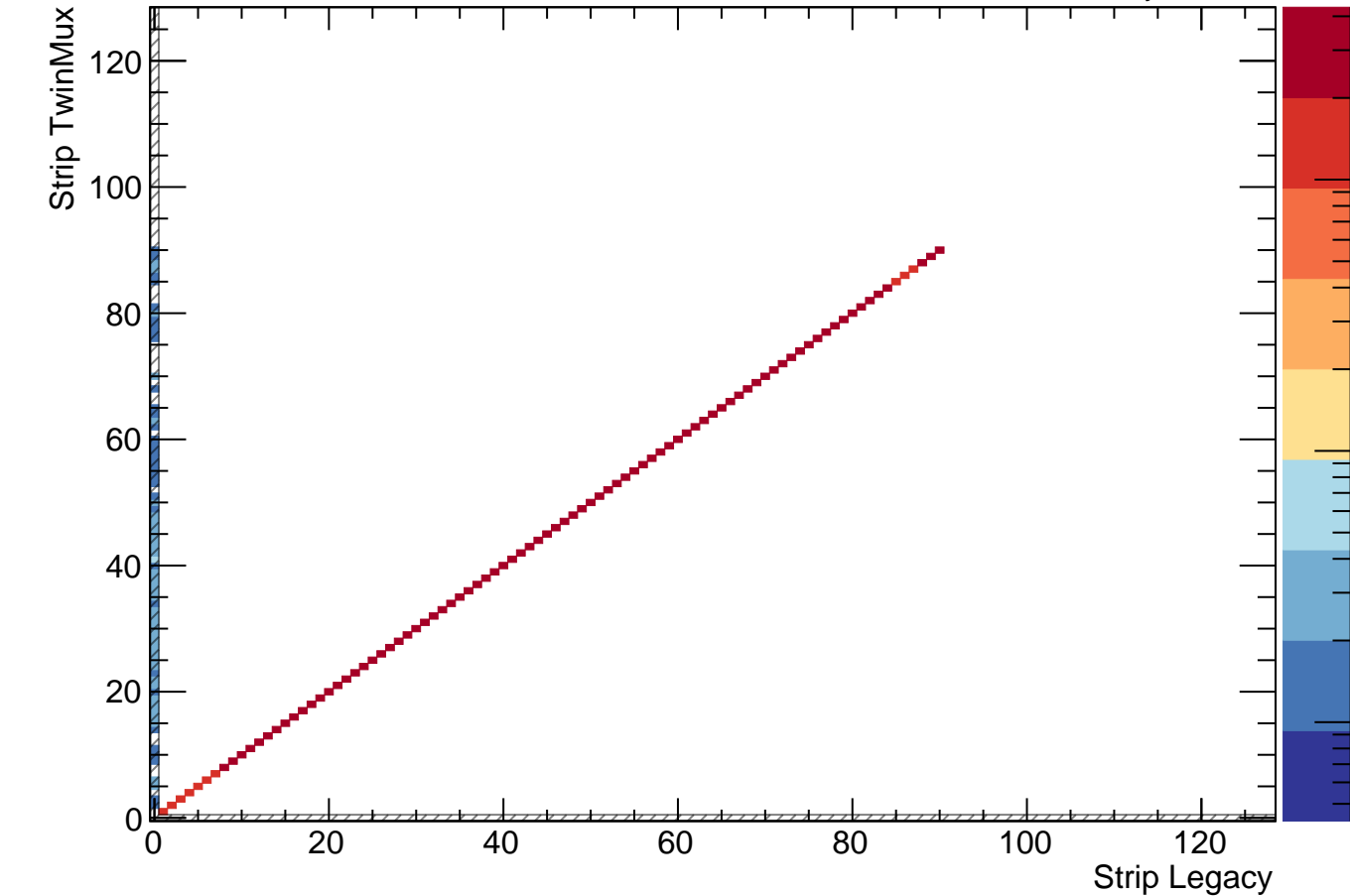
Strip Legacy

Digis

10^2

10

1



RB2in/W-1

CMS Preliminary - run 306122

Strip TwinMux

120
100
80
60
40
20
0

0

20

40

60

80

100

120

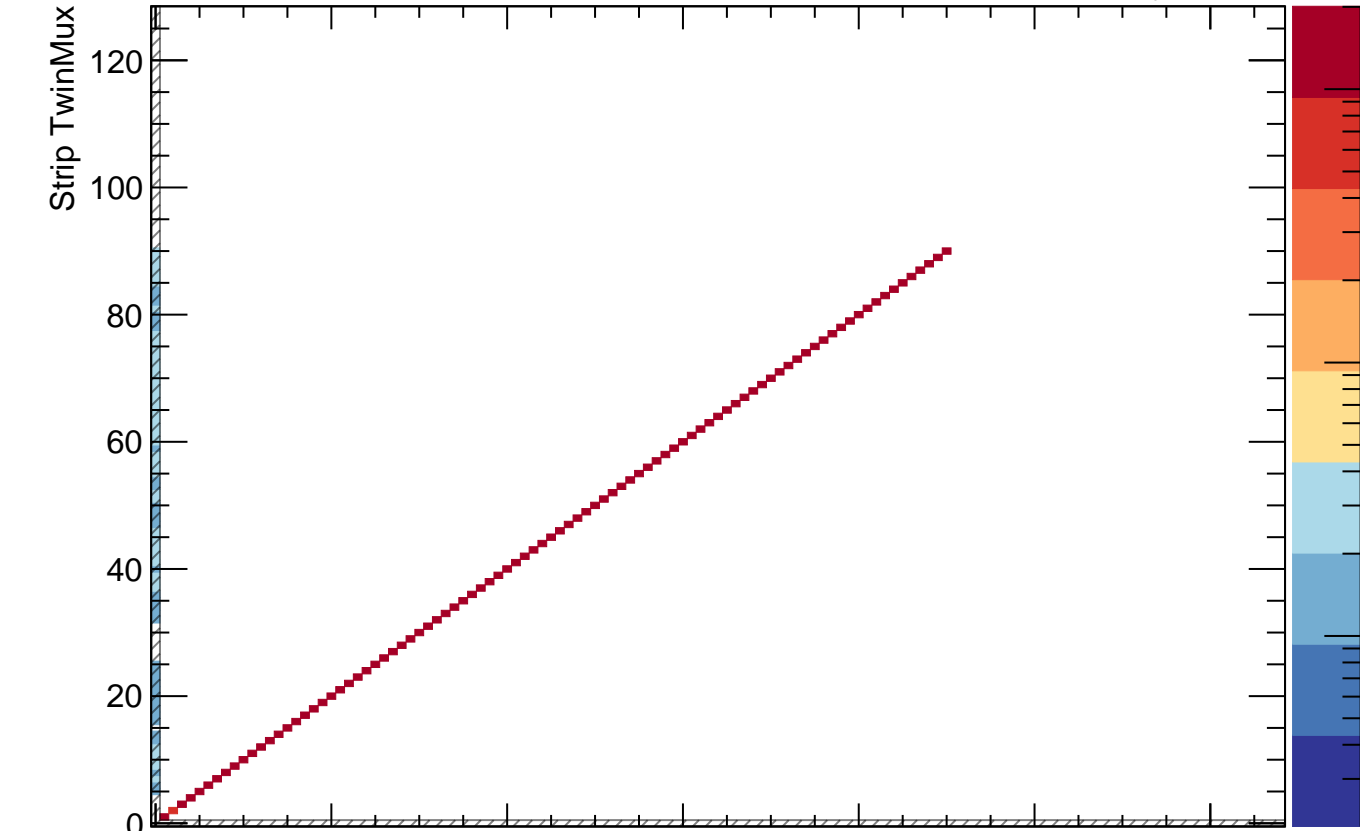
Strip Legacy

10^2

10

1

Digis



RB2in/W+0

CMS Preliminary - run 306122

Strip TwinMux

120
100
80
60
40
20
0

Digis

10^2

10

1

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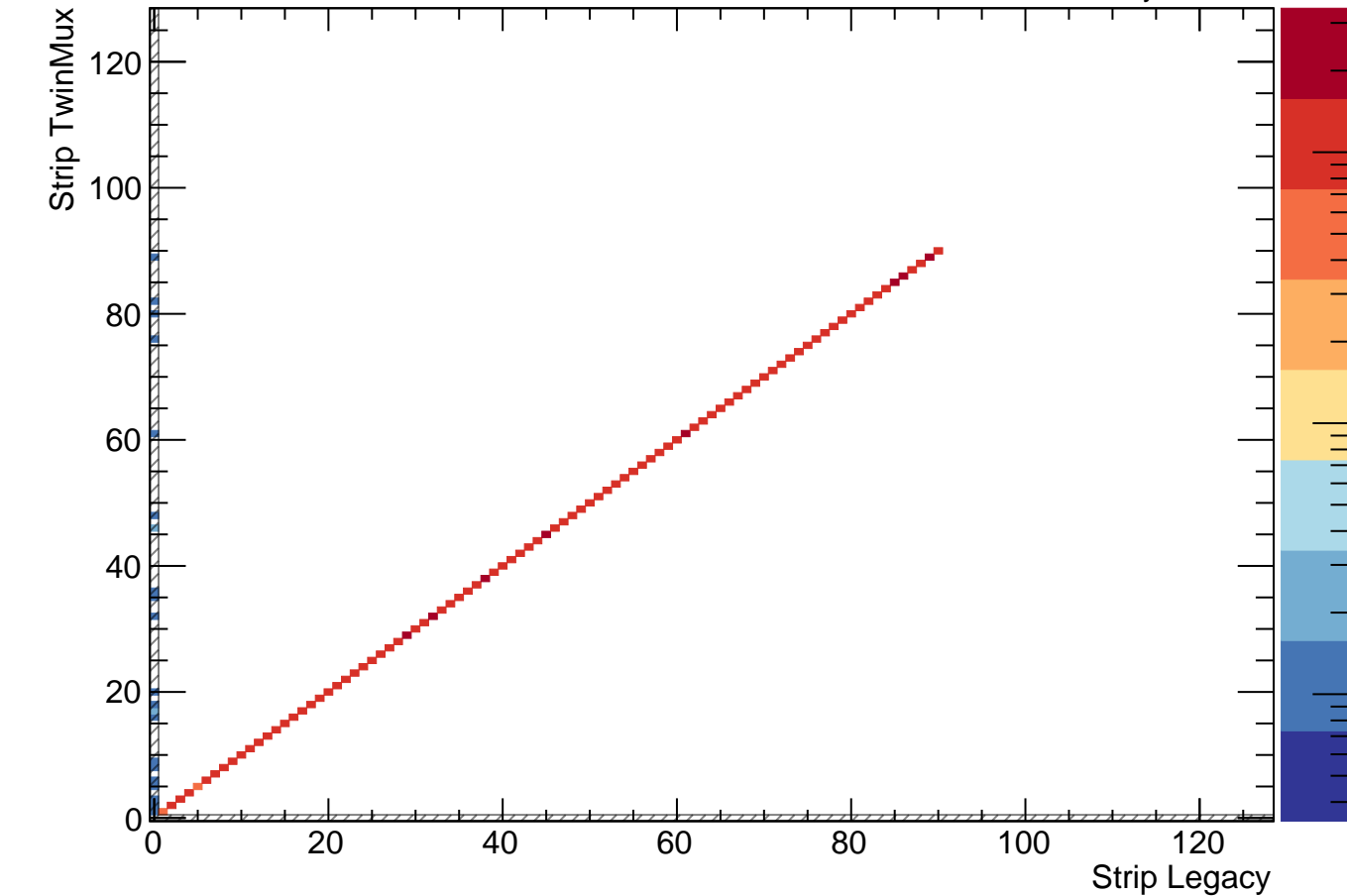
60

80

100

120

Strip Legacy



RB2in/W+1

CMS Preliminary - run 306122

Strip TwinMux

120
100
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60
40
20
0

0

20

40

60

80

100

120

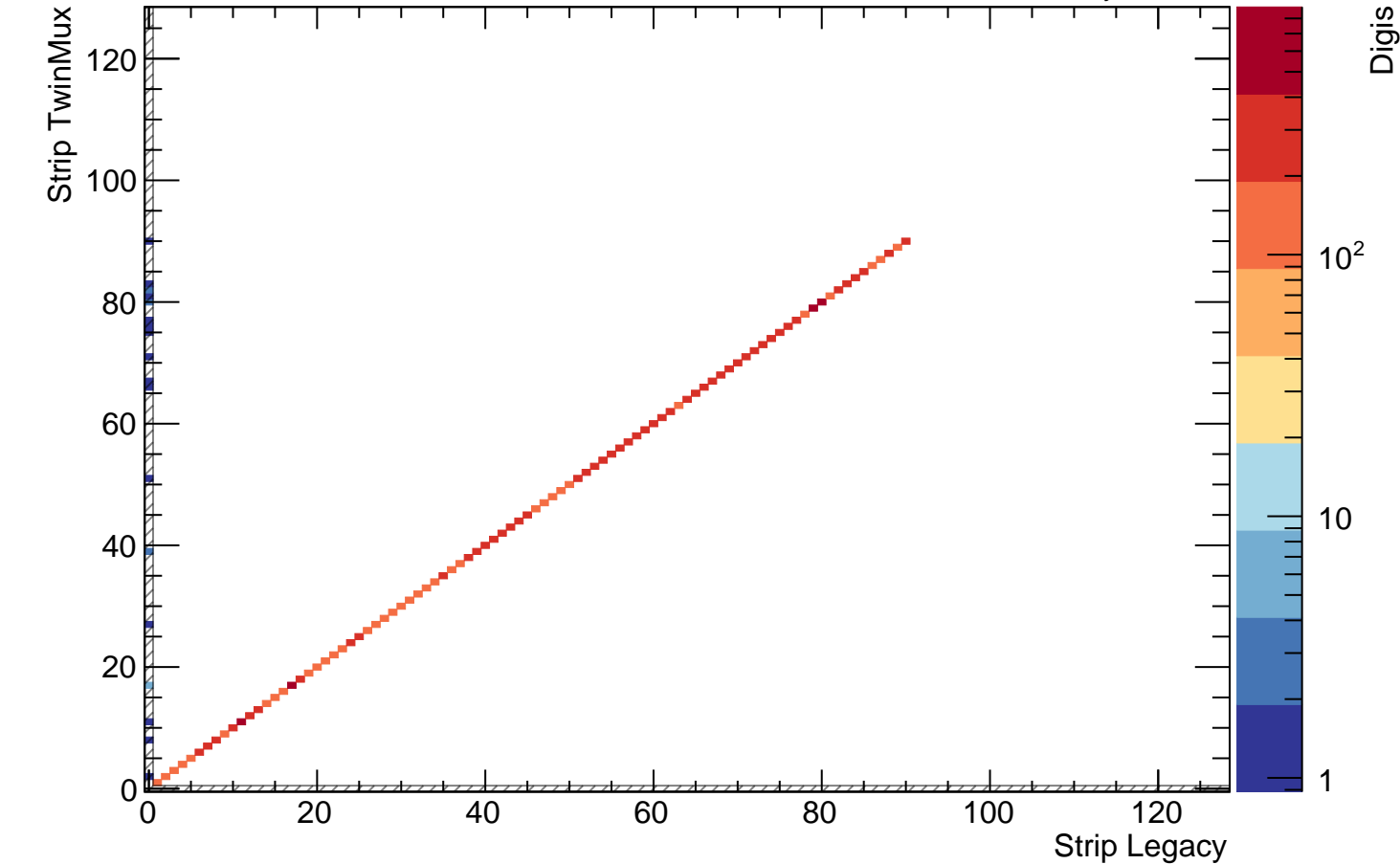
Strip Legacy

Digits

10^2

10

1



RB2in/W+2

CMS Preliminary - run 306122

Strip TwinMux

120
100
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40

60

80

100

120

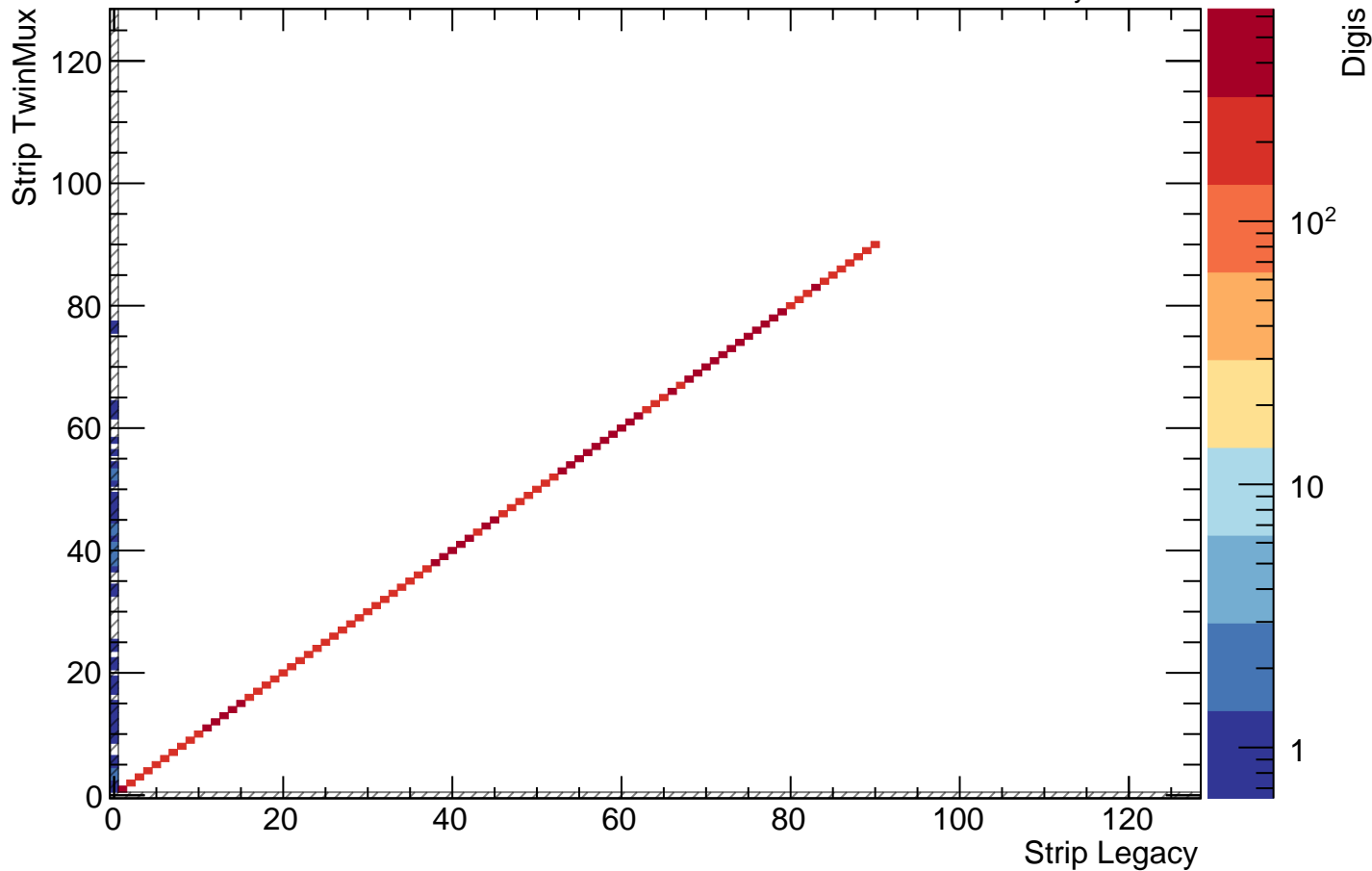
Strip Legacy

Digits

10^2

10

1



RB2out/W-2

CMS Preliminary - run 306122

Strip TwinMux

120
100
80
60
40
20
0

Digis

10^2
10
1

Strip Legacy

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RB2out/W-1

CMS Preliminary - run 306122

Strip TwinMux

120
100
80
60
40
20
0

0

20

40

60

80

100

120

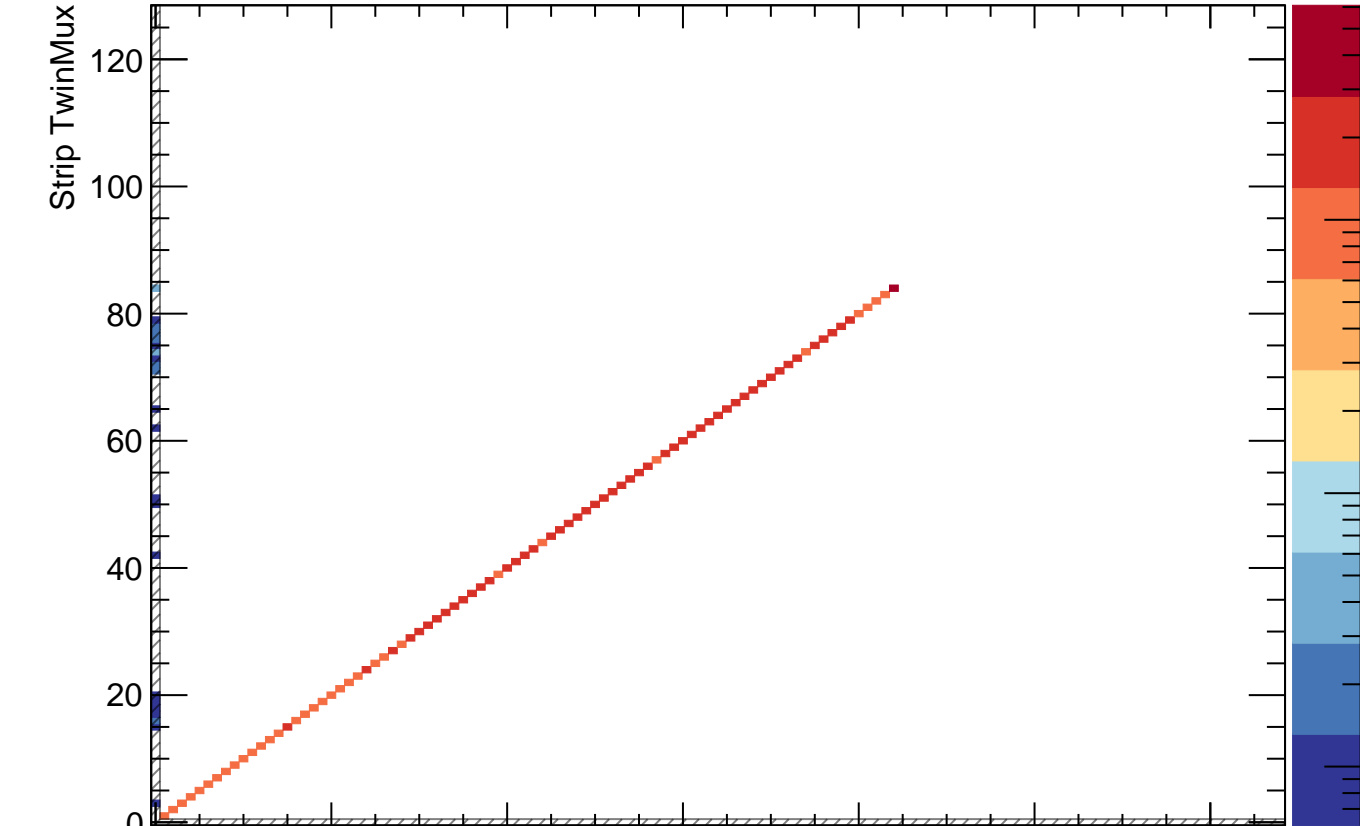
Strip Legacy

Digits

10^2

10

1



RB2out/W+0

CMS Preliminary - run 306122

Strip TwinMux

120
100
80
60
40
20
0

0

20

40

60

80

100

120

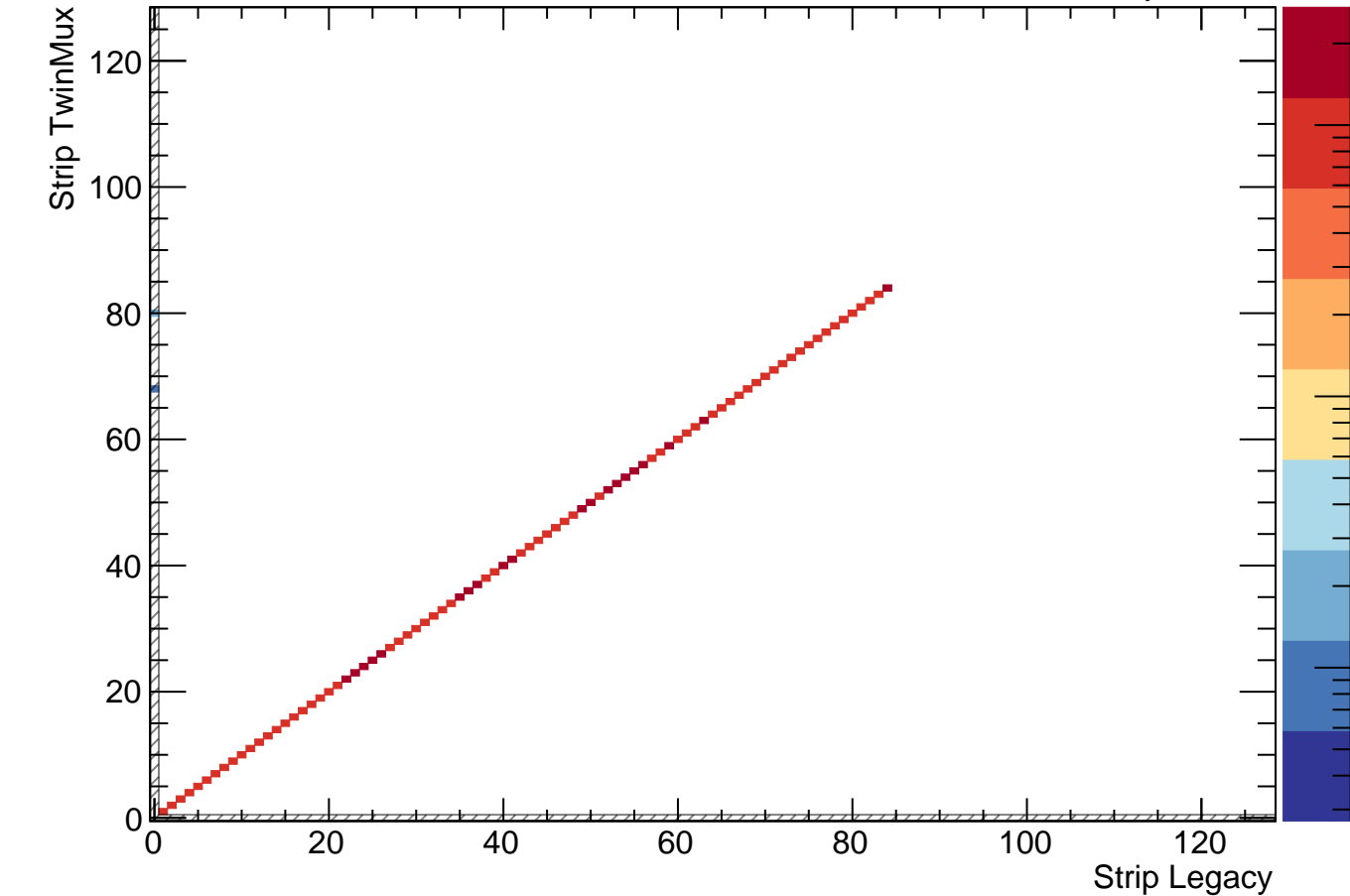
Strip Legacy

Digis

10^2

10

1



RB2out/W+1

CMS Preliminary - run 306122

Strip TwinMux

120
100
80
60
40
20
0

Digis

10^2

10

1

0

20

40

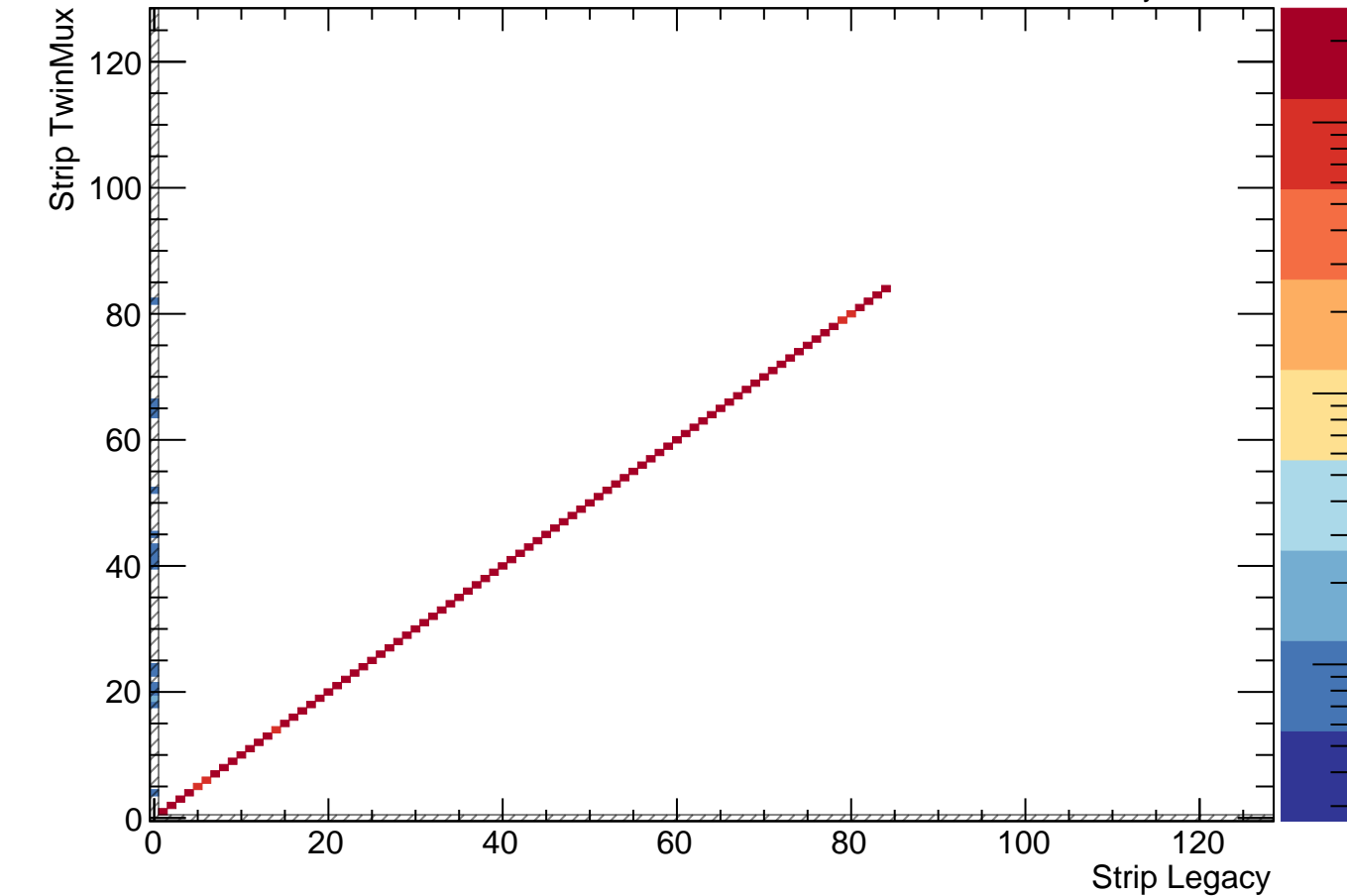
60

80

100

120

Strip Legacy



RB2out/W+2

CMS Preliminary - run 306122

Strip TwinMux

120
100
80
60
40
20
0

0

20

40

60

80

100

120

Strip Legacy

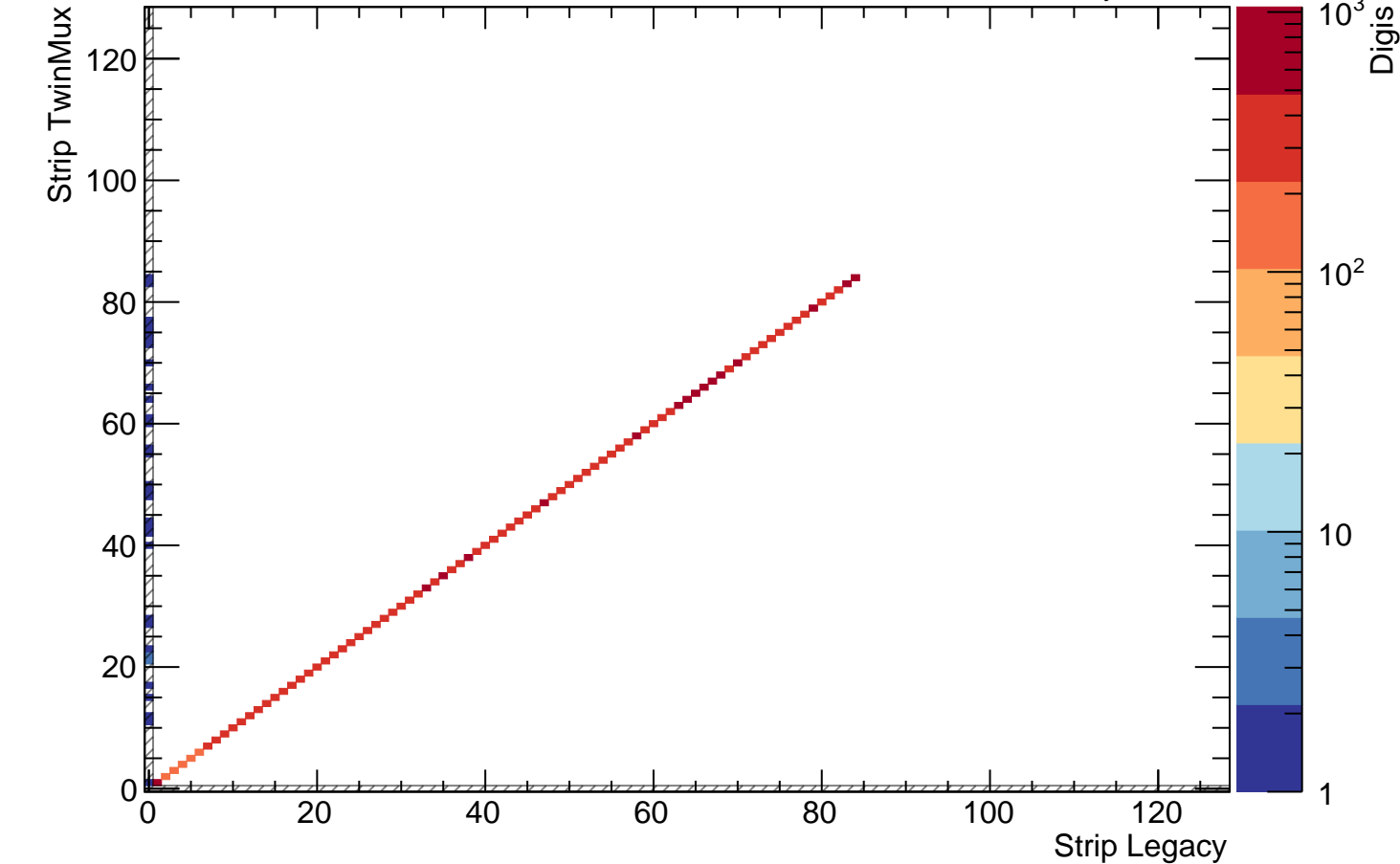
Digs

10^3

10^2

10

1



RB3/W-2

CMS Preliminary - run 306122

Strip TwinMux

120
100
80
60
40
20
0

0

20

40

60

80

100

120

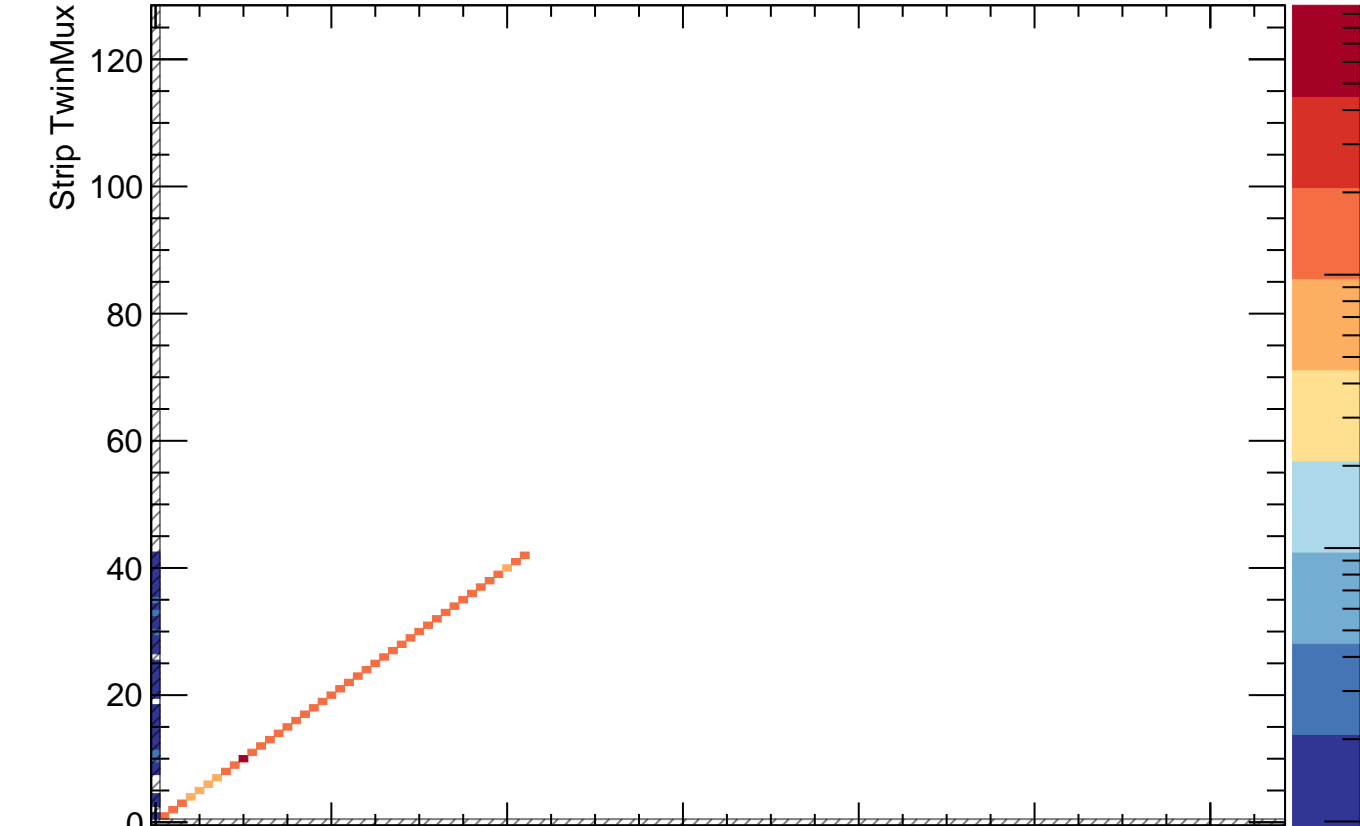
Strip Legacy

Digis

10^2

10

1



RB3/W-1

CMS Preliminary - run 306122

Strip TwinMux

120
100
80
60
40
20
0

10^3
Digs
 10^2
10
1

0

20

40

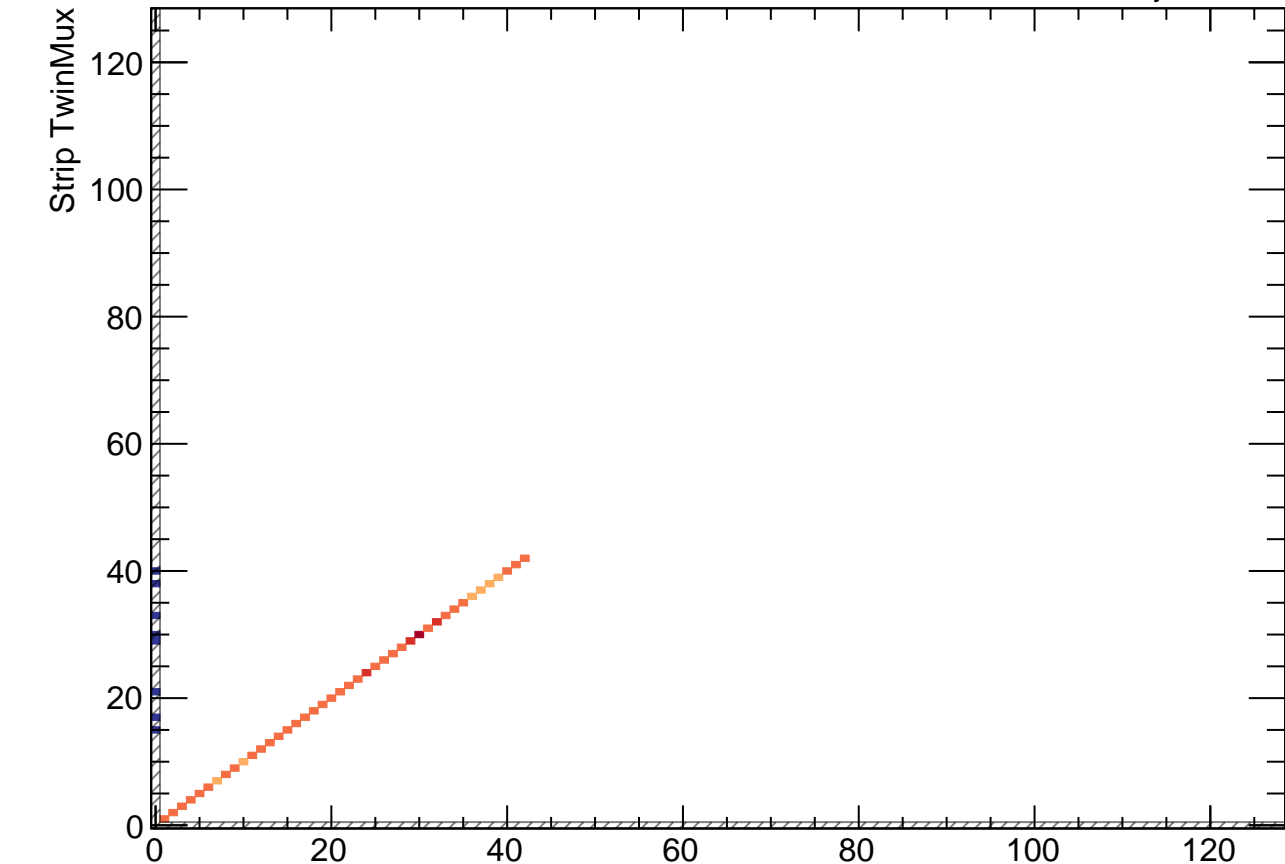
60

80

100

120

Strip Legacy



RB3/W+0

CMS Preliminary - run 306122

Strip TwinMux

120
100
80
60
40
20
0

0

20

40

60

80

100

120

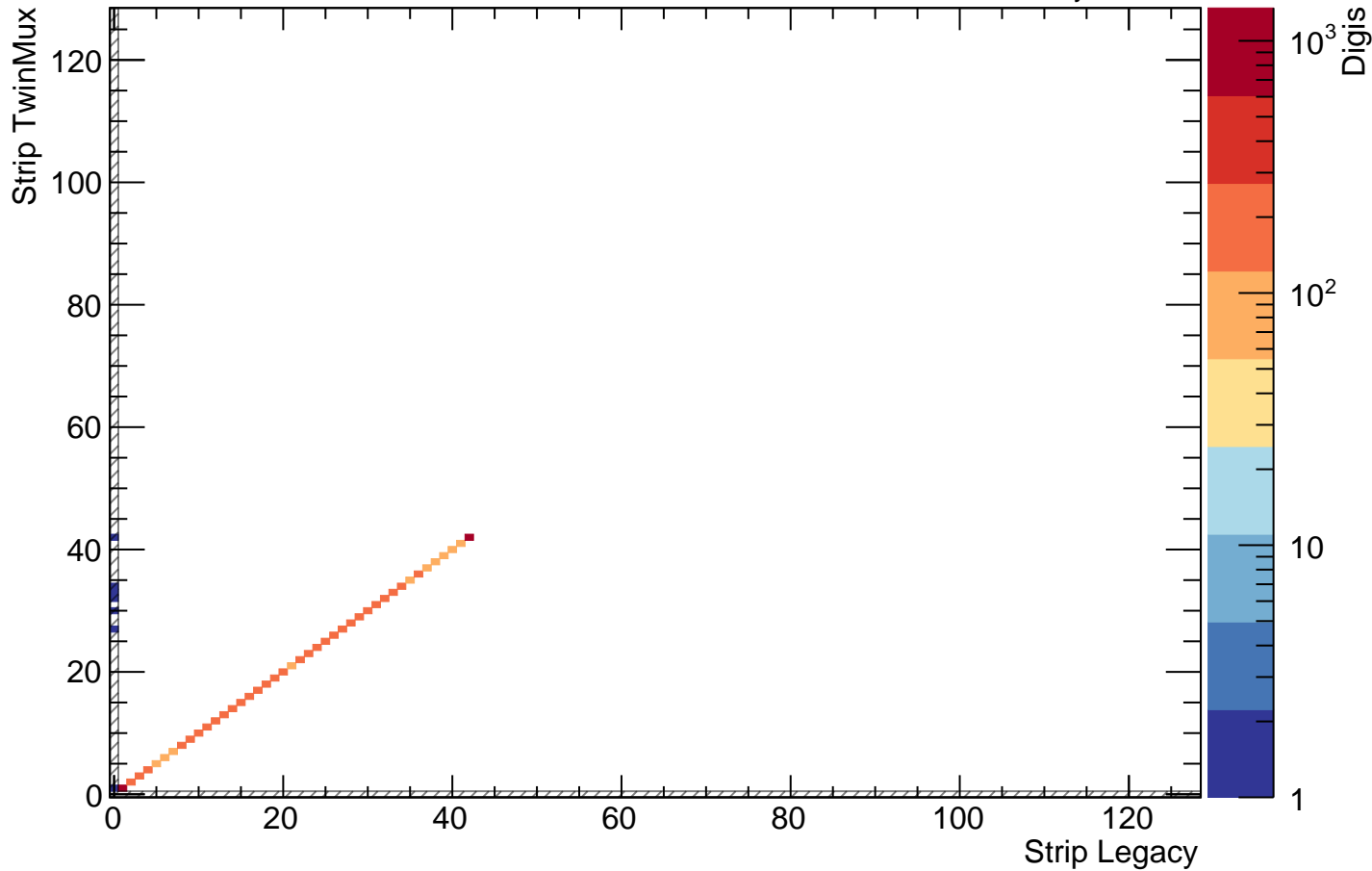
Strip Legacy

10^3
Digs

10^2

10

1



RB3/W+1

CMS Preliminary - run 306122

Strip TwinMux

120
100
80
60
40
20
0

0

20

40

60

80

100

120

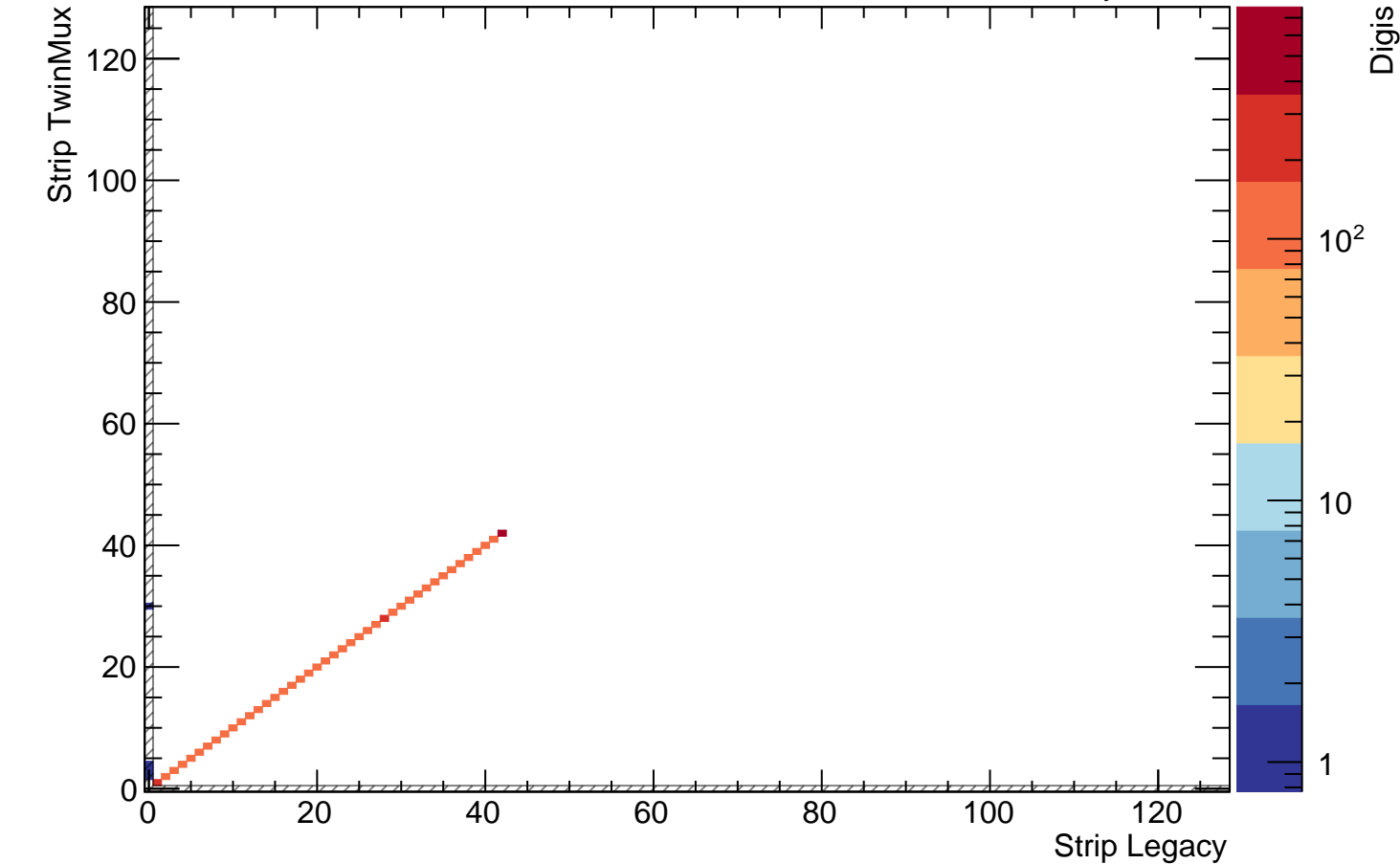
Strip Legacy

Digits

10^2

10

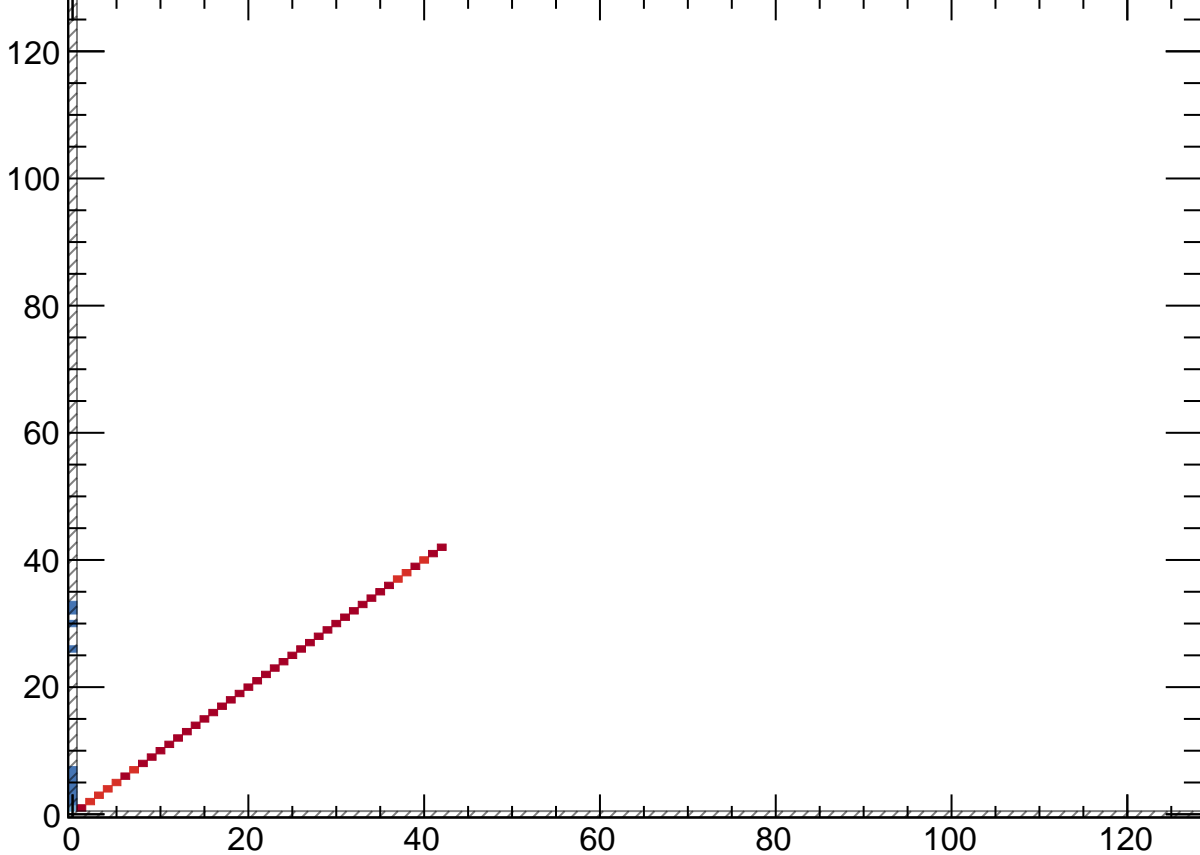
1



RB3/W+2

CMS Preliminary - run 306122

Strip TwinMux



Digis

10^2

10

1

Strip Legacy

RB4/W-2

CMS Preliminary - run 306122

Strip TwinMux

120
100
80
60
40
20
0

0

20

40

60

80

100

120

Strip Legacy

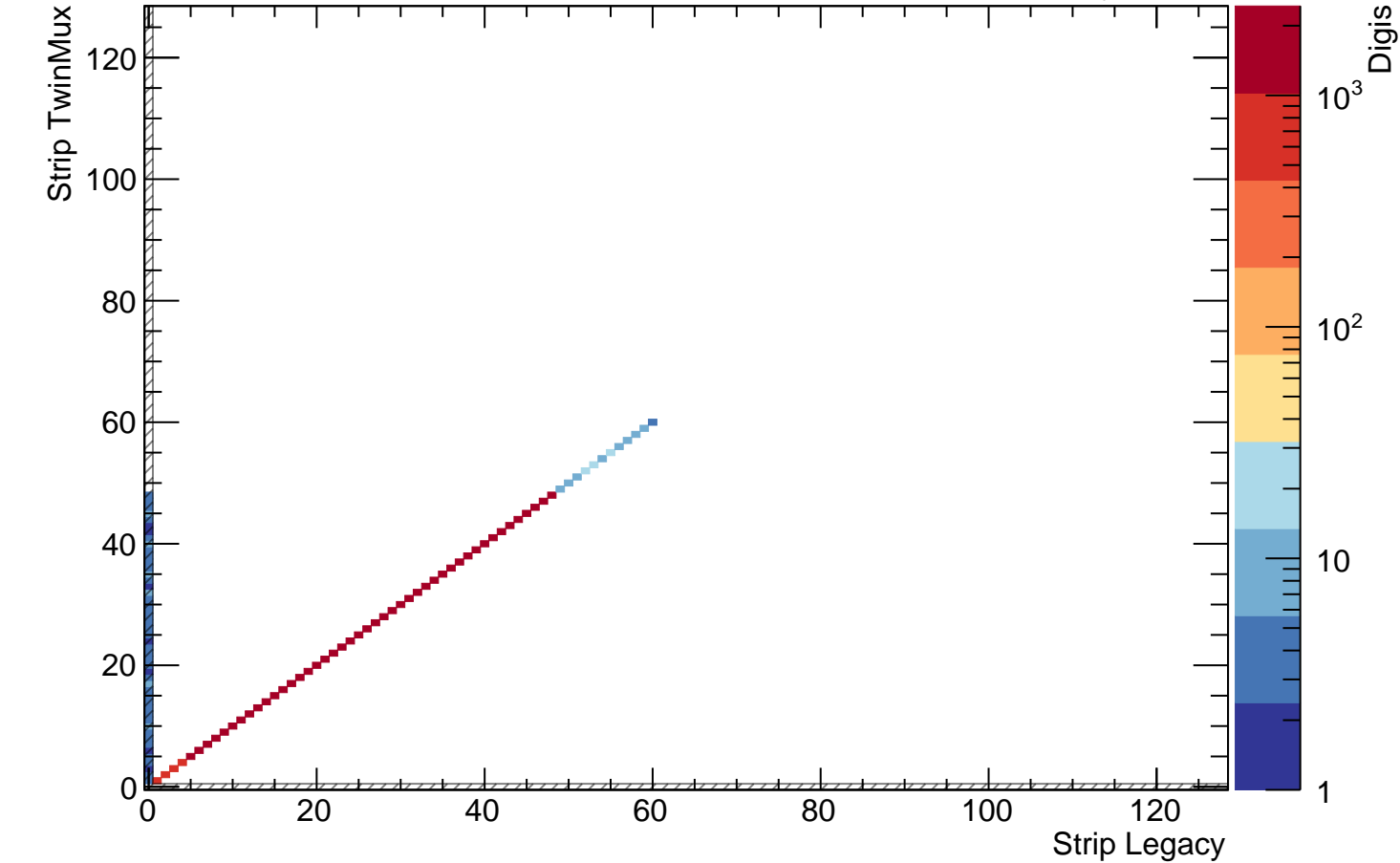
10^3

10^2

10

1

Digis



RB4/W-1

CMS Preliminary - run 306122

Strip TwinMux

120
100
80
60
40
20
0

0

20

40

60

80

100

120

Strip Legacy

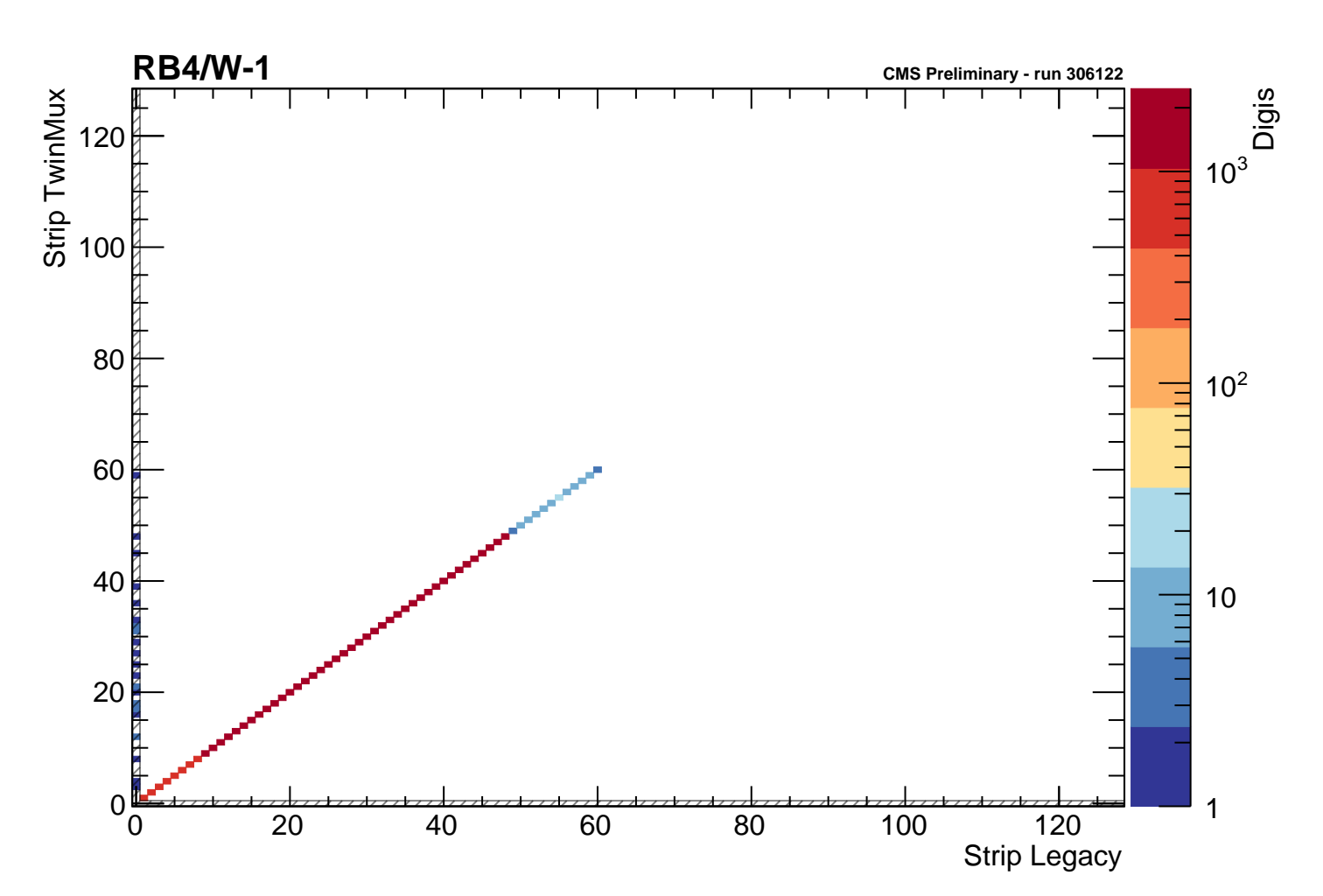
10^3

10^2

10

1

Digis



RB4/W+0

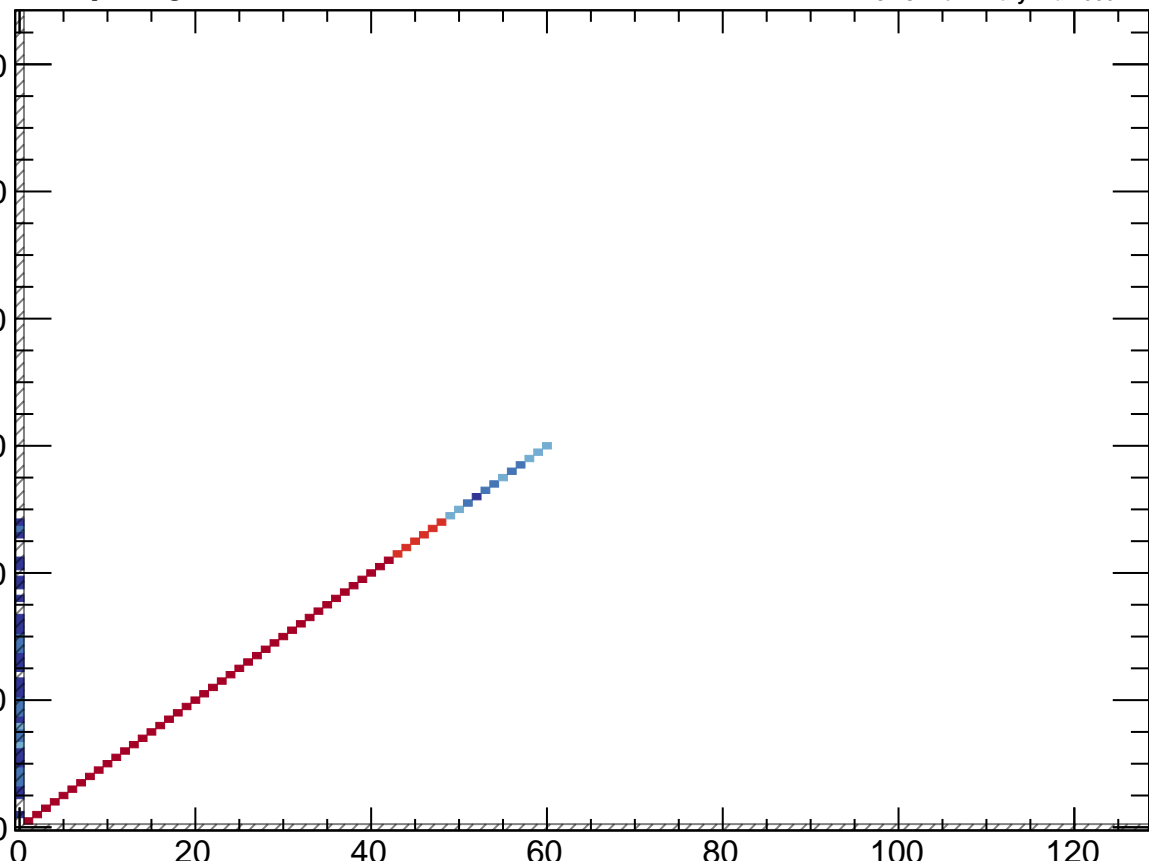
CMS Preliminary - run 306122

Strip TwinMux

120
100
80
60
40
20
0

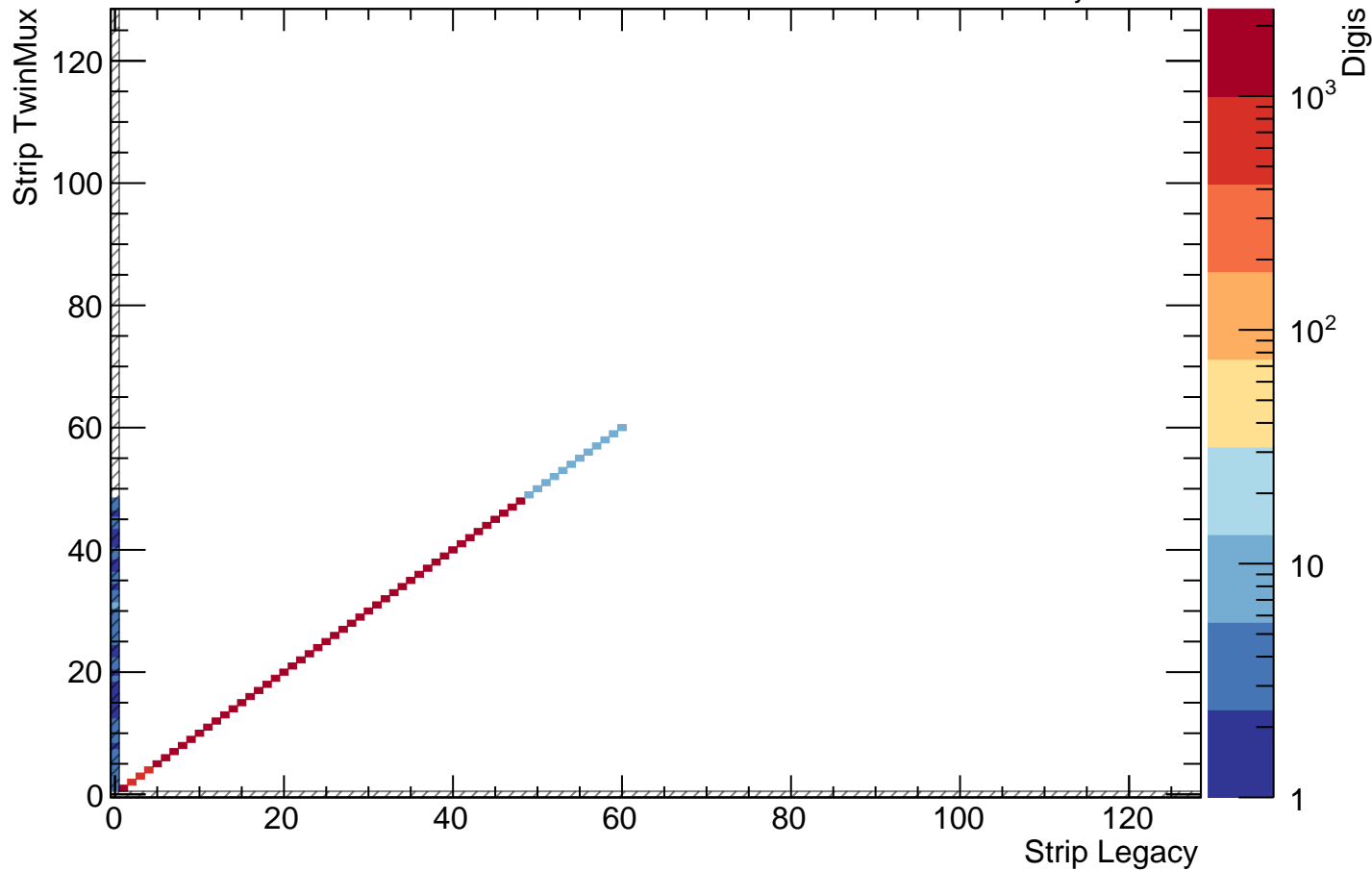
10^3
 10^2
10
1

Strip Legacy



RB4/W+1

CMS Preliminary - run 306122



RB4/W+2

CMS Preliminary - run 306122

Strip TwinMux

120
100
80
60
40
20
0

Digis

10^3
 10^2
10
1

0

20

40

60

80

100

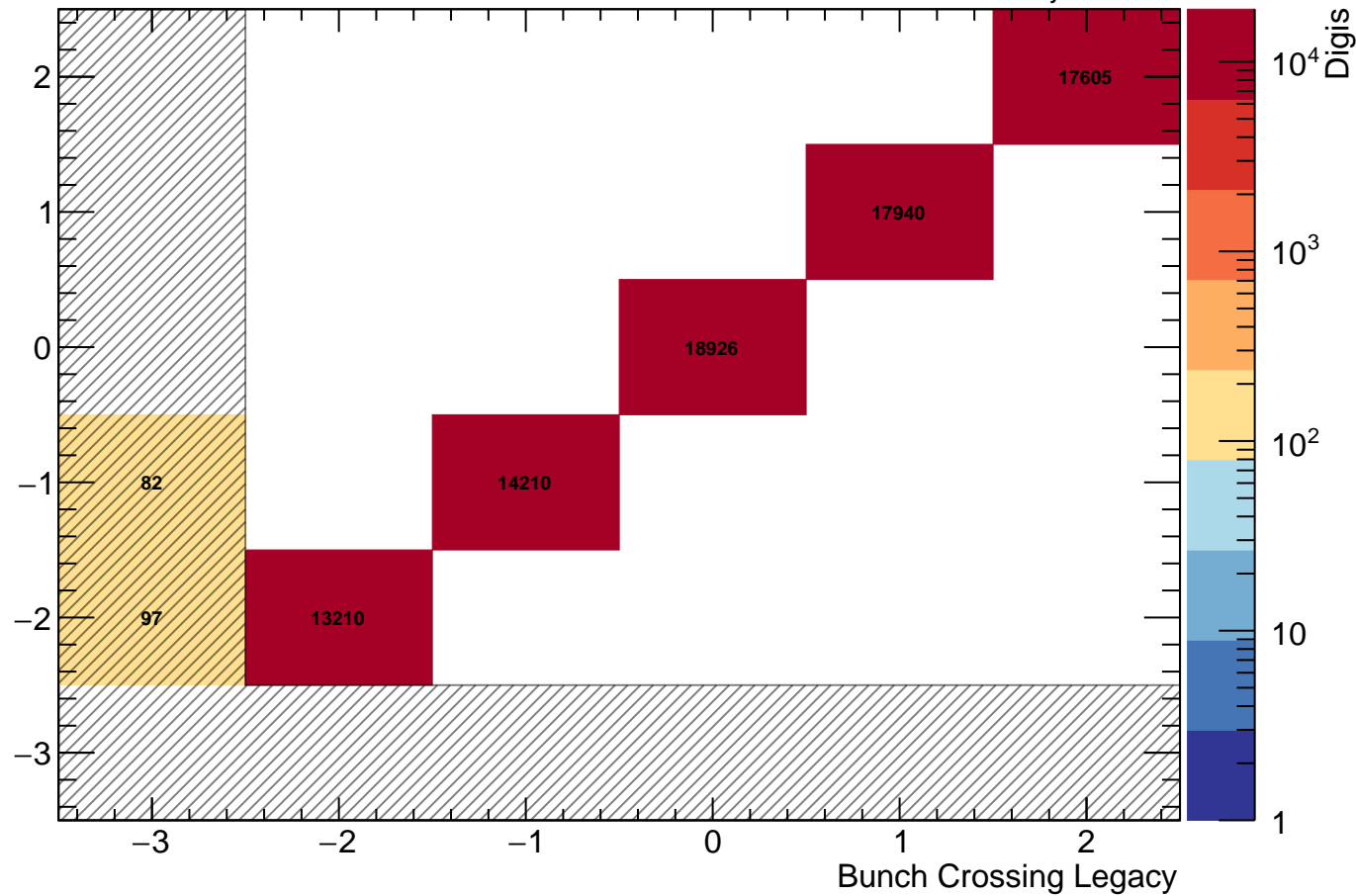
120

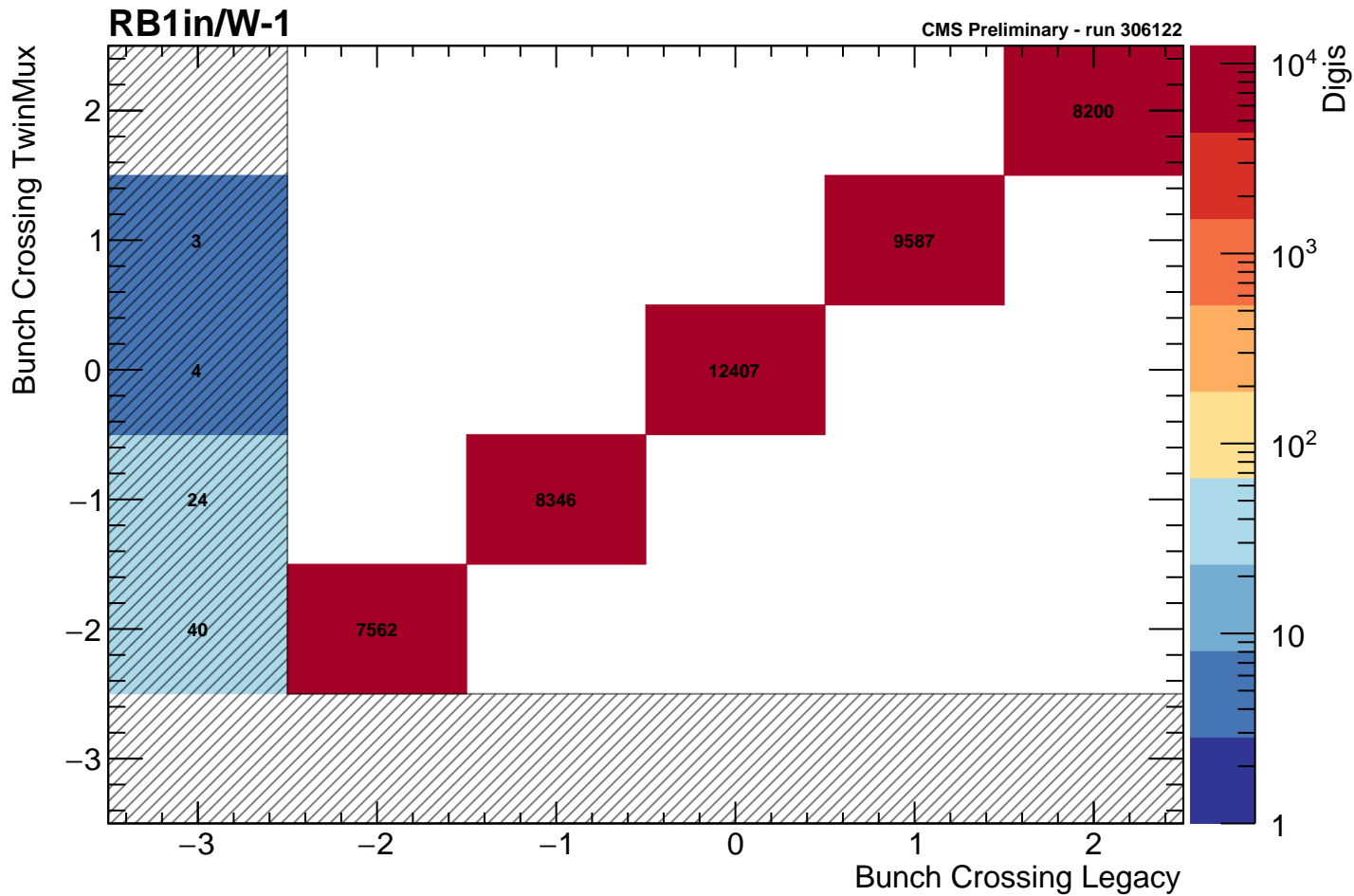
Strip Legacy

RB1in/W-2

CMS Preliminary - run 306122

Bunch Crossing TwinMux

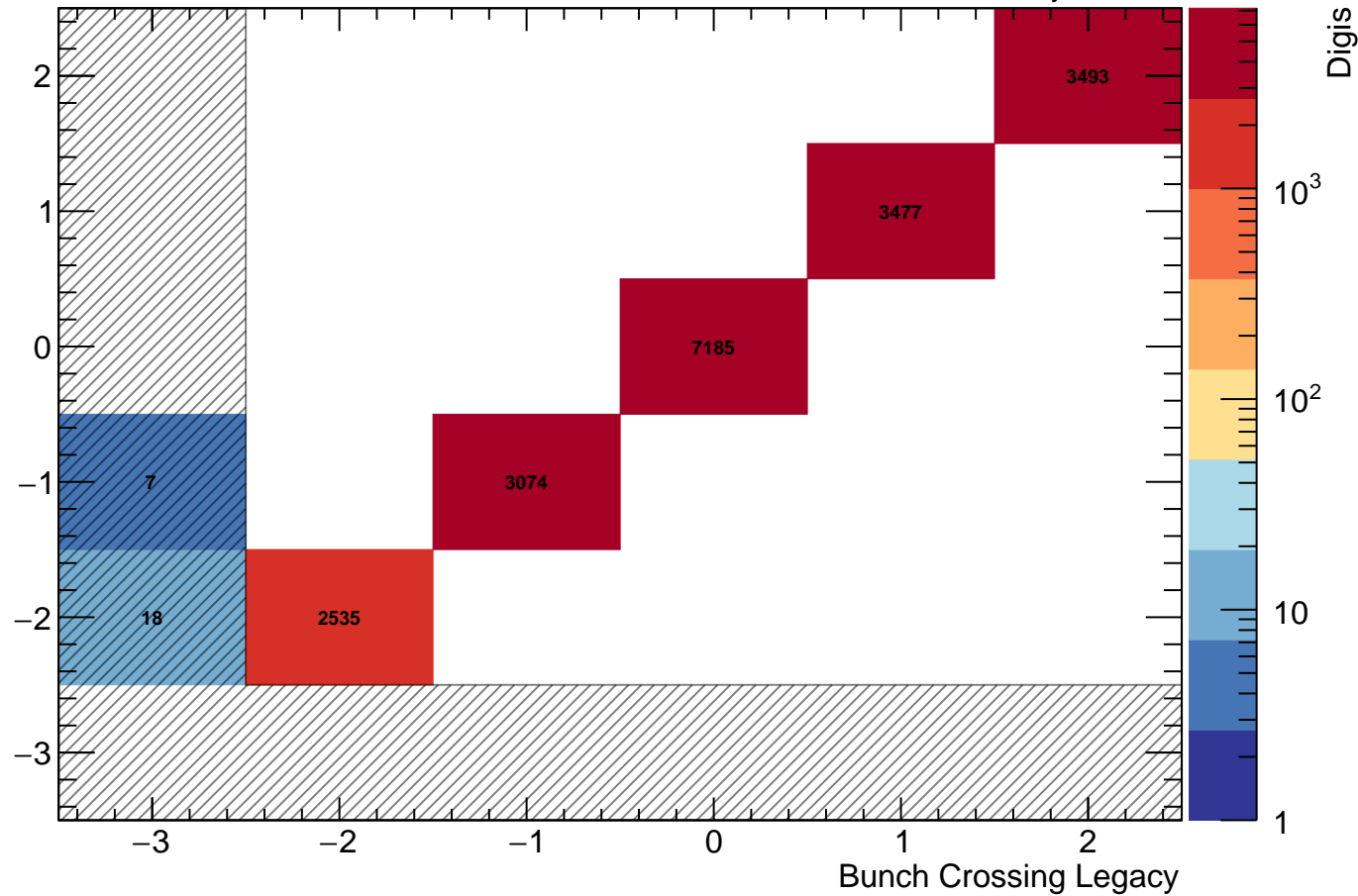


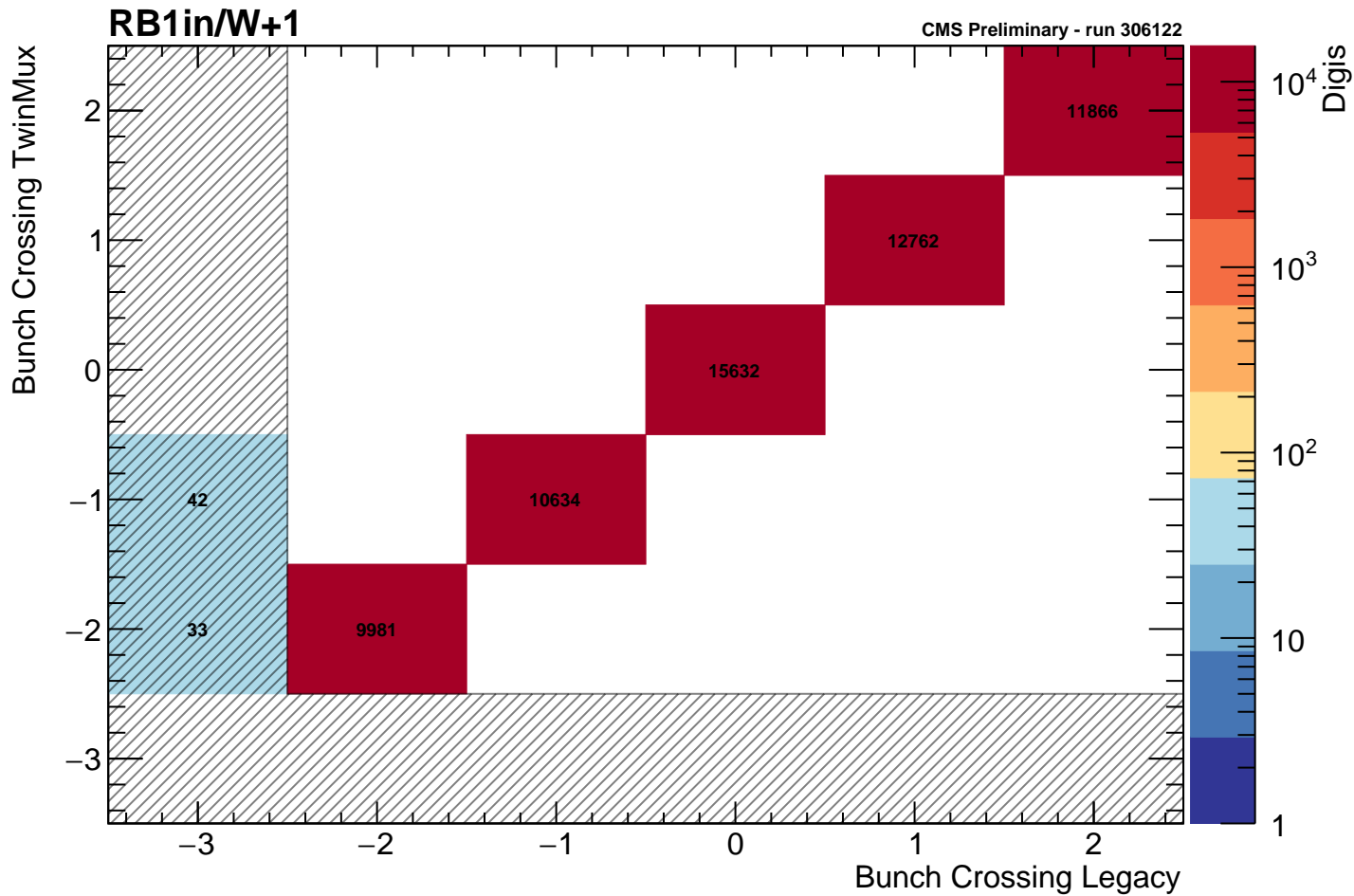


RB1in/W+0

CMS Preliminary - run 306122

Bunch Crossing TwinMux

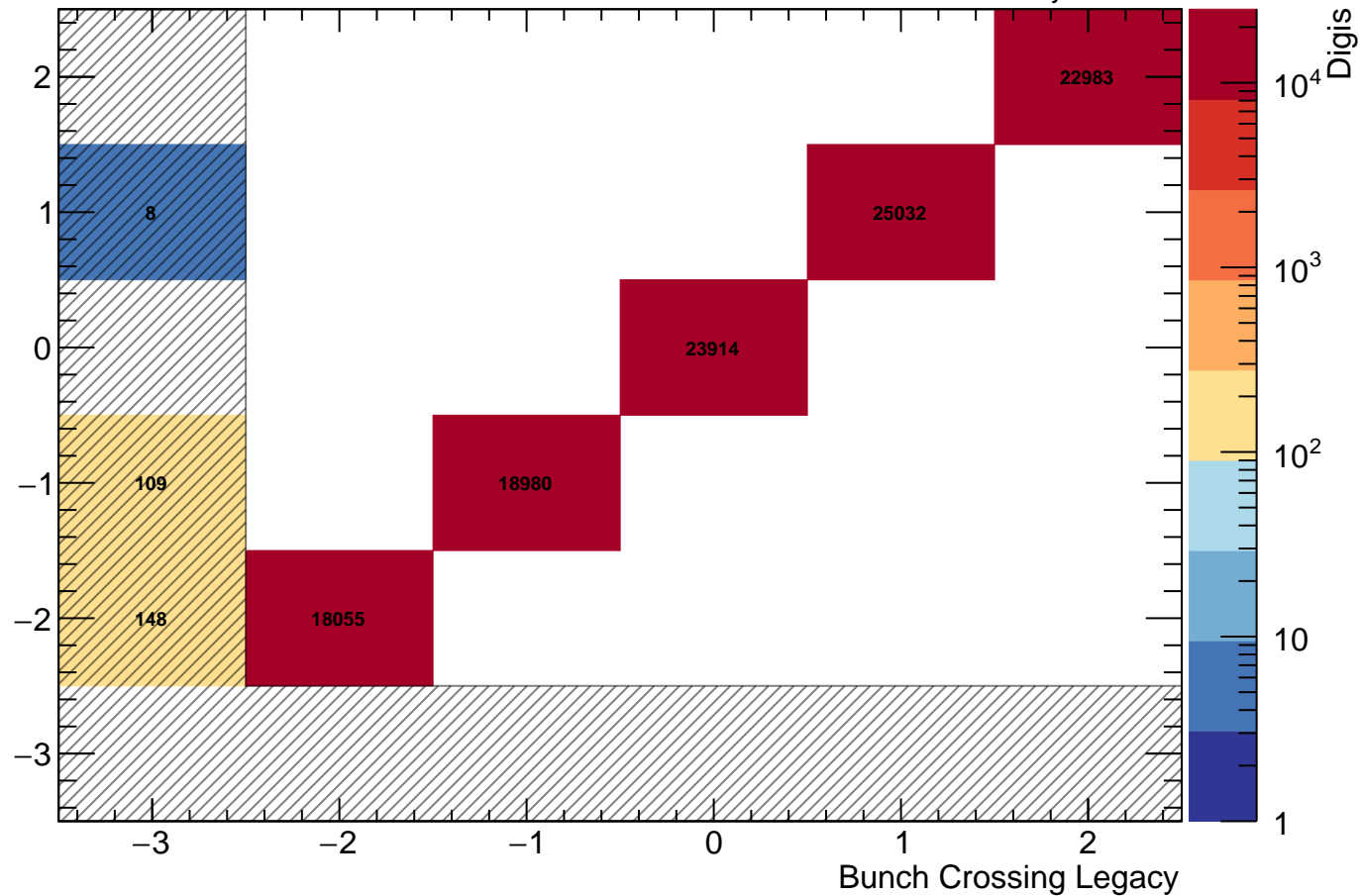




RB1in/W+2

CMS Preliminary - run 306122

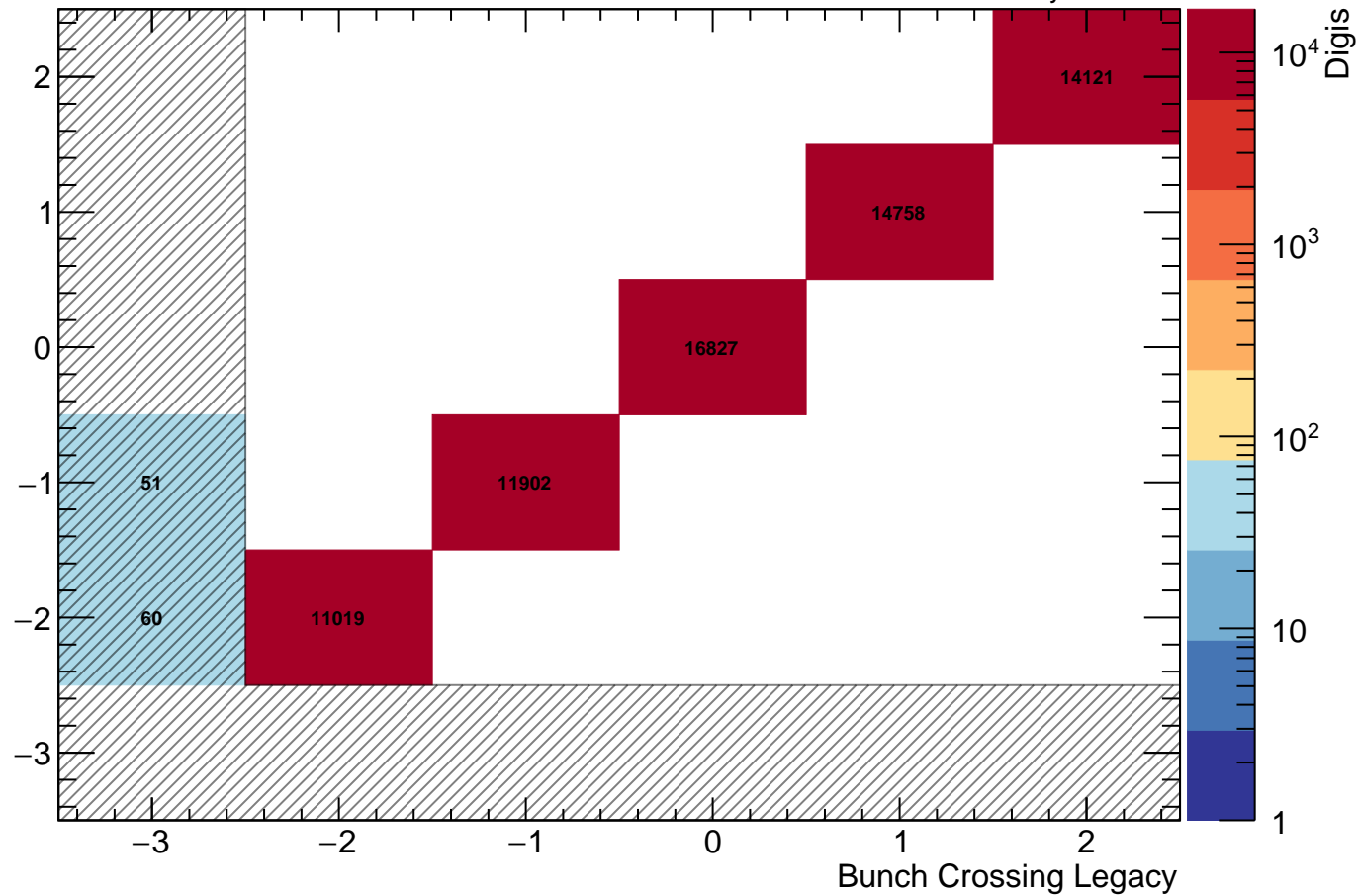
Bunch Crossing TwinMux

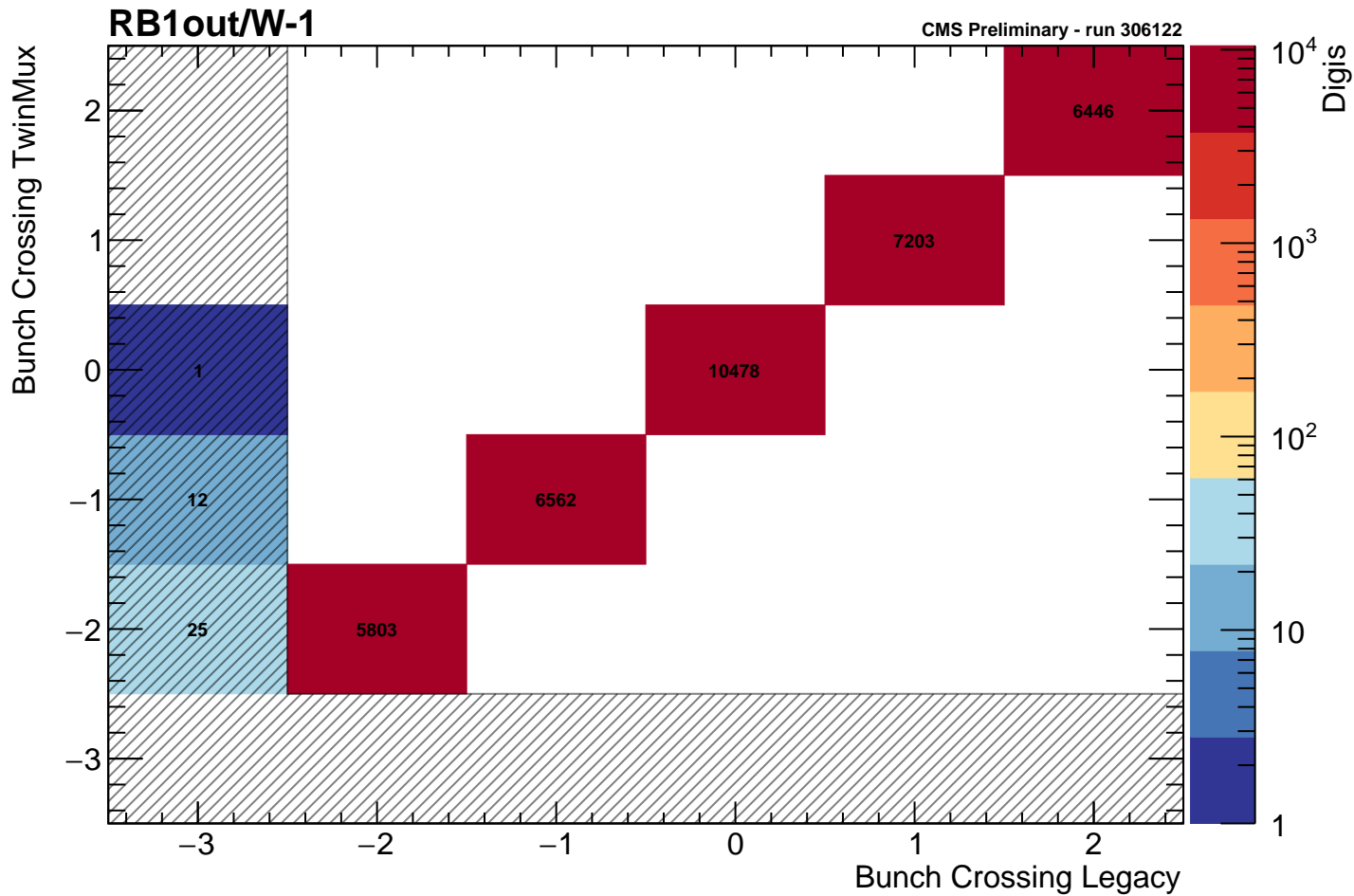


RB1out/W-2

CMS Preliminary - run 306122

Bunch Crossing TwinMux

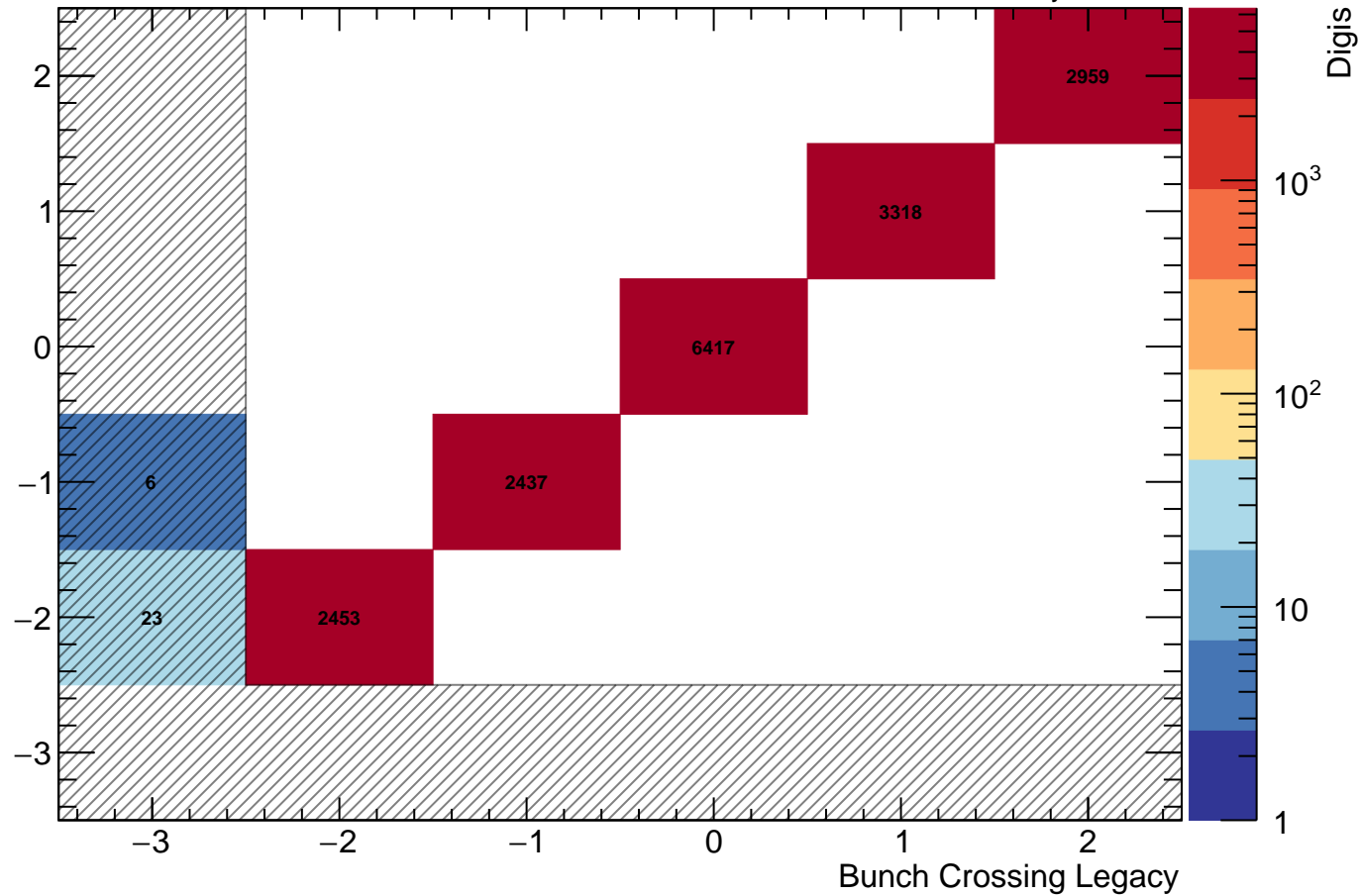


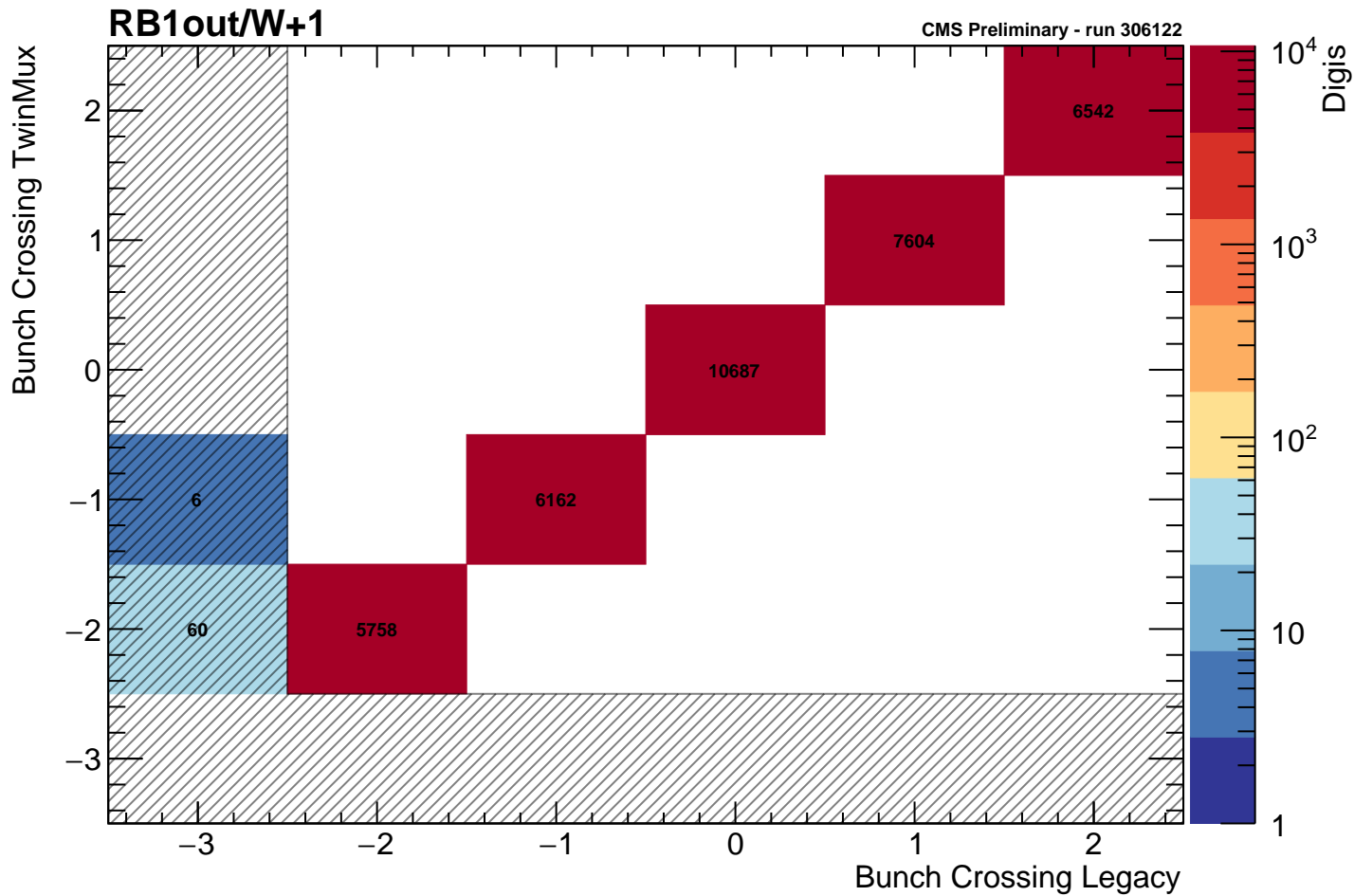


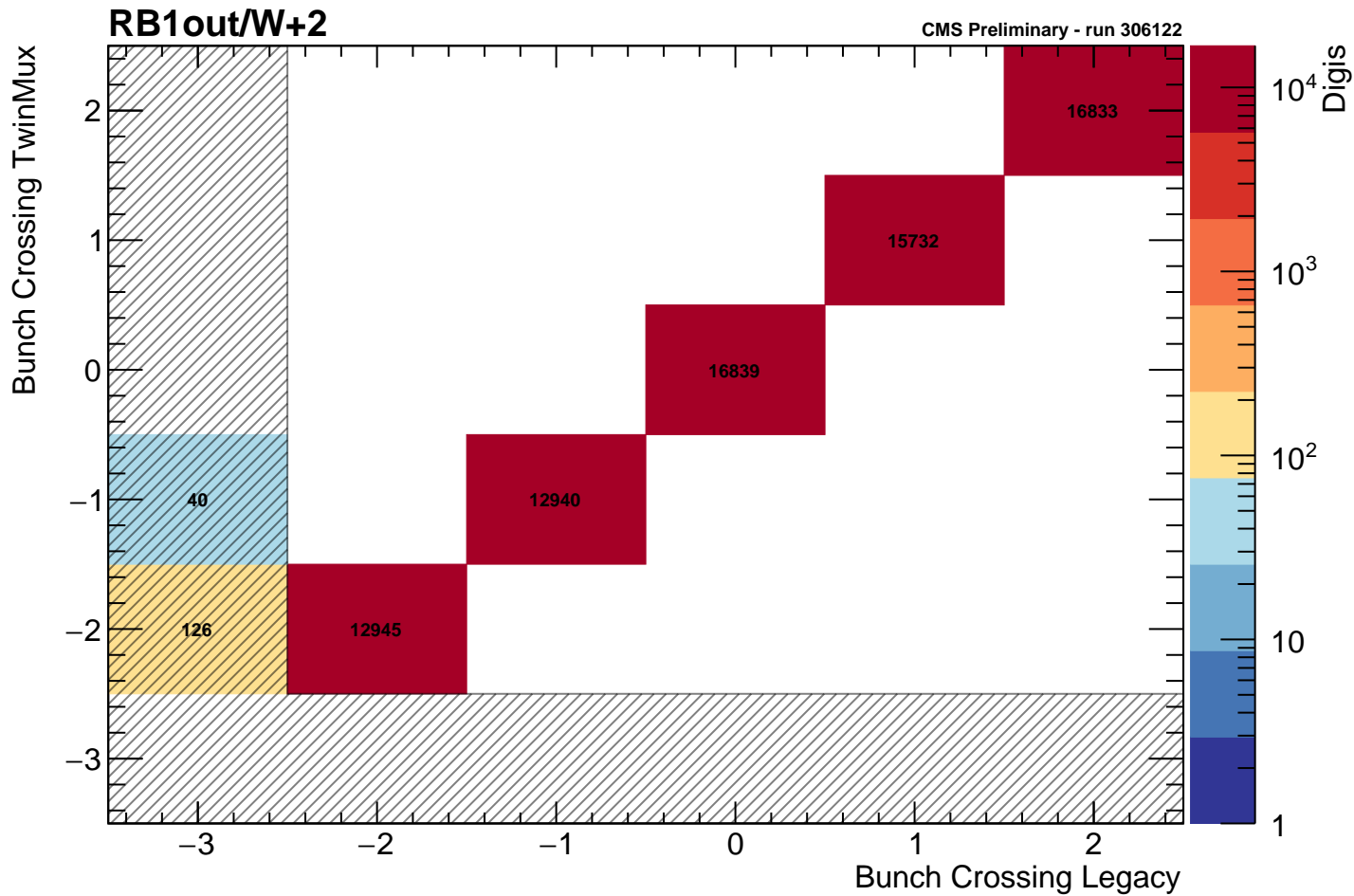
RB1out/W+0

CMS Preliminary - run 306122

Bunch Crossing TwinMux



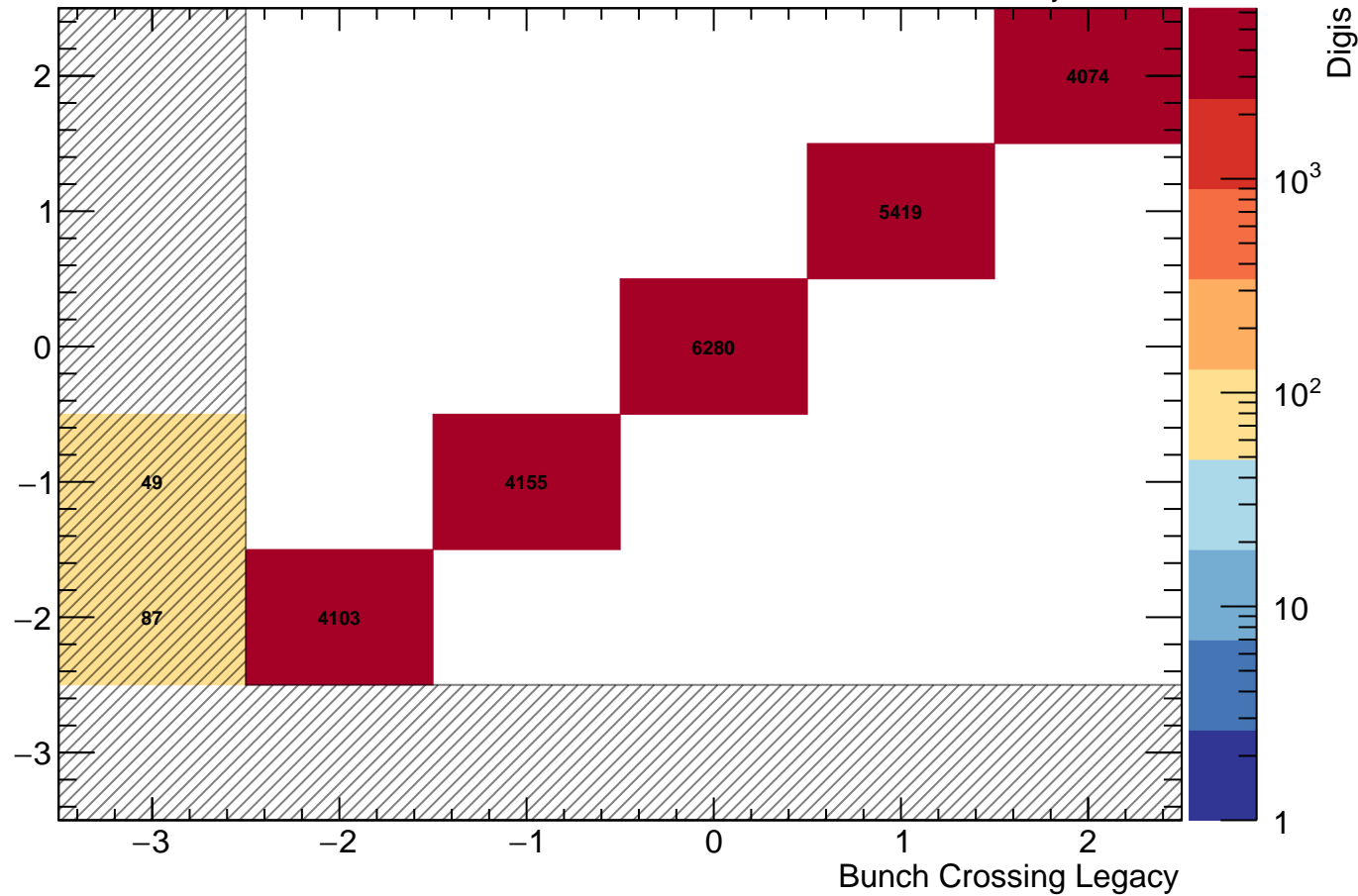




RB2in/W-2

CMS Preliminary - run 306122

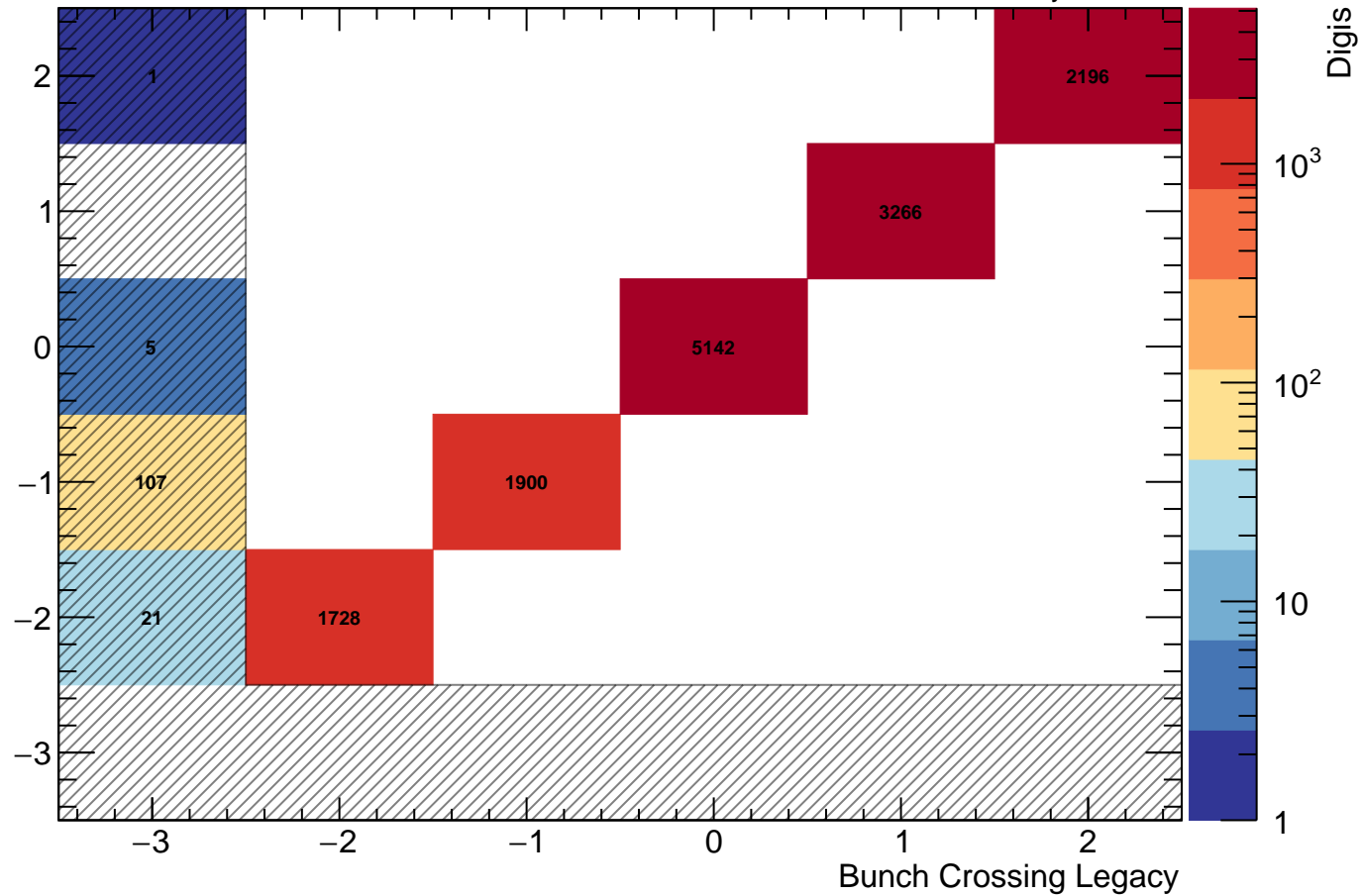
Bunch Crossing TwinMux



RB2in/W-1

CMS Preliminary - run 306122

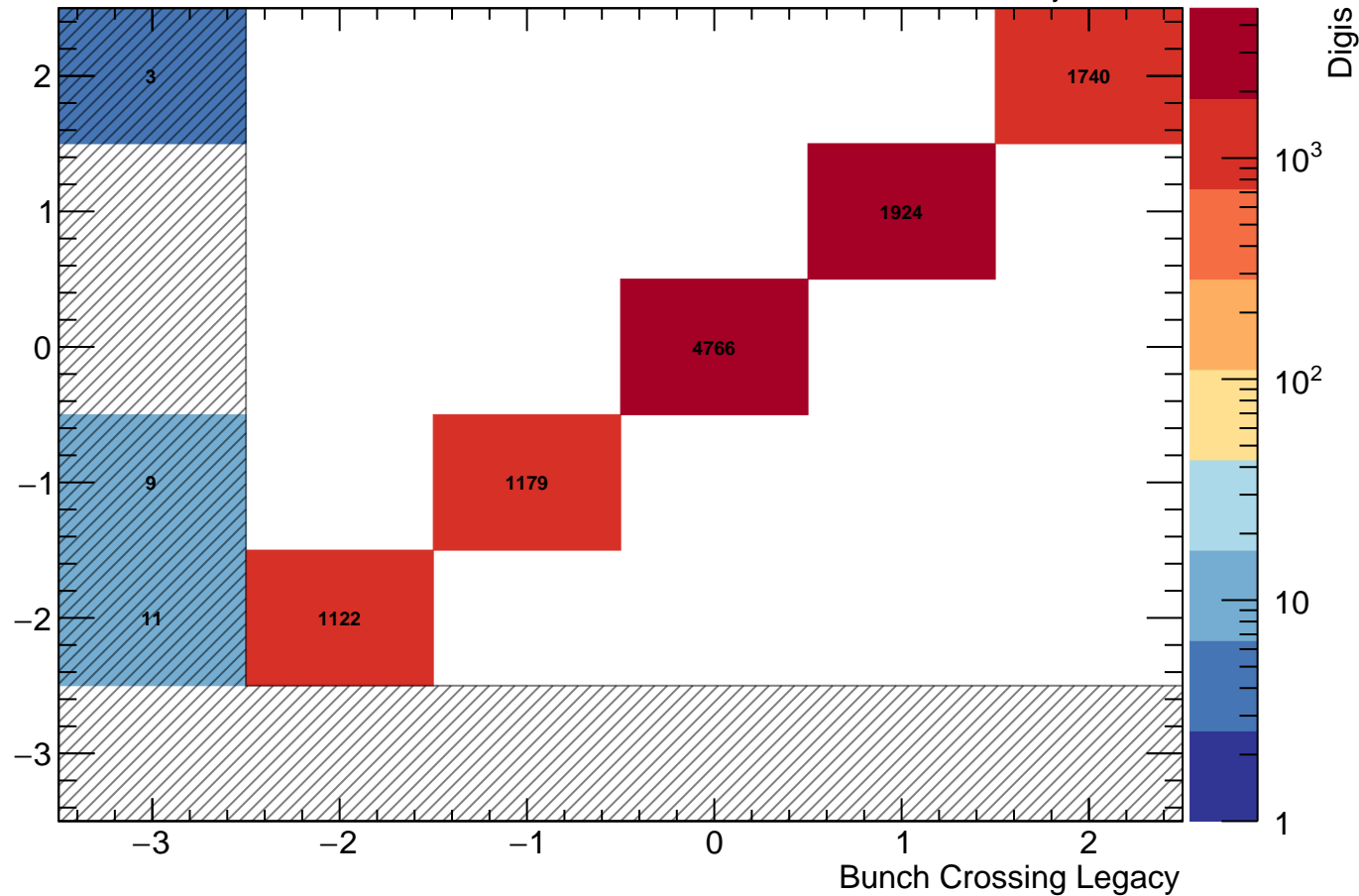
Bunch Crossing TwinMux



RB2in/W+0

CMS Preliminary - run 306122

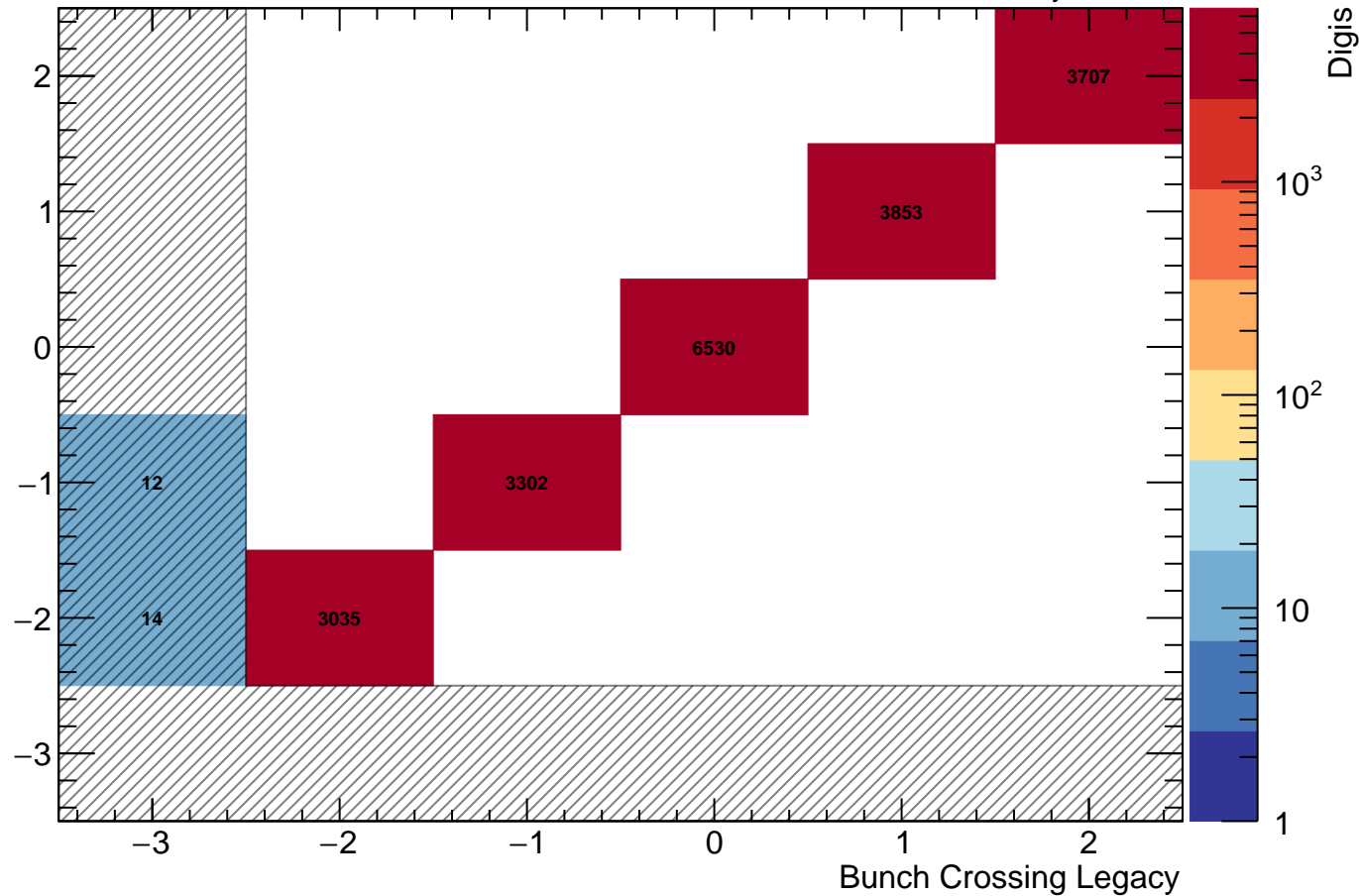
Bunch Crossing TwinMux



RB2in/W+1

CMS Preliminary - run 306122

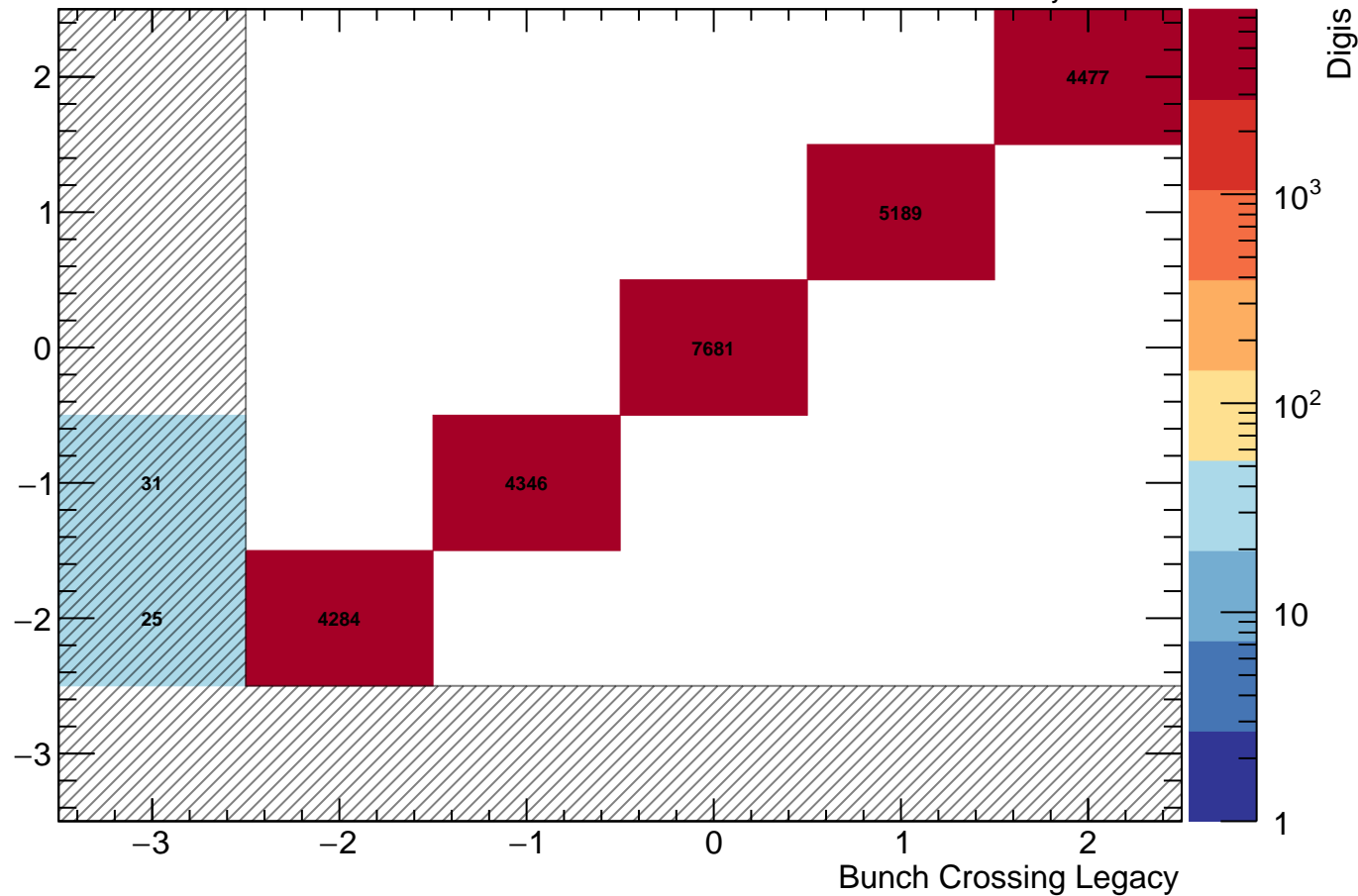
Bunch Crossing TwinMux



RB2in/W+2

CMS Preliminary - run 306122

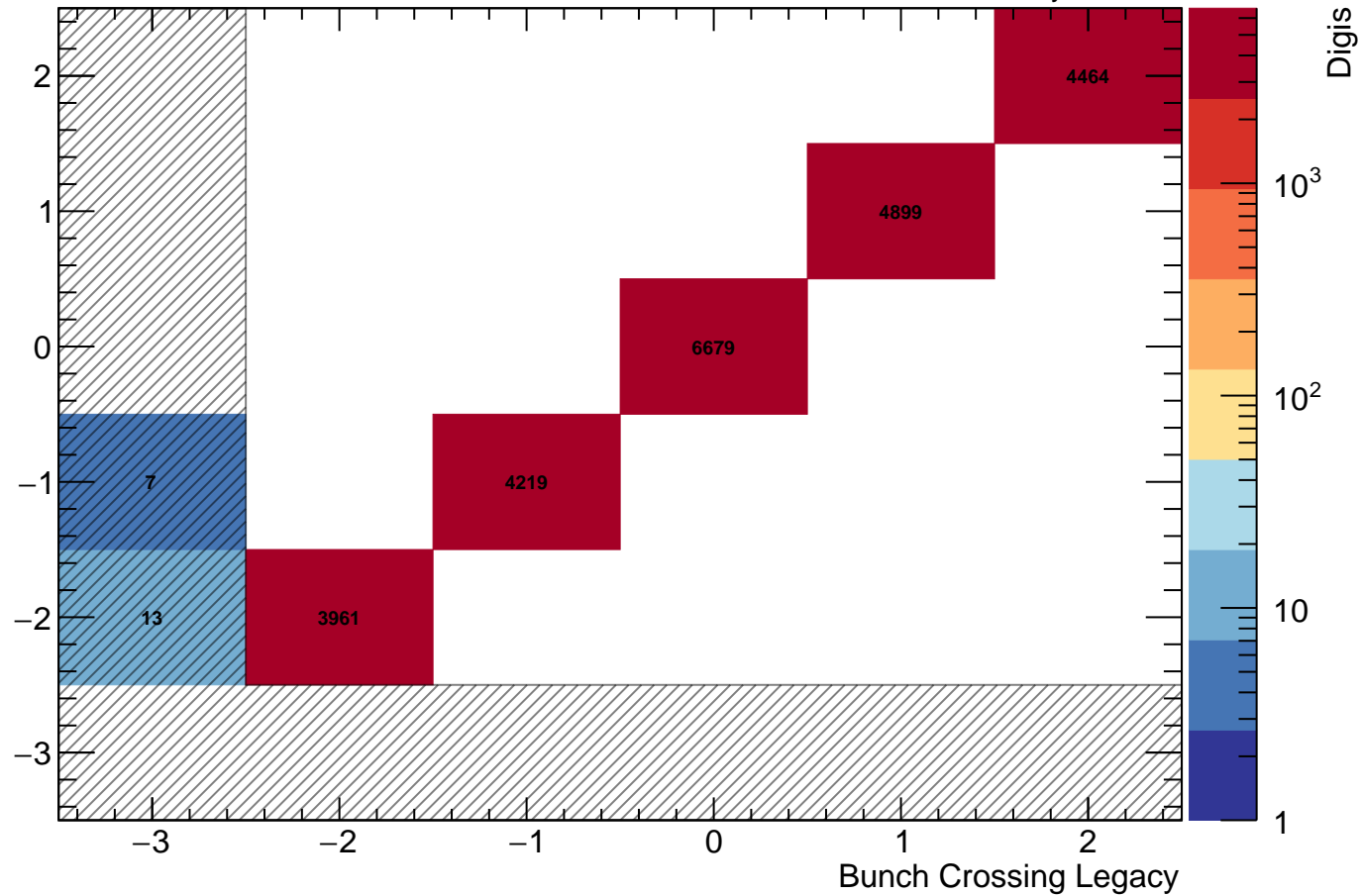
Bunch Crossing TwinMux



RB2out/W-2

CMS Preliminary - run 306122

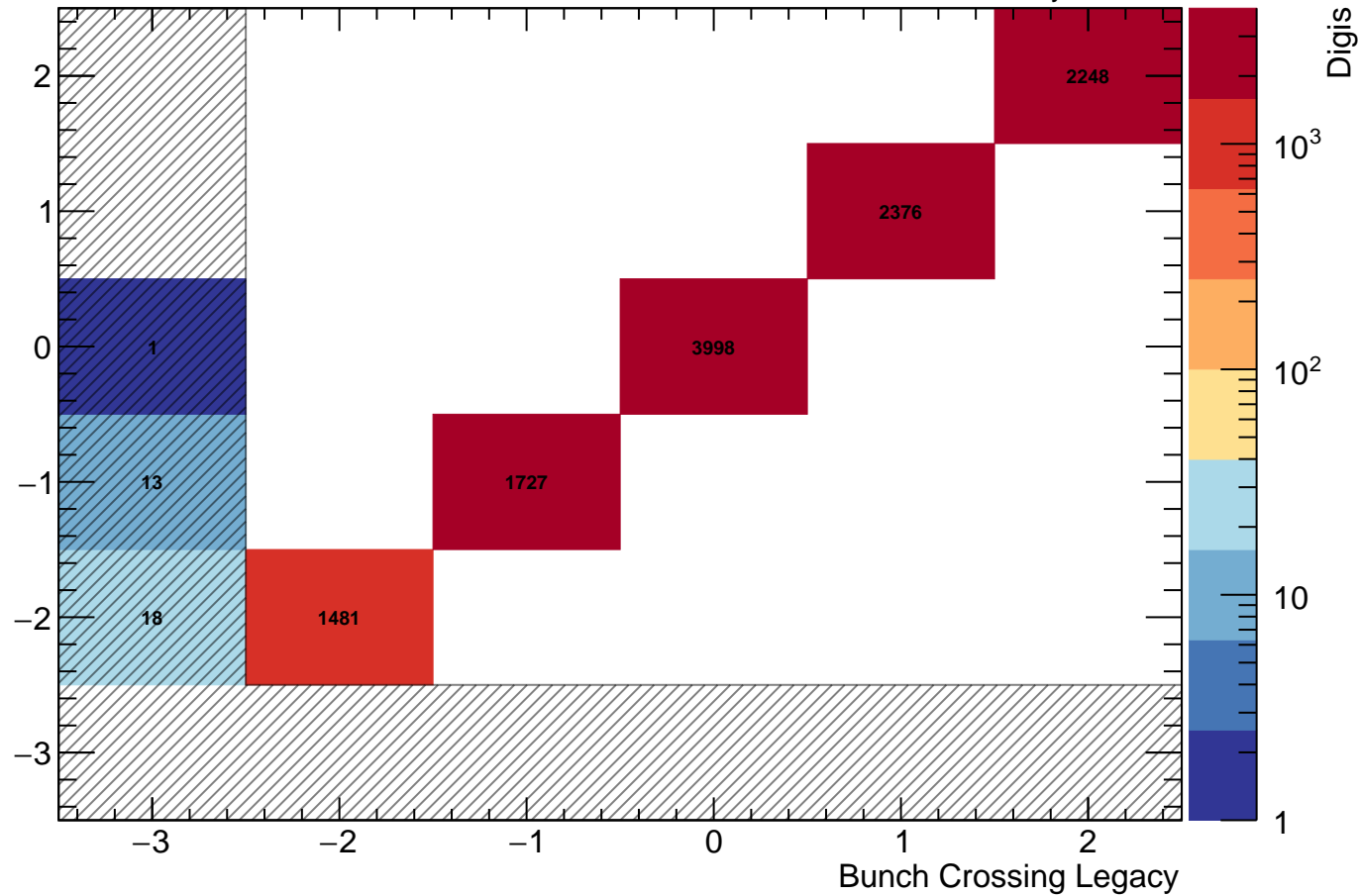
Bunch Crossing TwinMux



RB2out/W-1

CMS Preliminary - run 306122

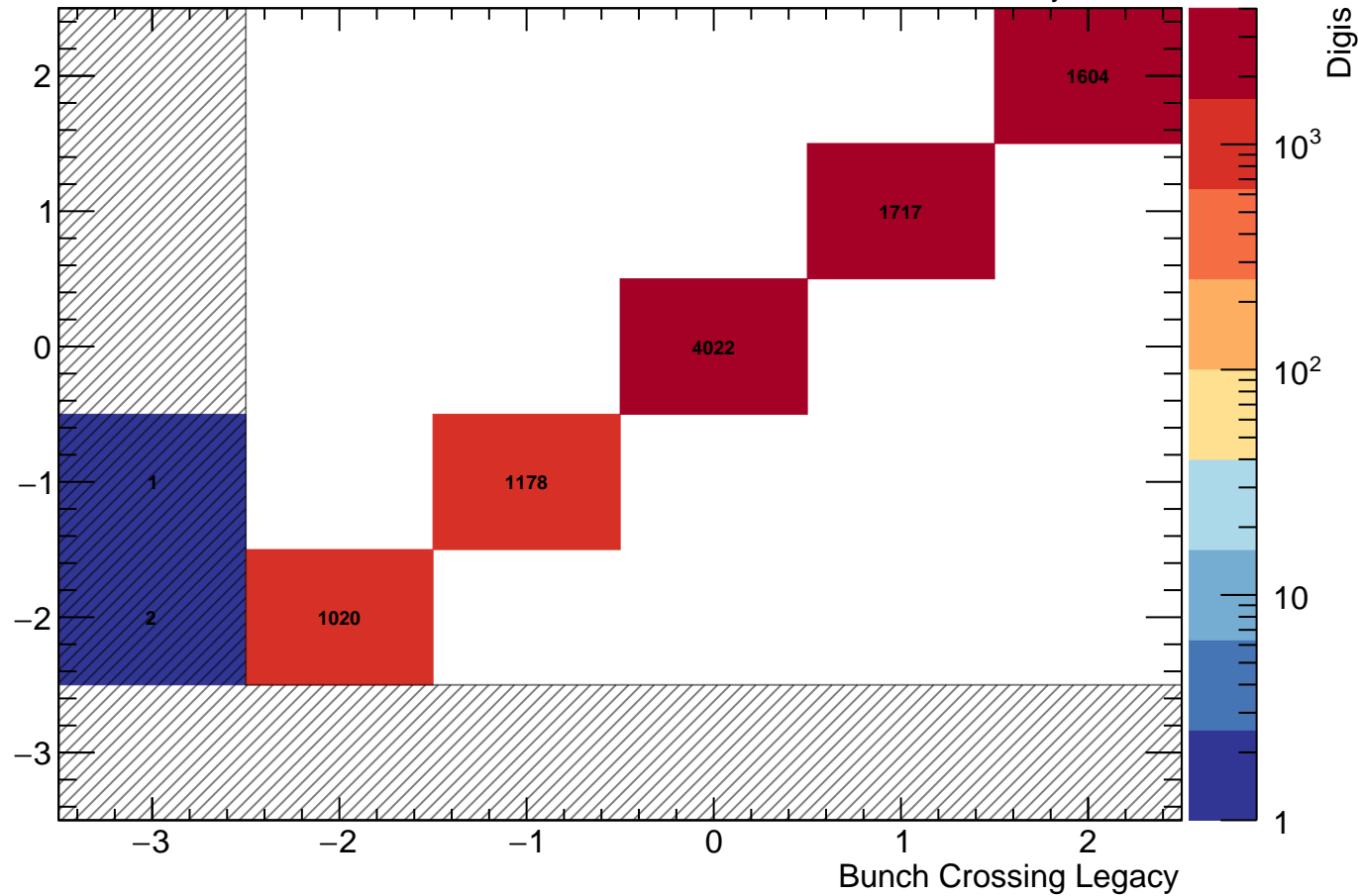
Bunch Crossing TwinMux



RB2out/W+0

CMS Preliminary - run 306122

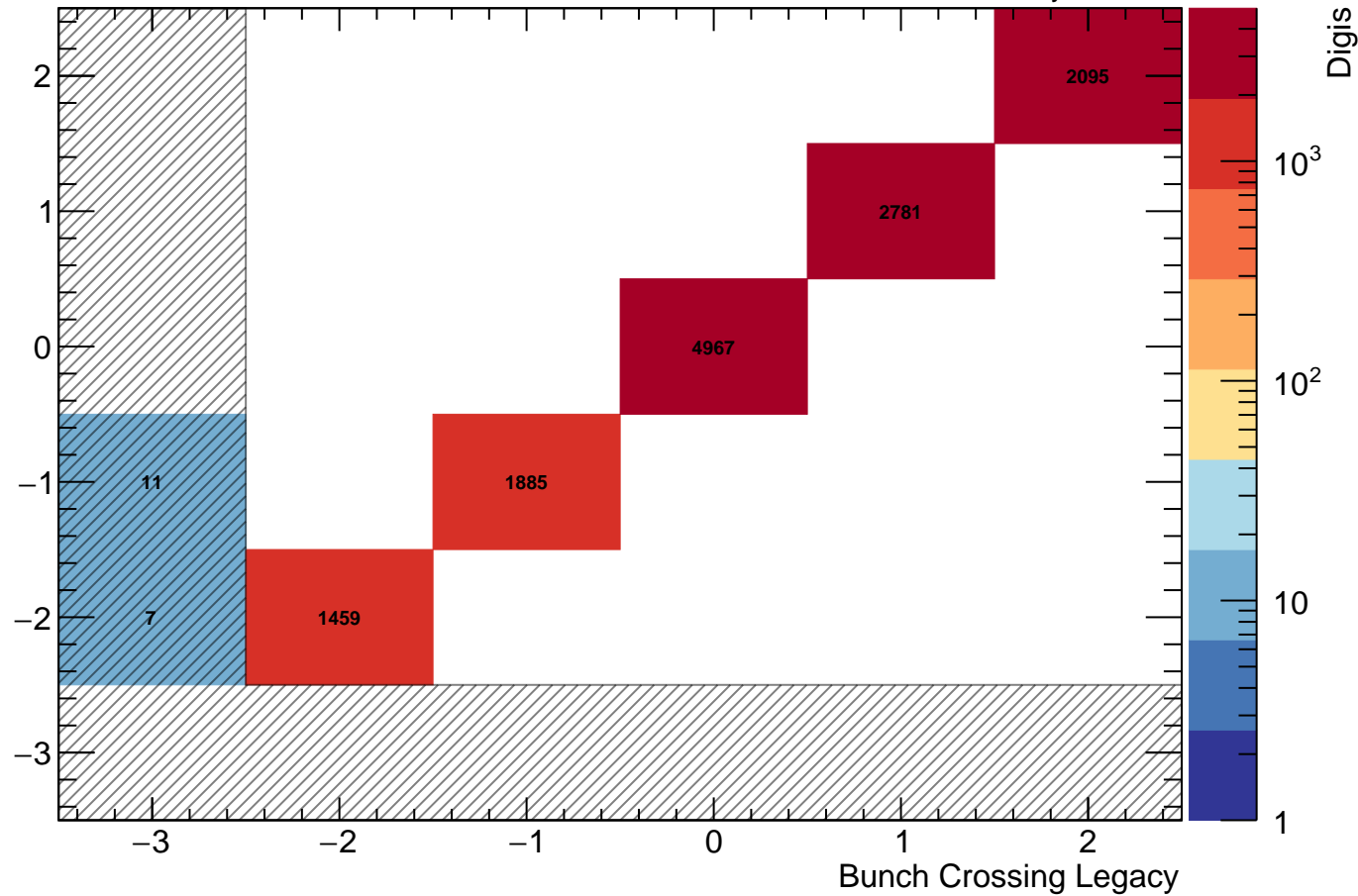
Bunch Crossing TwinMux



RB2out/W+1

CMS Preliminary - run 306122

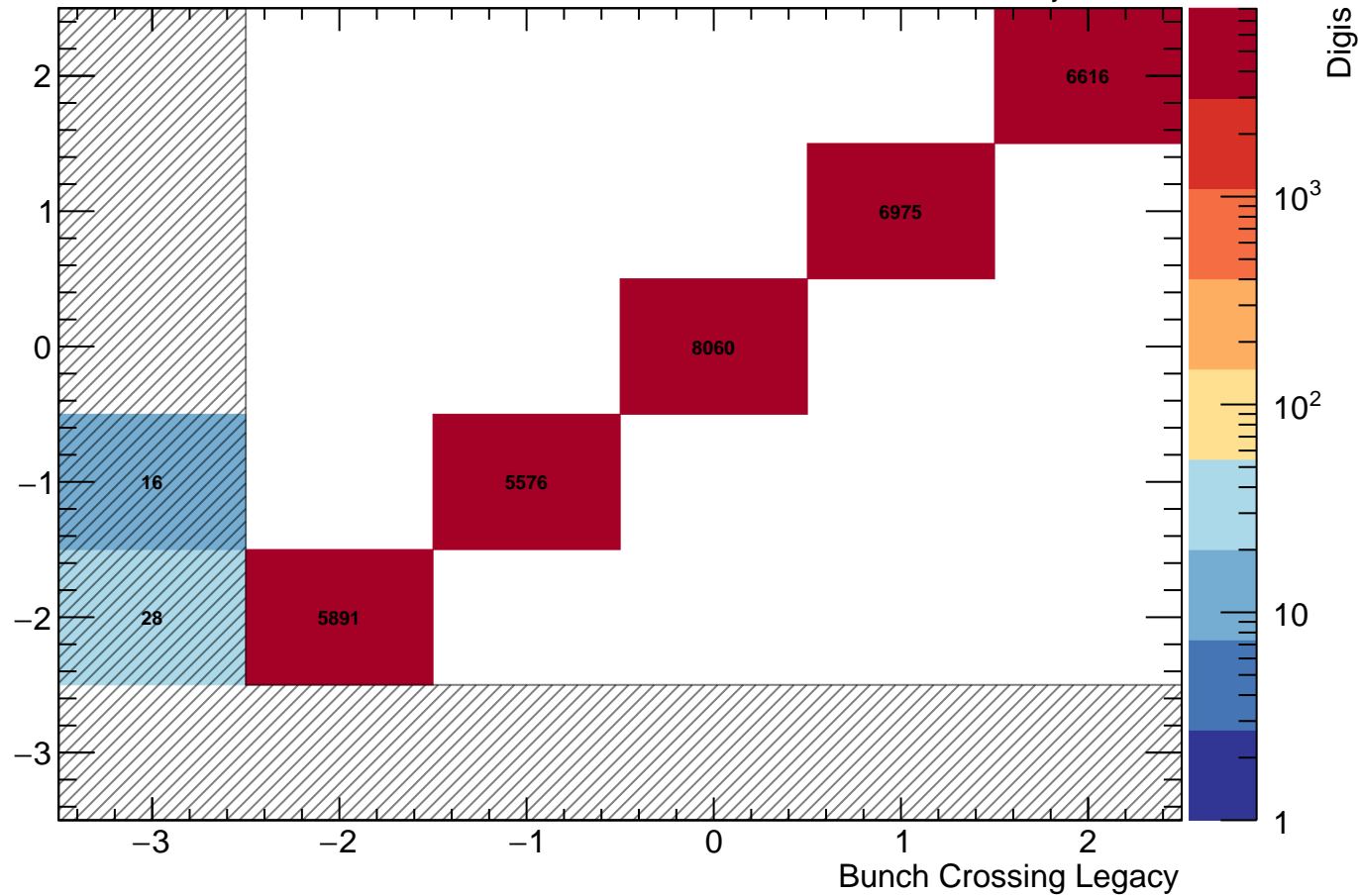
Bunch Crossing TwinMux



RB2out/W+2

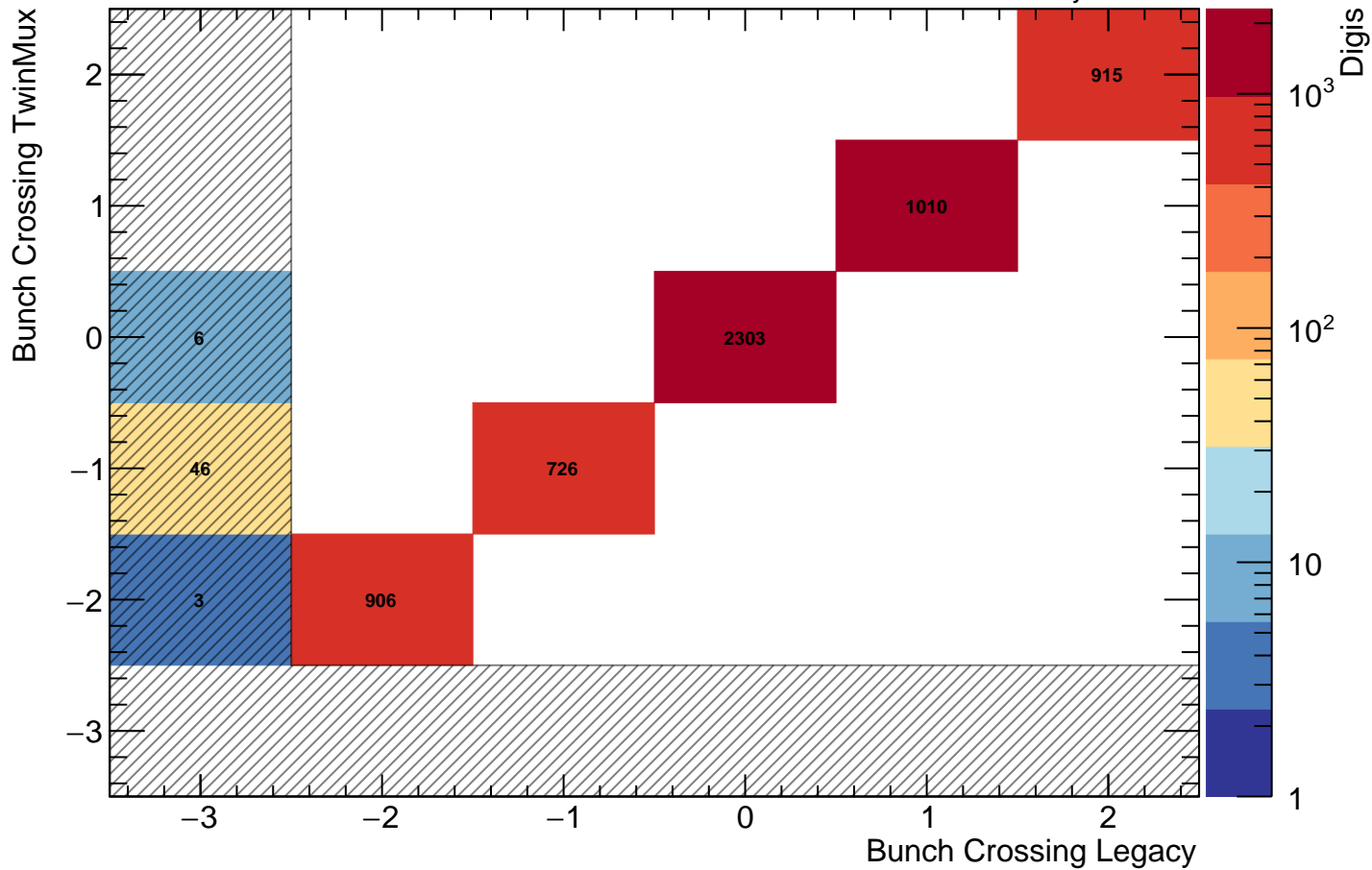
CMS Preliminary - run 306122

Bunch Crossing TwinMux



RB3/W-2

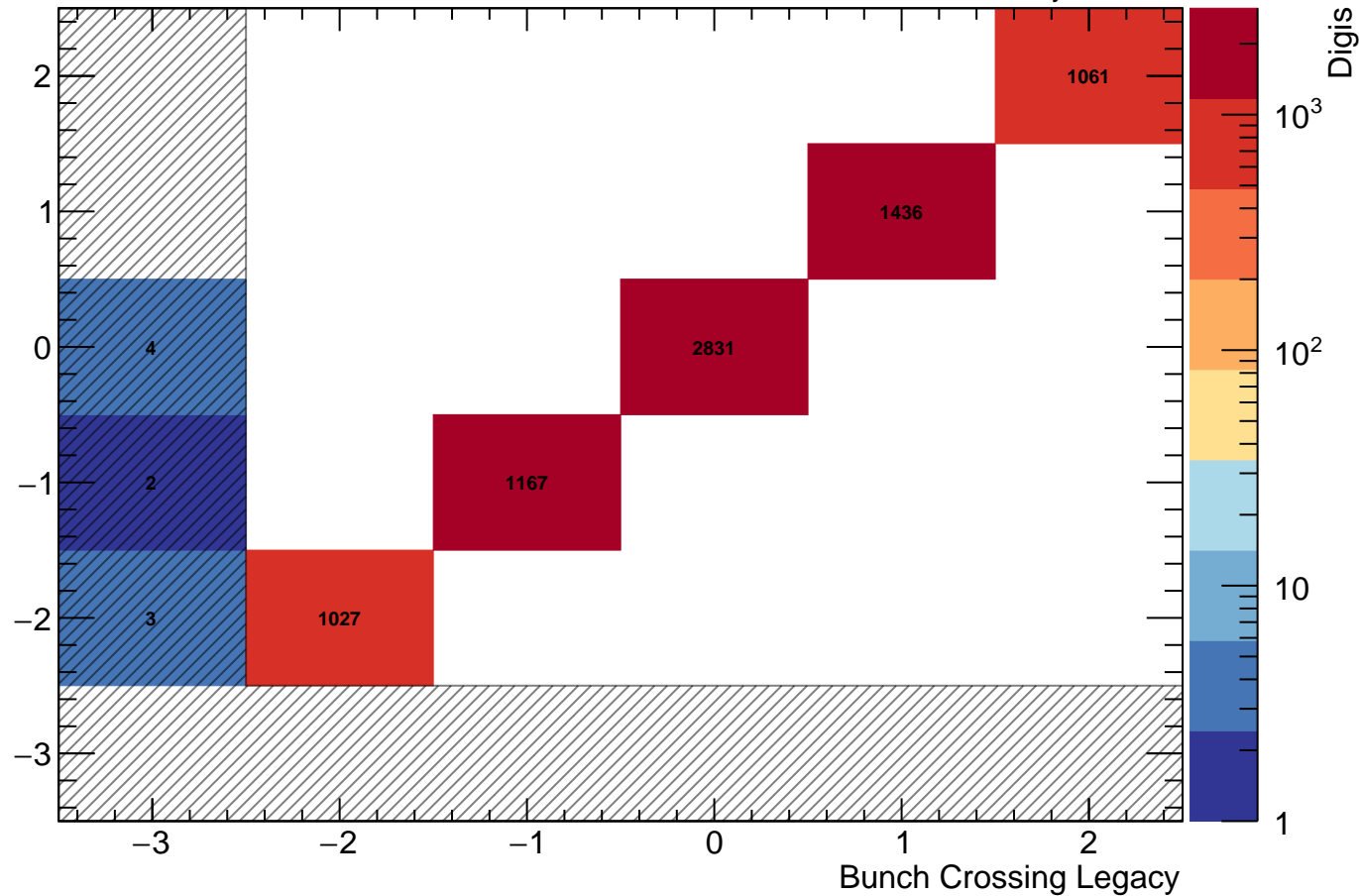
CMS Preliminary - run 306122

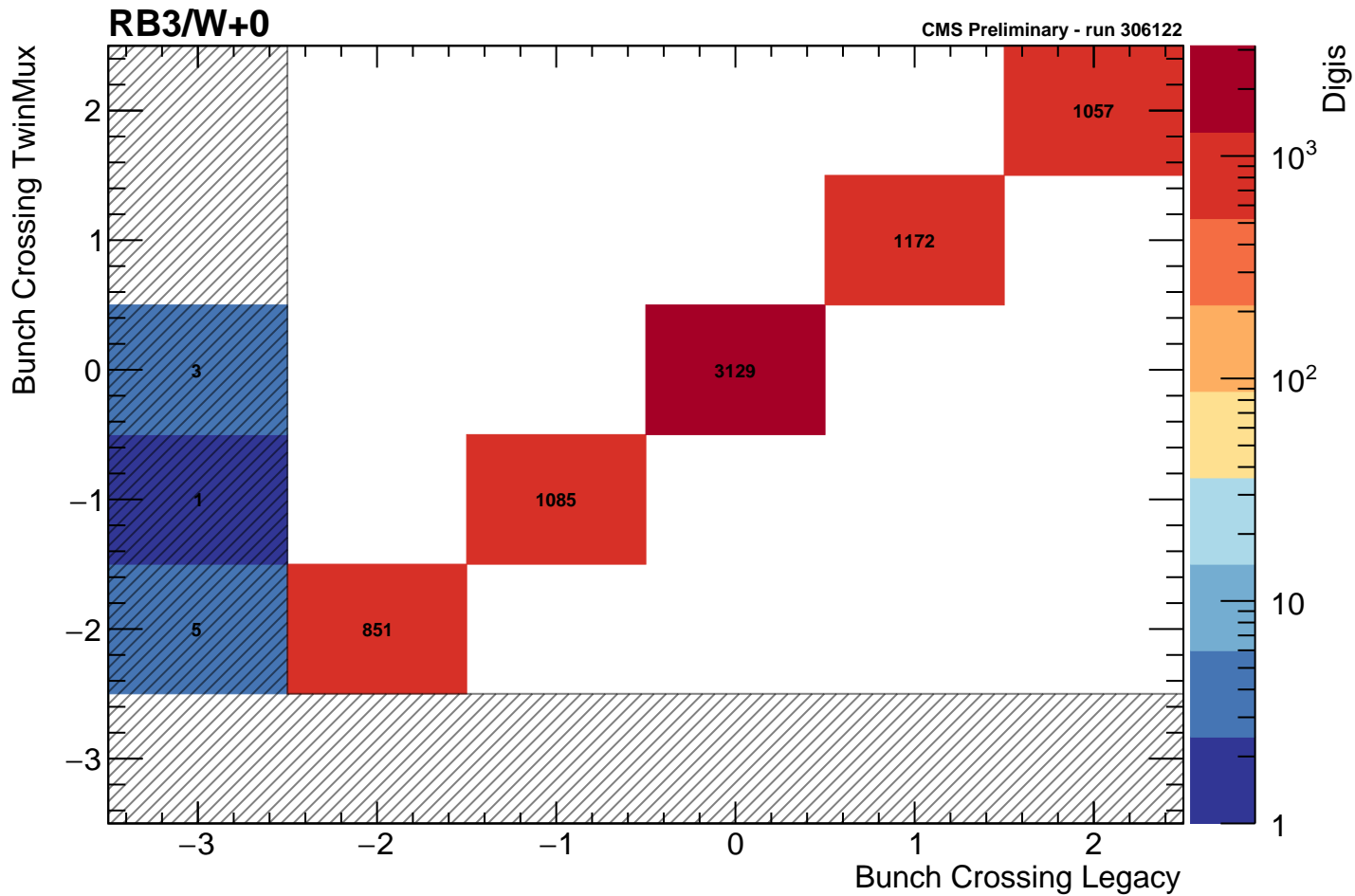


RB3/W-1

CMS Preliminary - run 306122

Bunch Crossing TwinMux

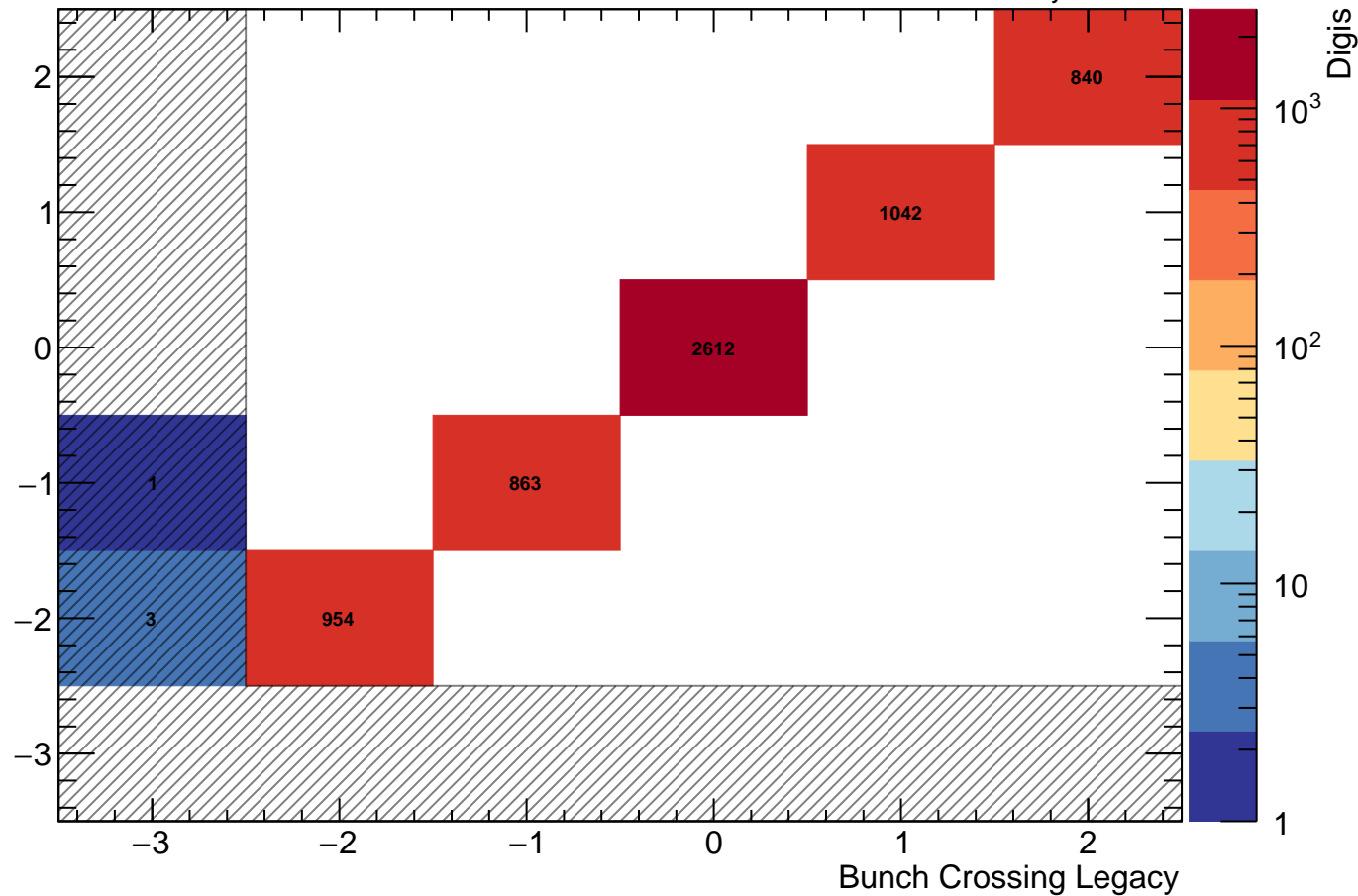




RB3/W+1

CMS Preliminary - run 306122

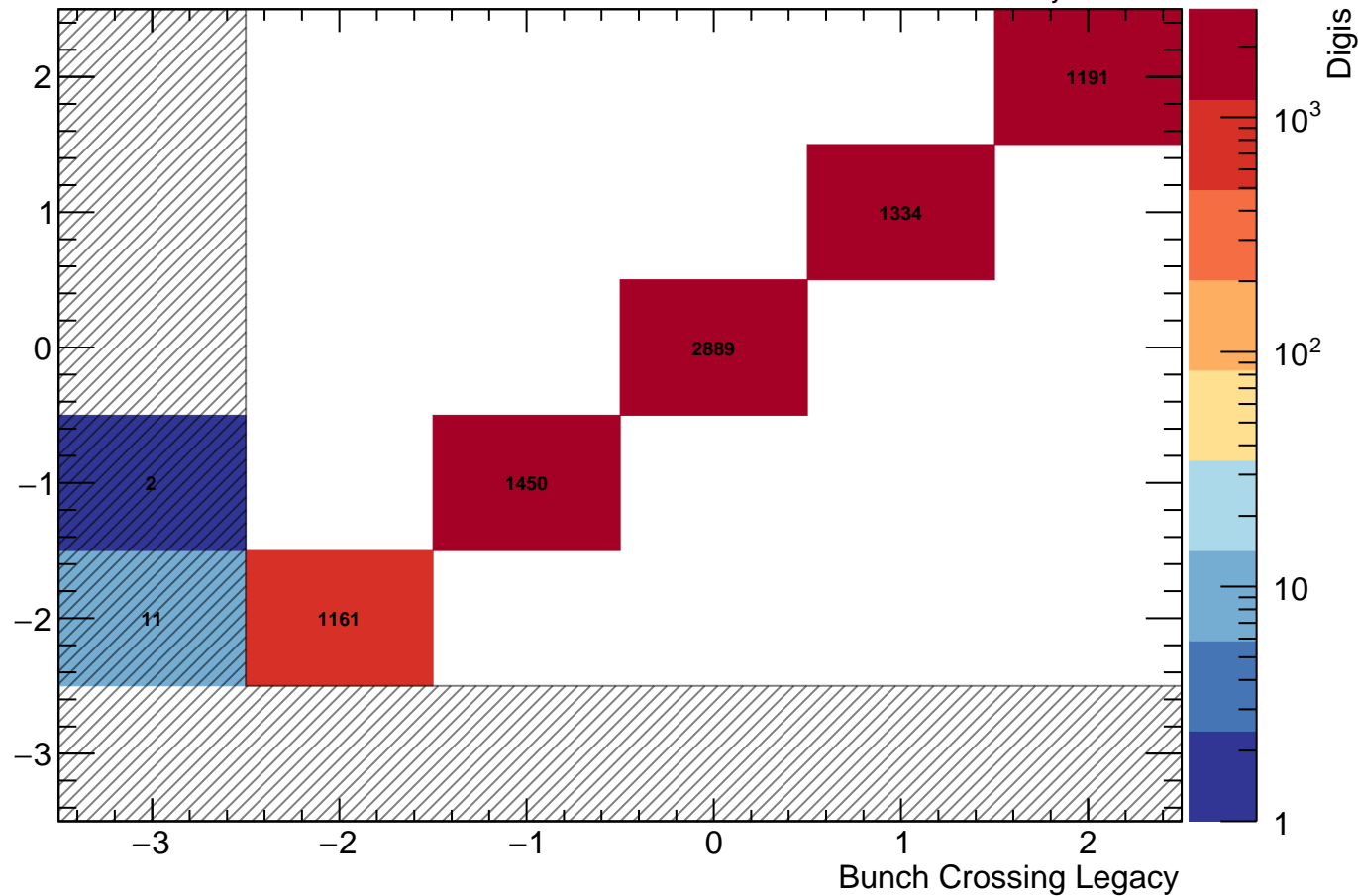
Bunch Crossing TwinMux



RB3/W+2

CMS Preliminary - run 306122

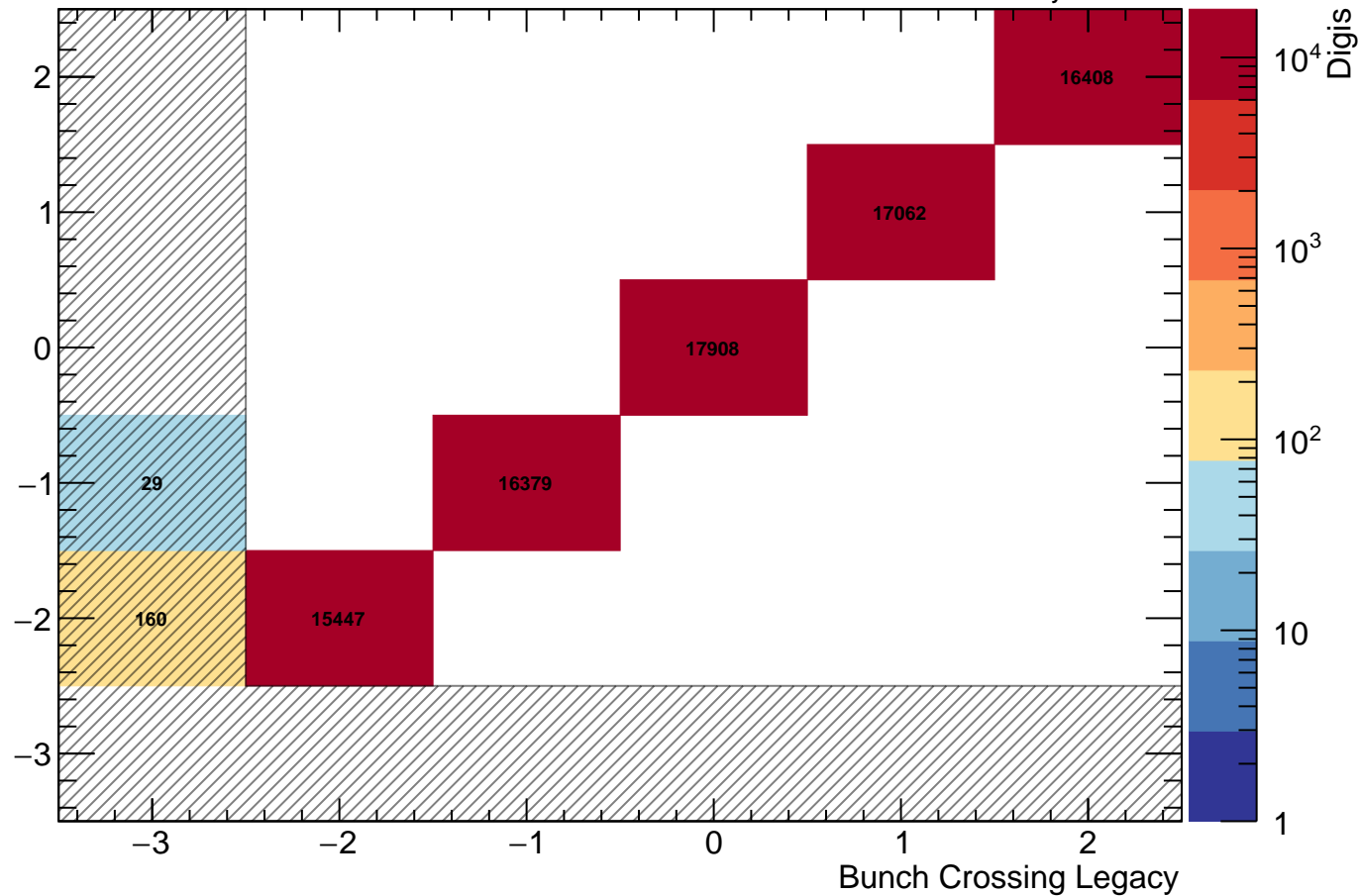
Bunch Crossing TwinMux



RB4/W-2

CMS Preliminary - run 306122

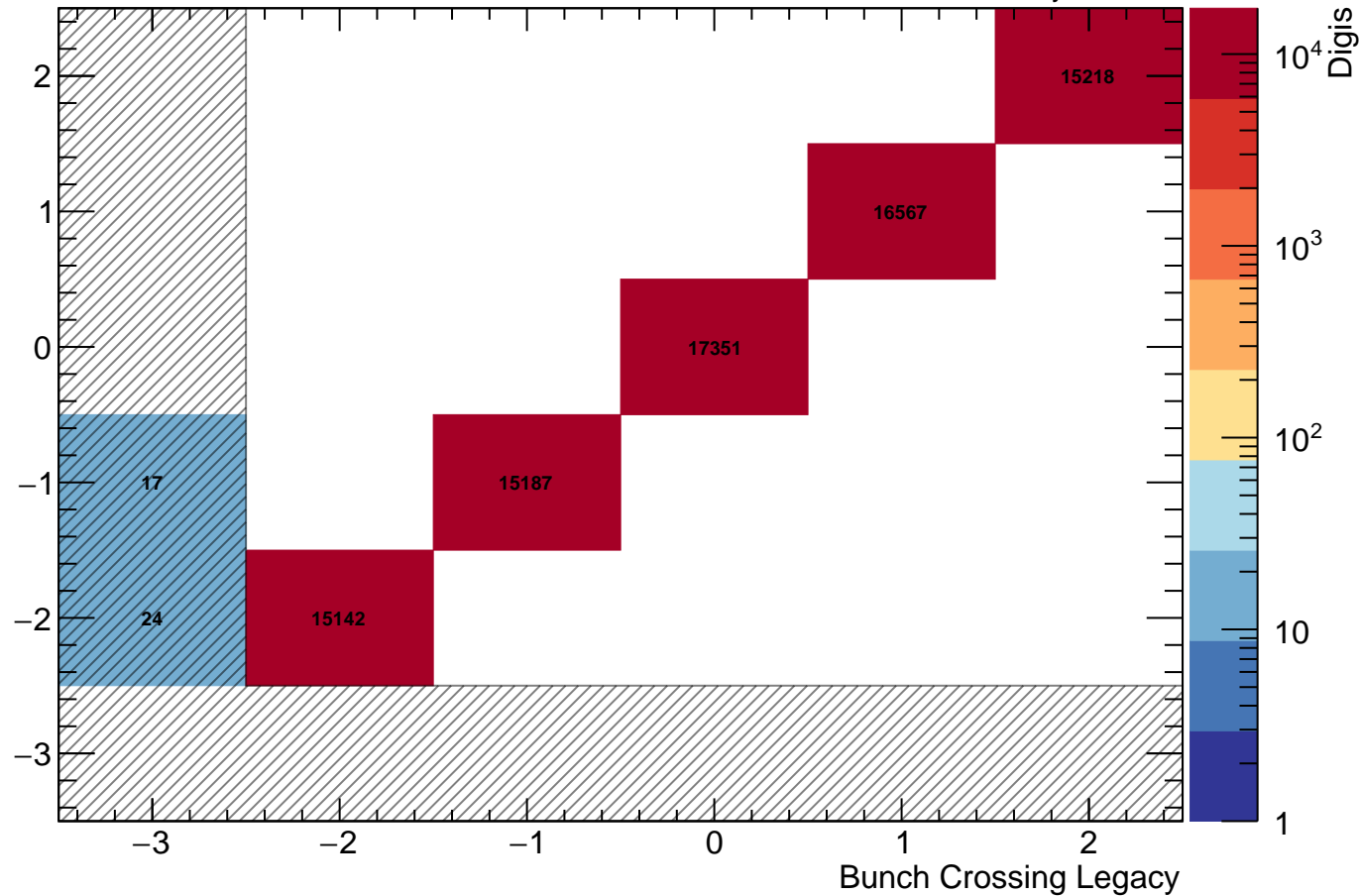
Bunch Crossing TwinMux



RB4/W-1

CMS Preliminary - run 306122

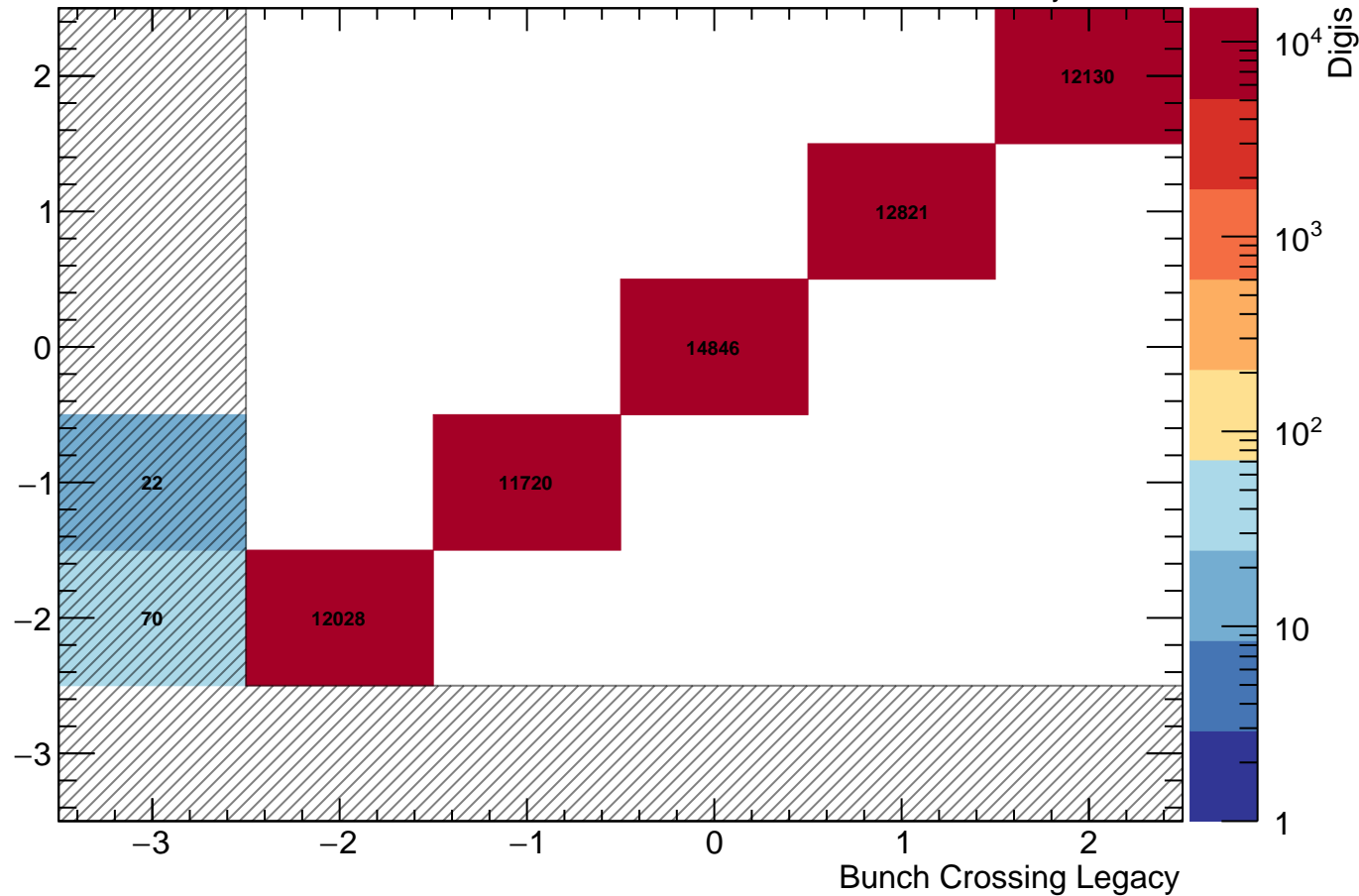
Bunch Crossing TwinMux

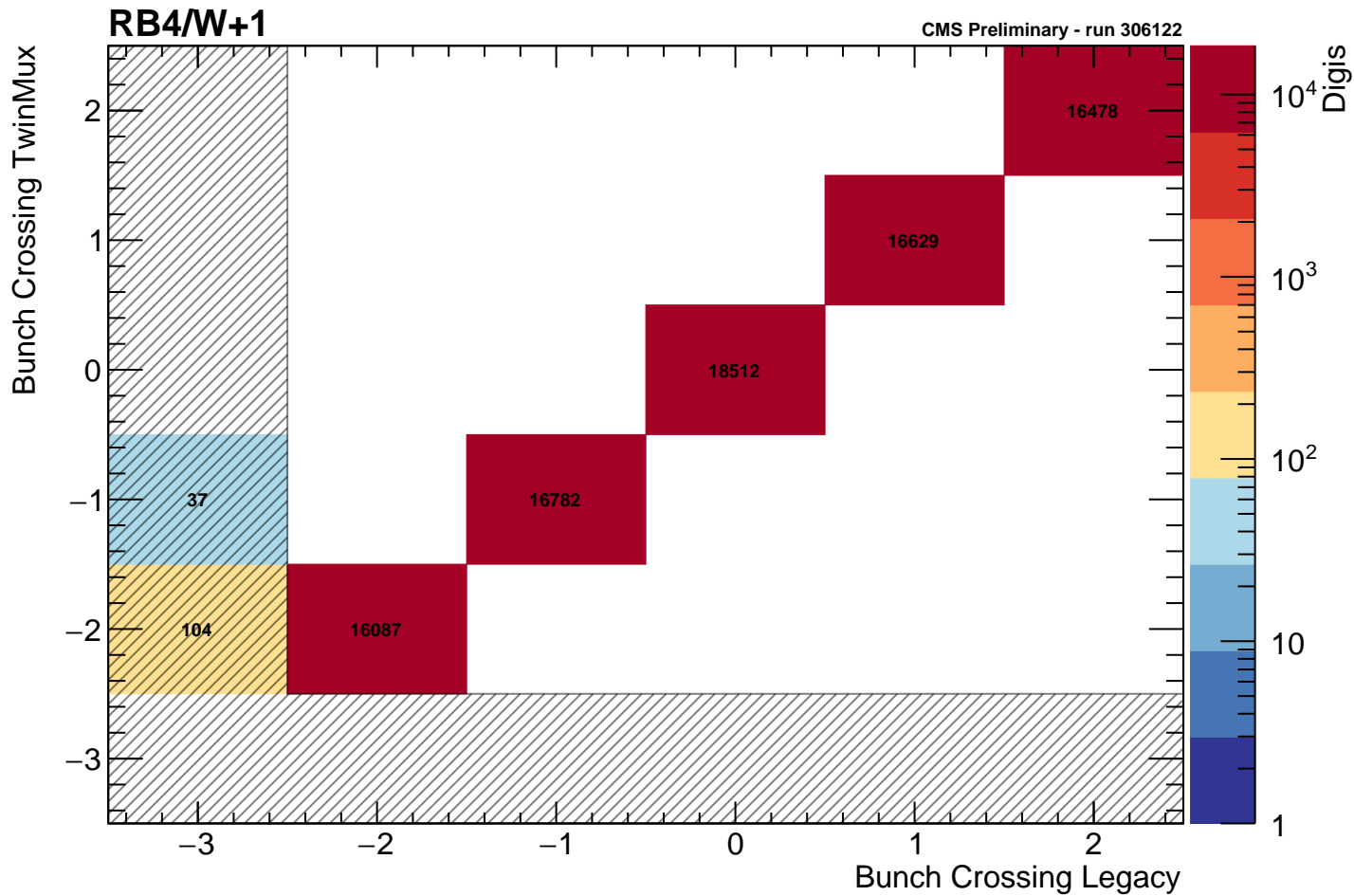


RB4/W+0

CMS Preliminary - run 306122

Bunch Crossing TwinMux

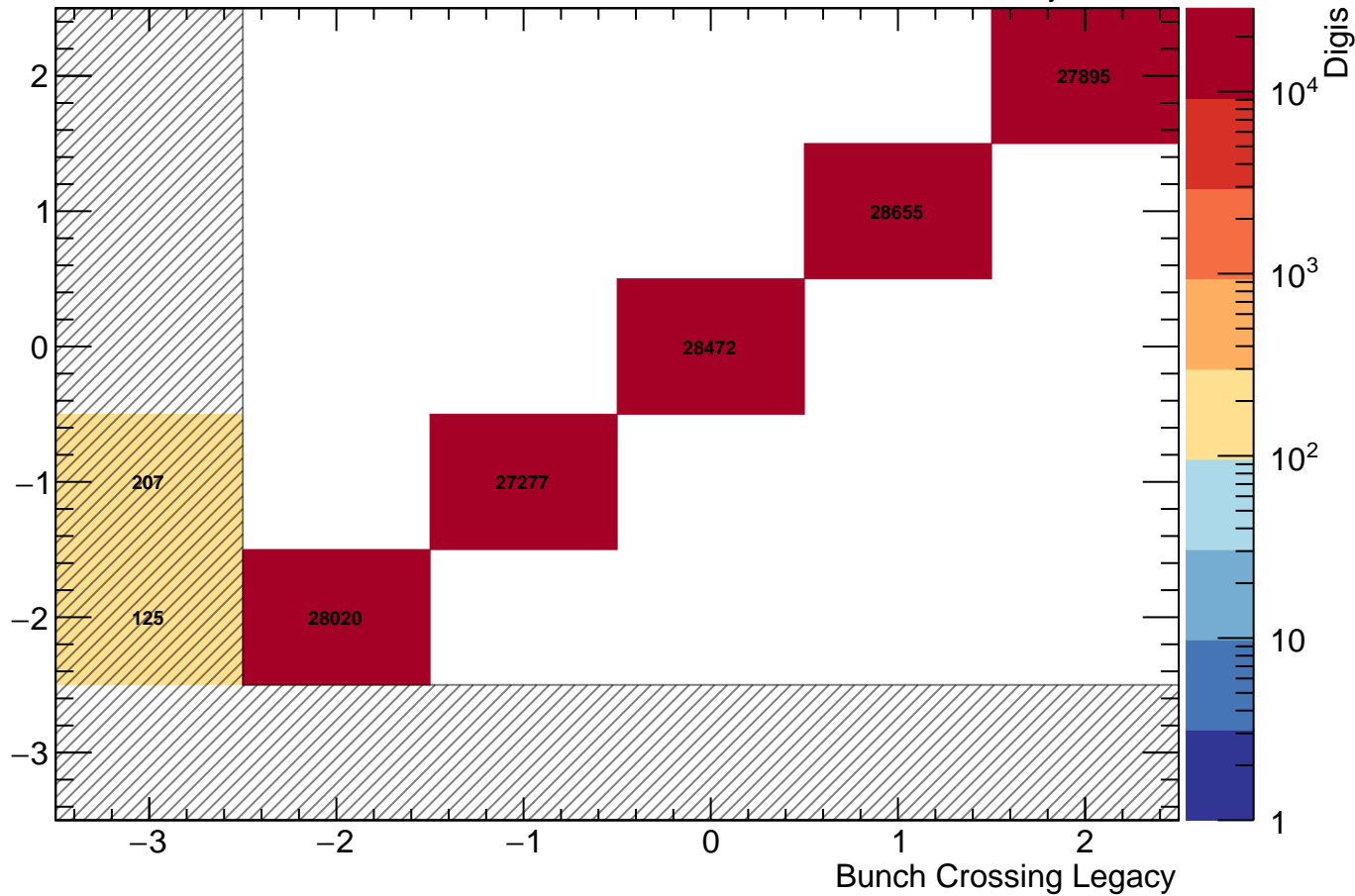




RB4/W+2

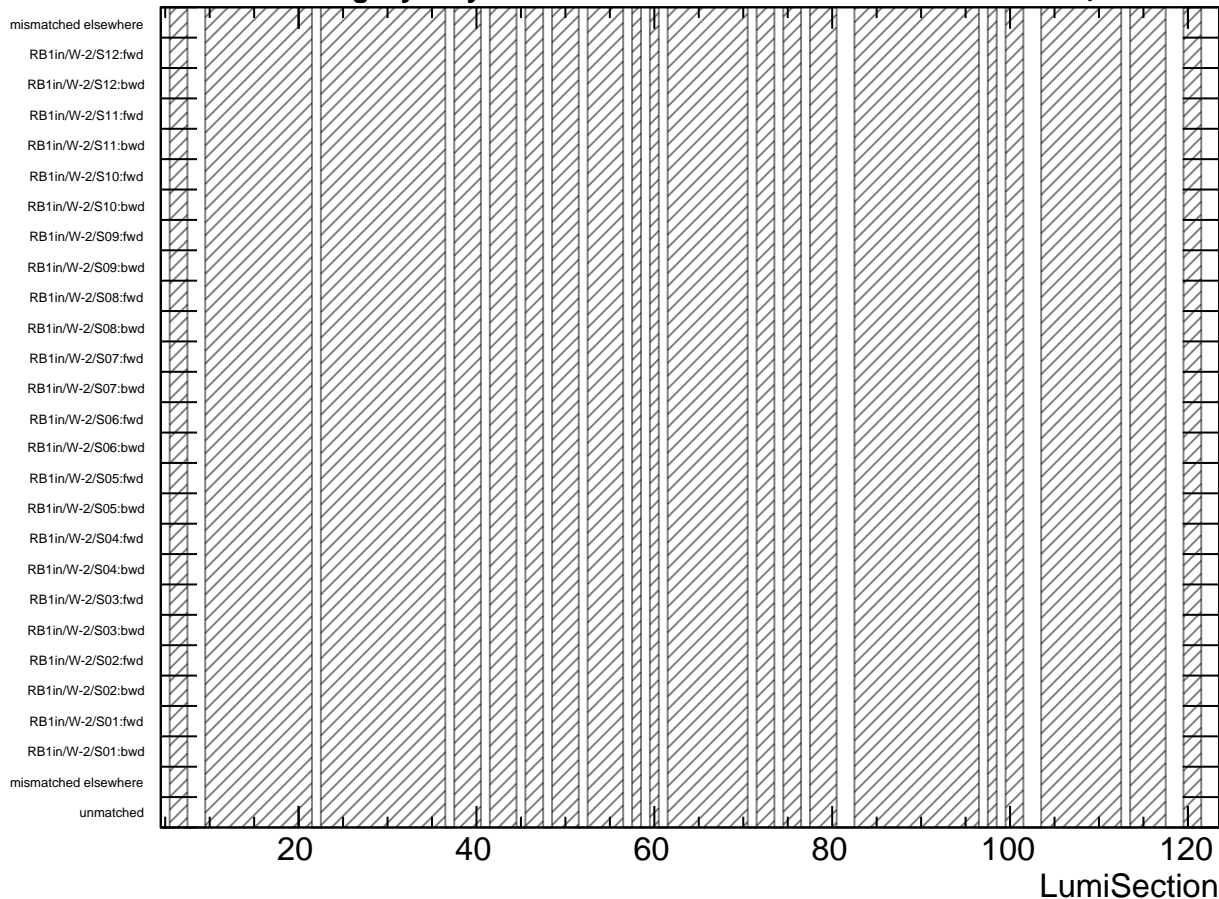
CMS Preliminary - run 306122

Bunch Crossing TwinMux



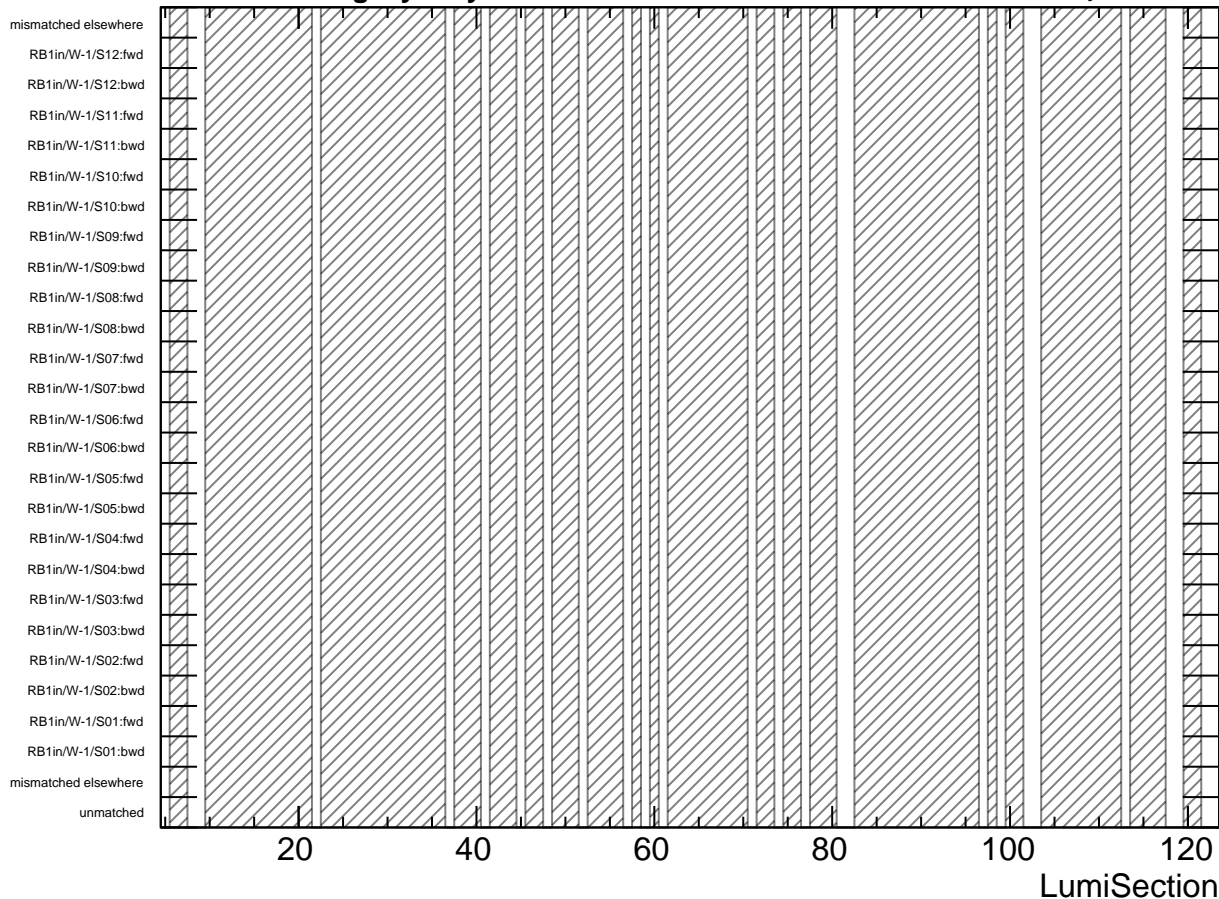
RB1in/W-2: Legacy only

CMS Preliminary - run 306122



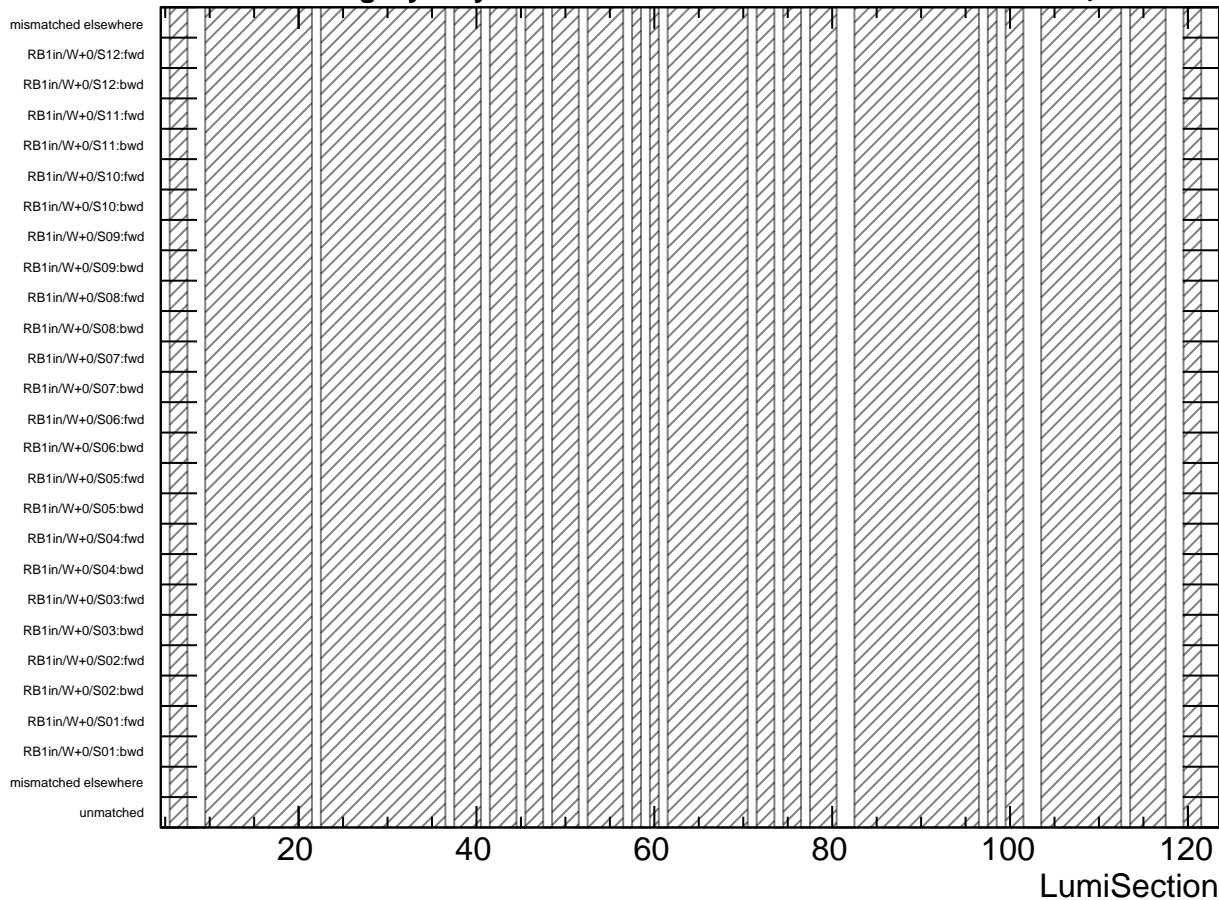
RB1in/W-1: Legacy only

CMS Preliminary - run 306122



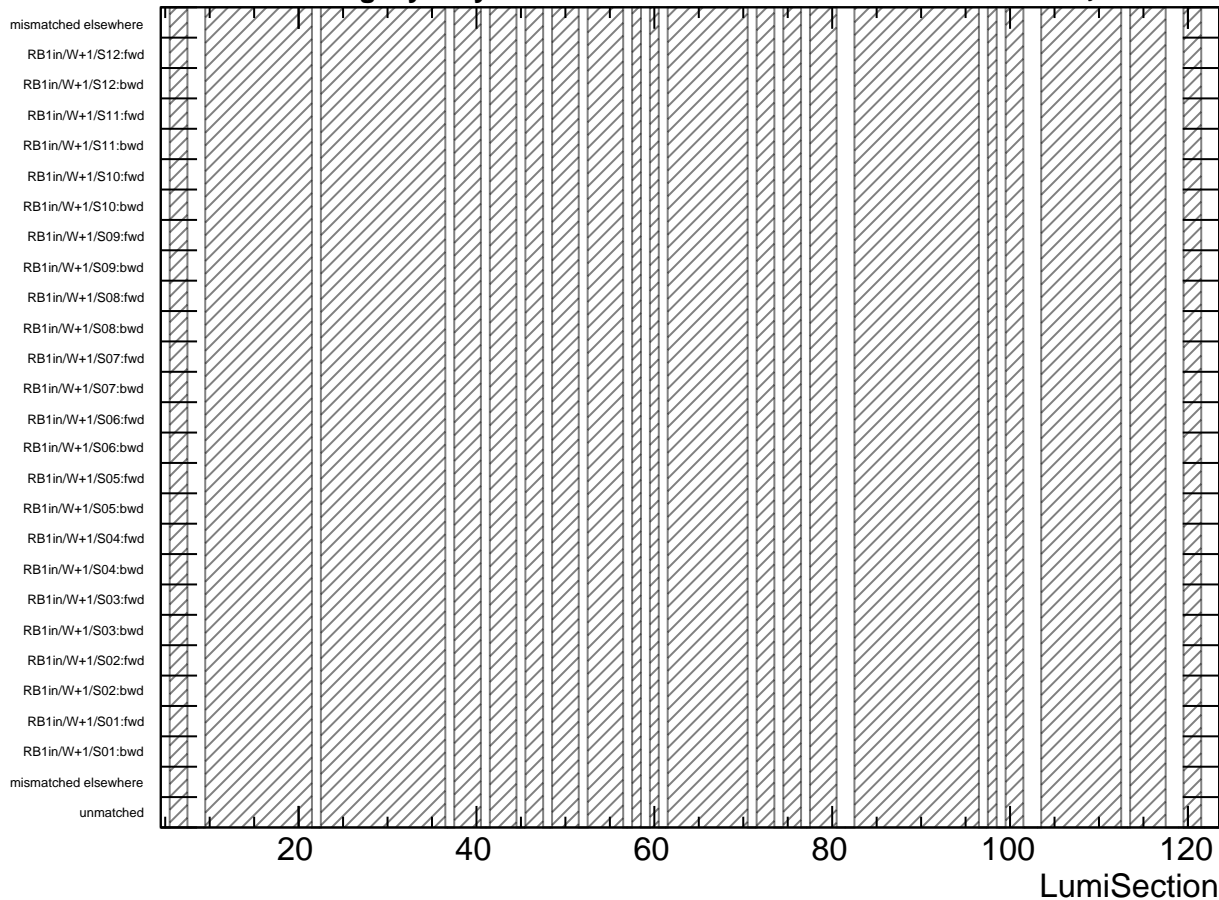
RB1in/W+0: Legacy only

CMS Preliminary - run 306122



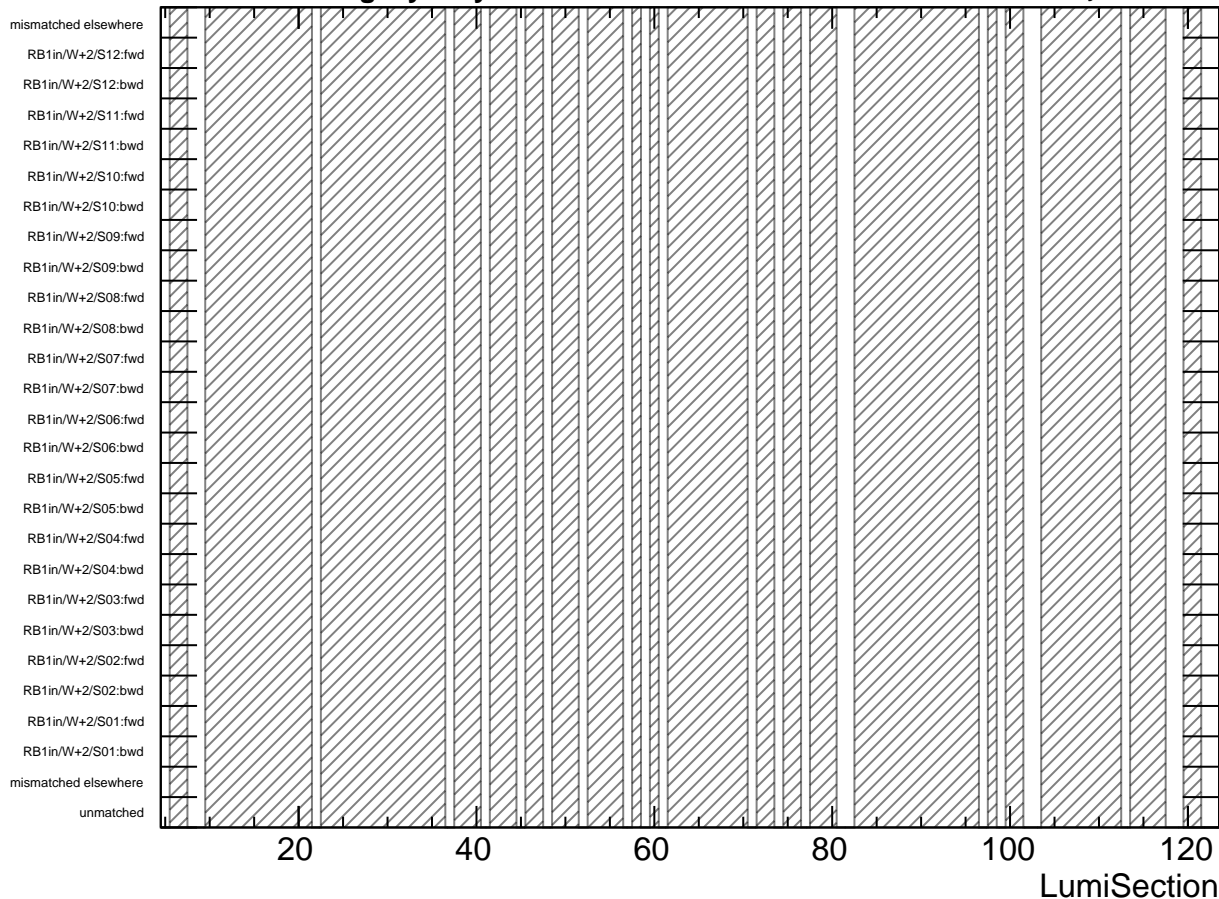
RB1in/W+1: Legacy only

CMS Preliminary - run 306122



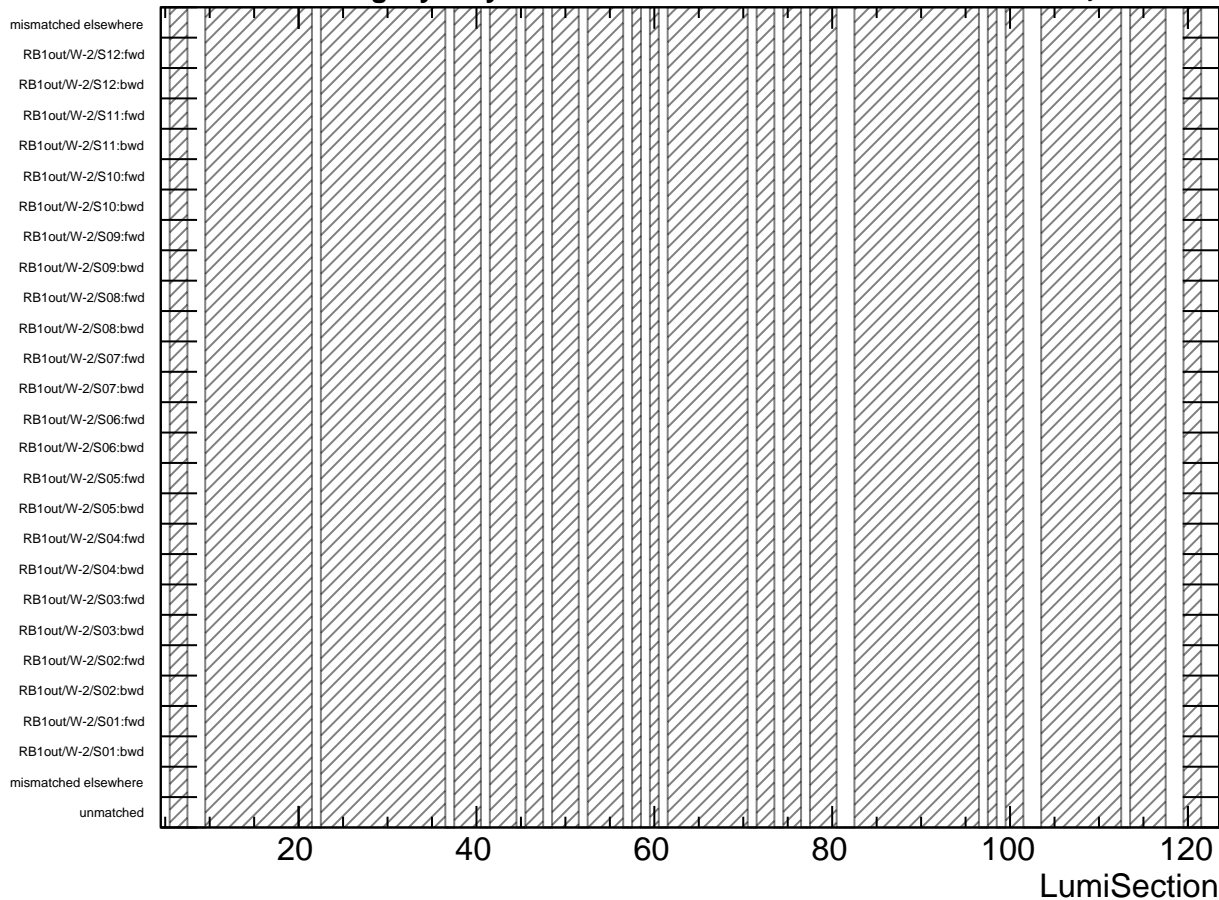
RB1in/W+2: Legacy only

CMS Preliminary - run 306122



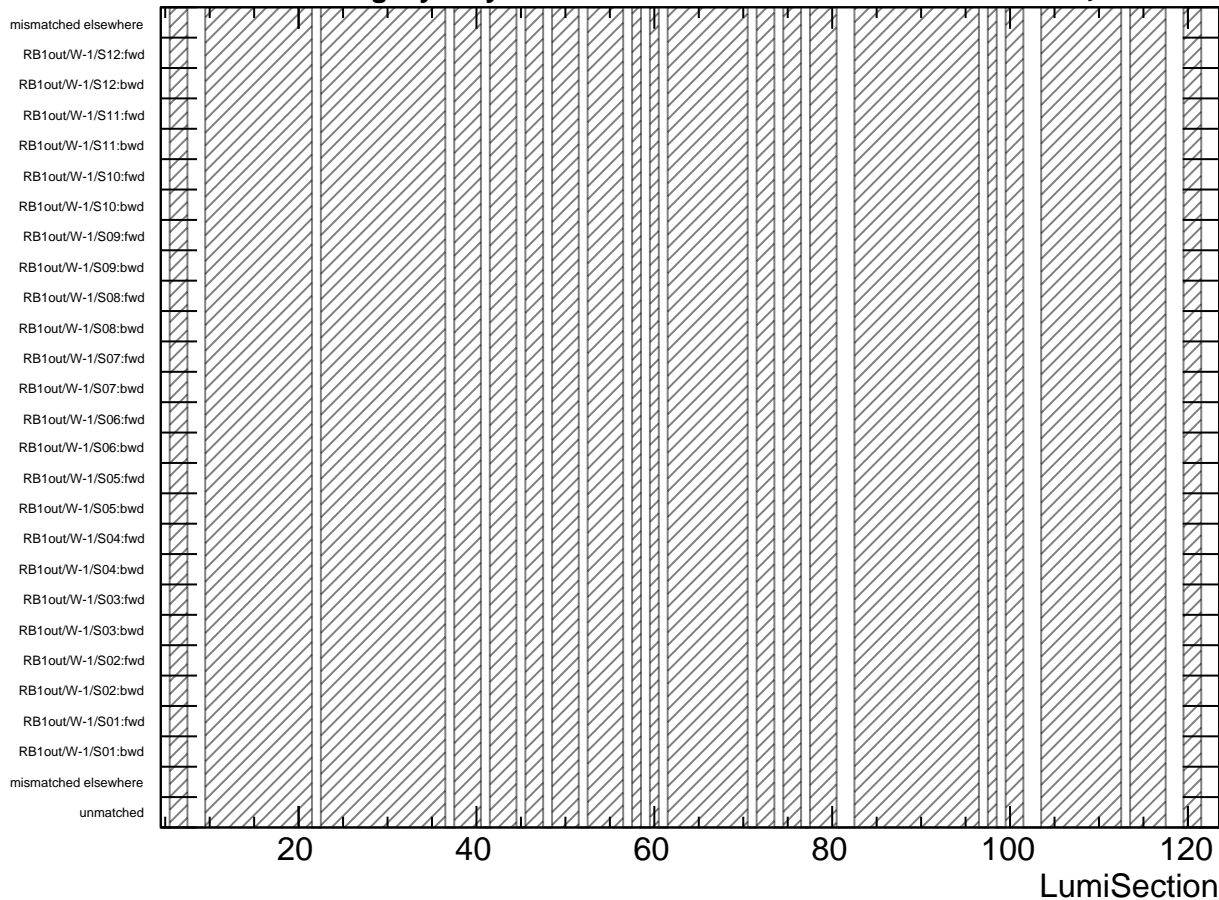
RB1out/W-2: Legacy only

CMS Preliminary - run 306122



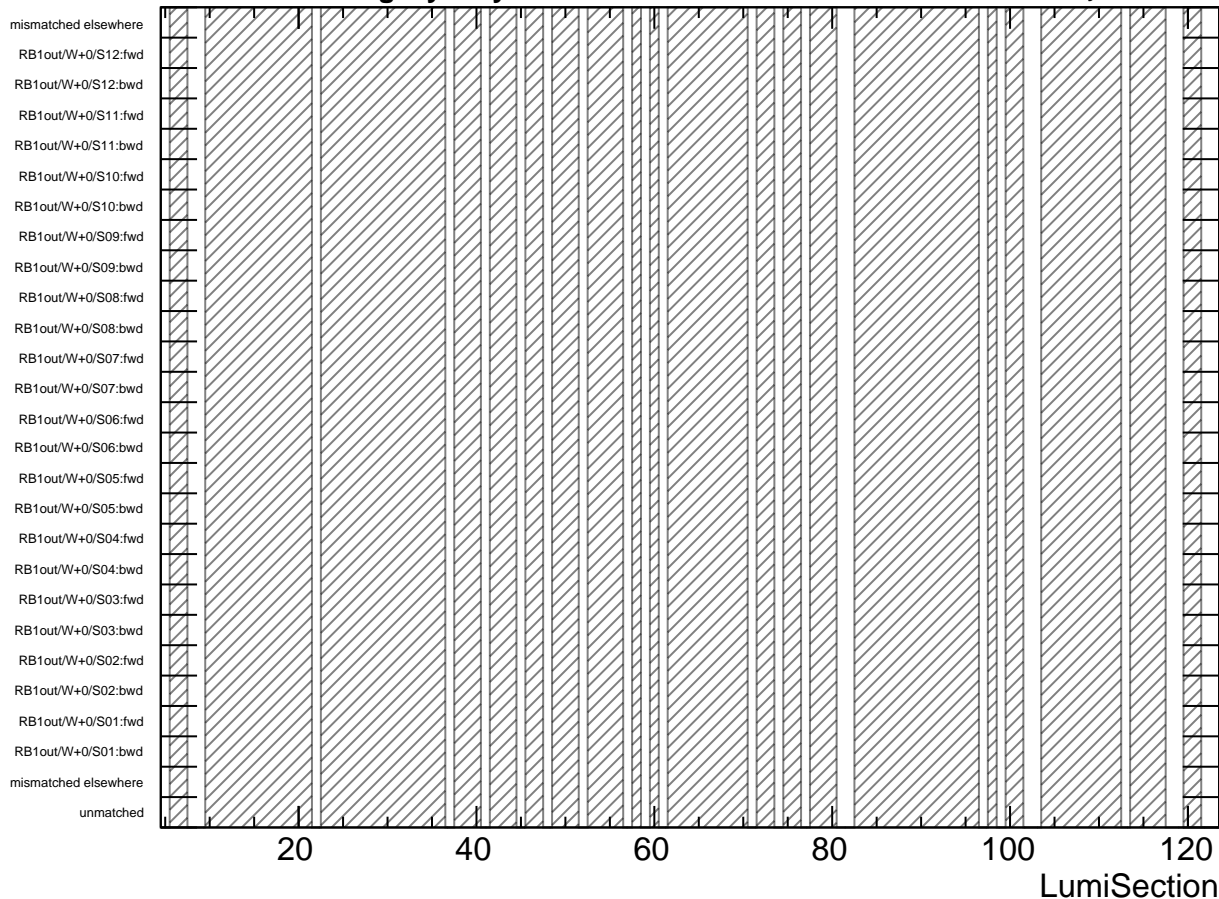
RB1out/W-1: Legacy only

CMS Preliminary - run 306122



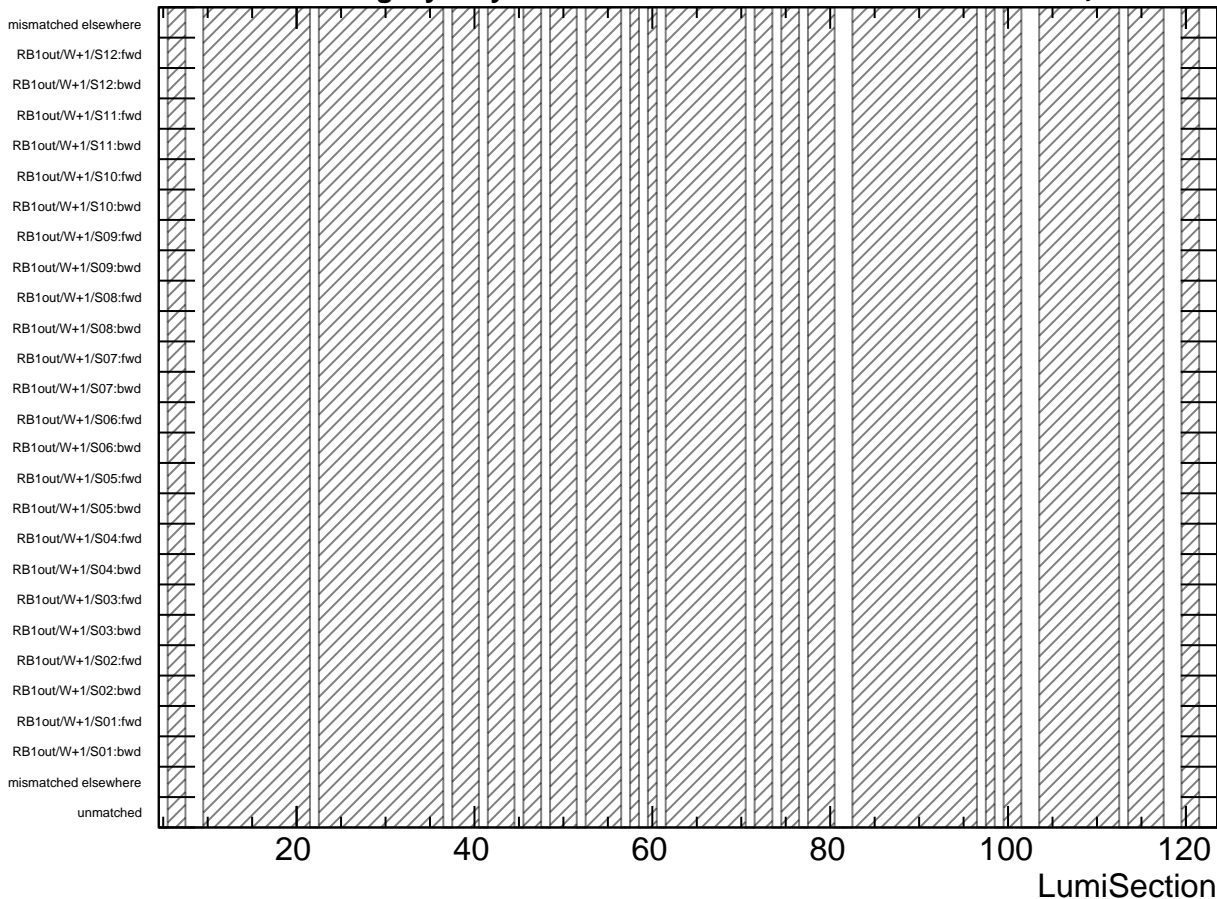
RB1out/W+0: Legacy only

CMS Preliminary - run 306122



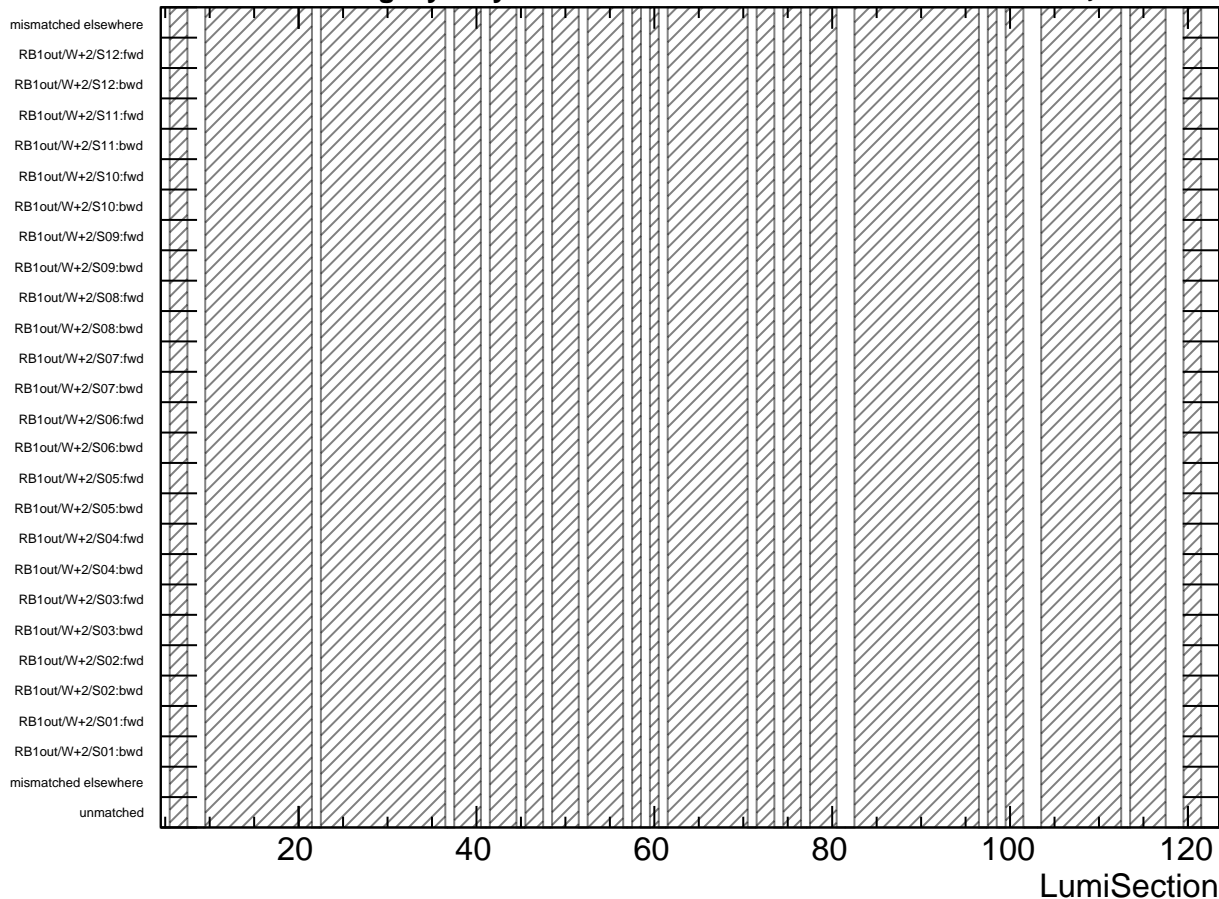
RB1out/W+1: Legacy only

CMS Preliminary - run 306122



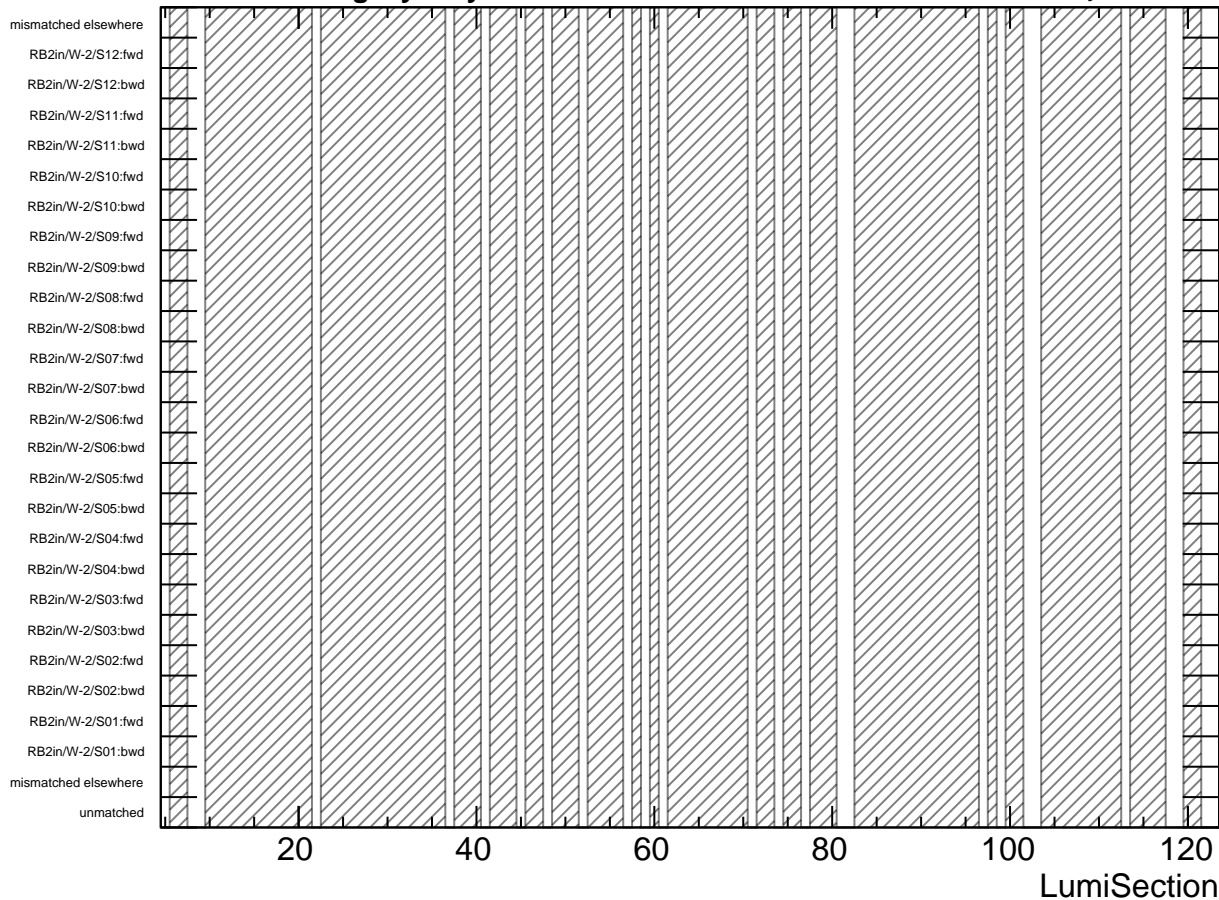
RB1out/W+2: Legacy only

CMS Preliminary - run 306122



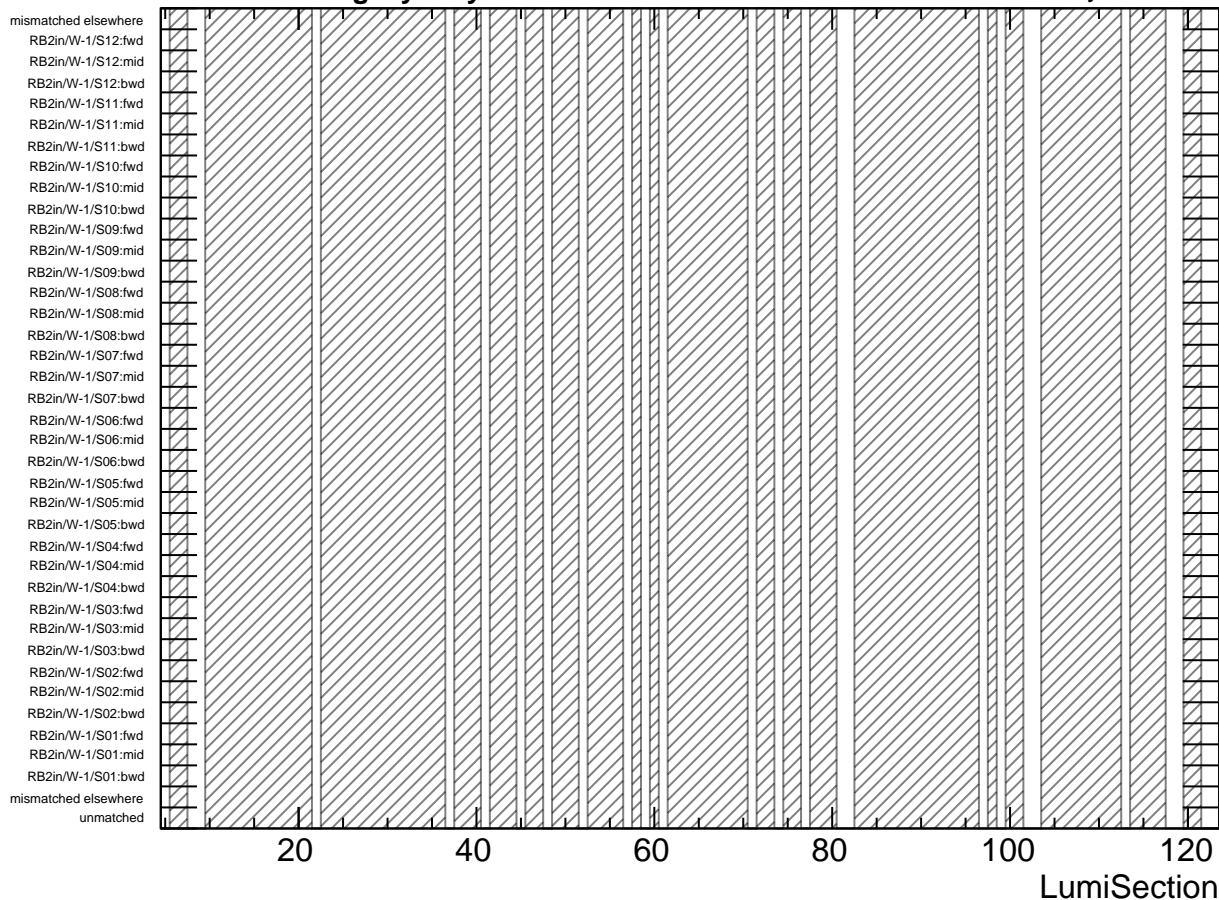
RB2in/W-2: Legacy only

CMS Preliminary - run 306122



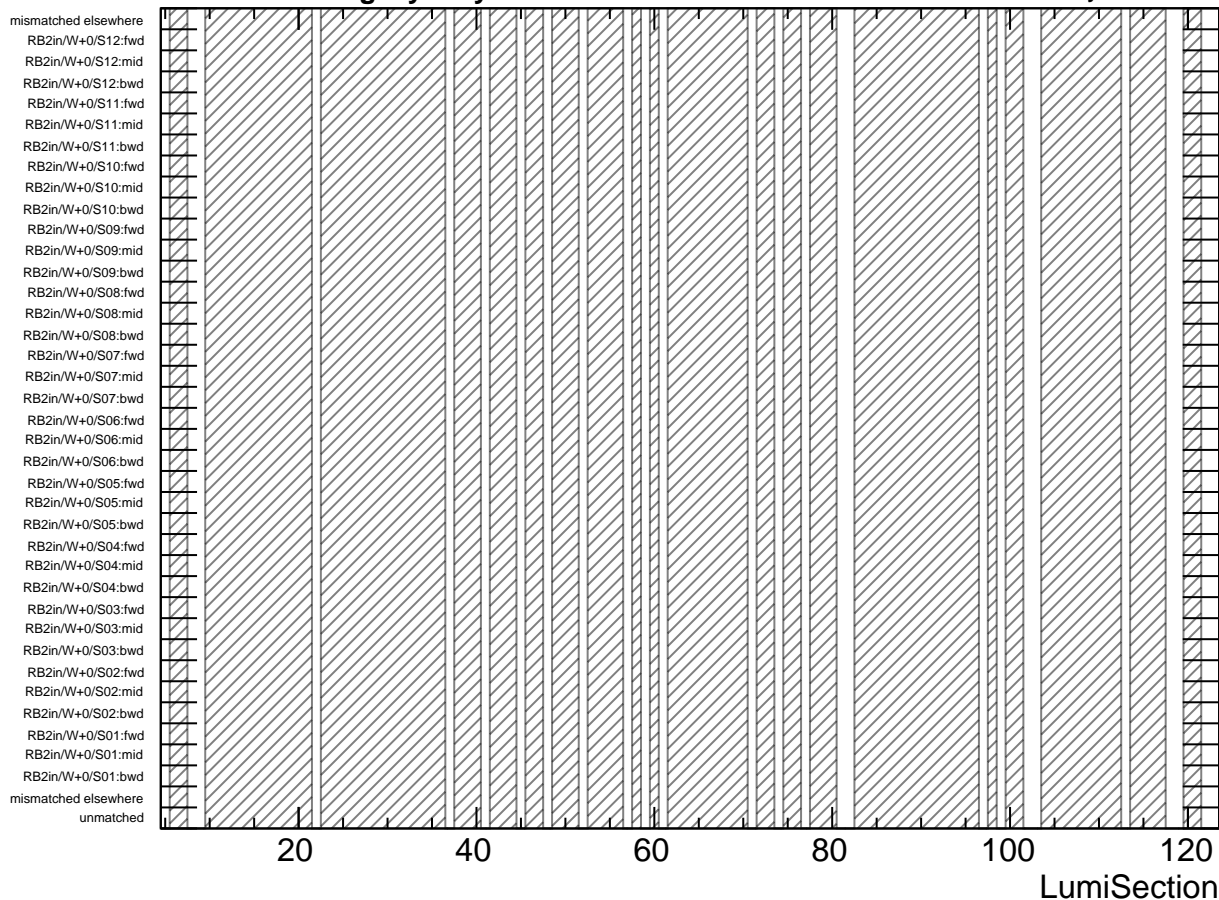
RB2in/W-1: Legacy only

CMS Preliminary - run 306122



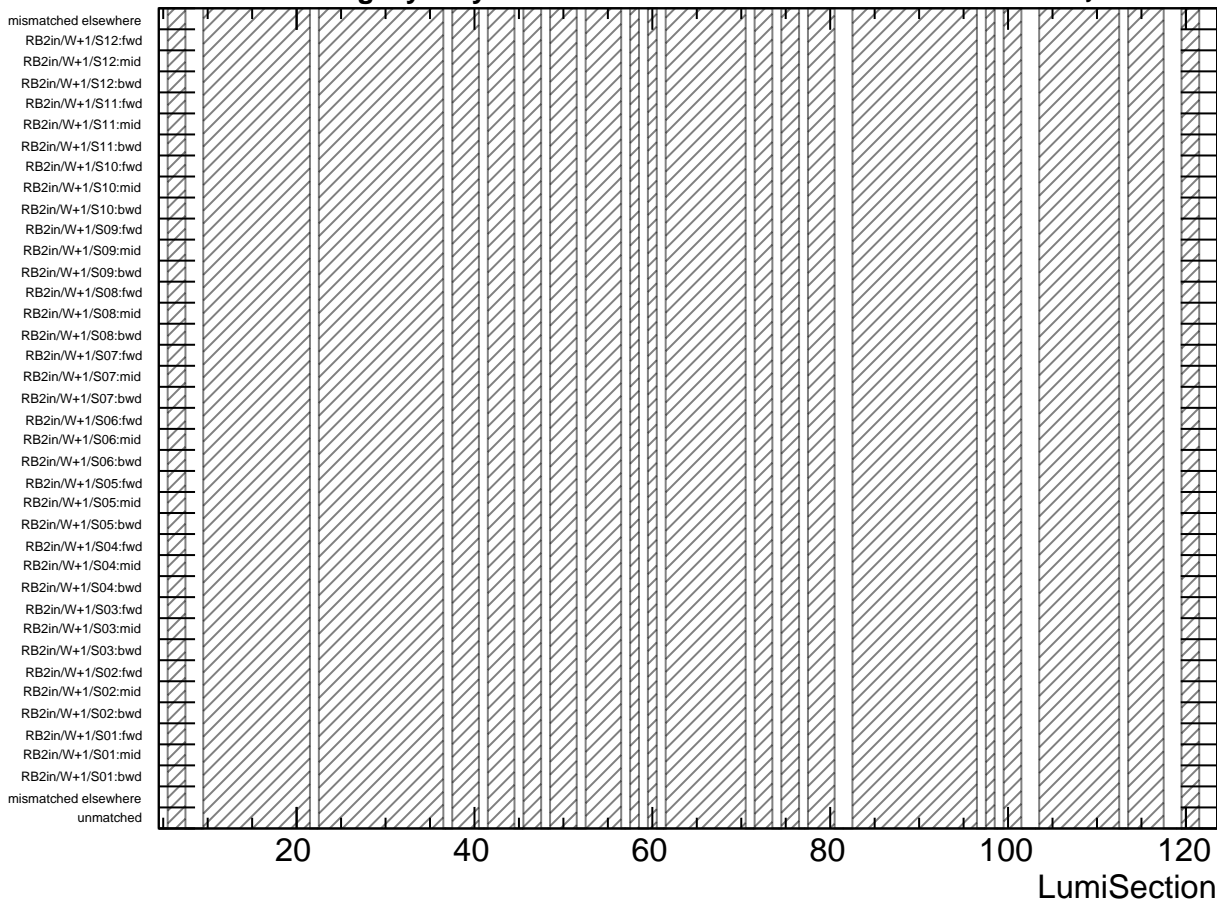
RB2in/W+0: Legacy only

CMS Preliminary - run 306122



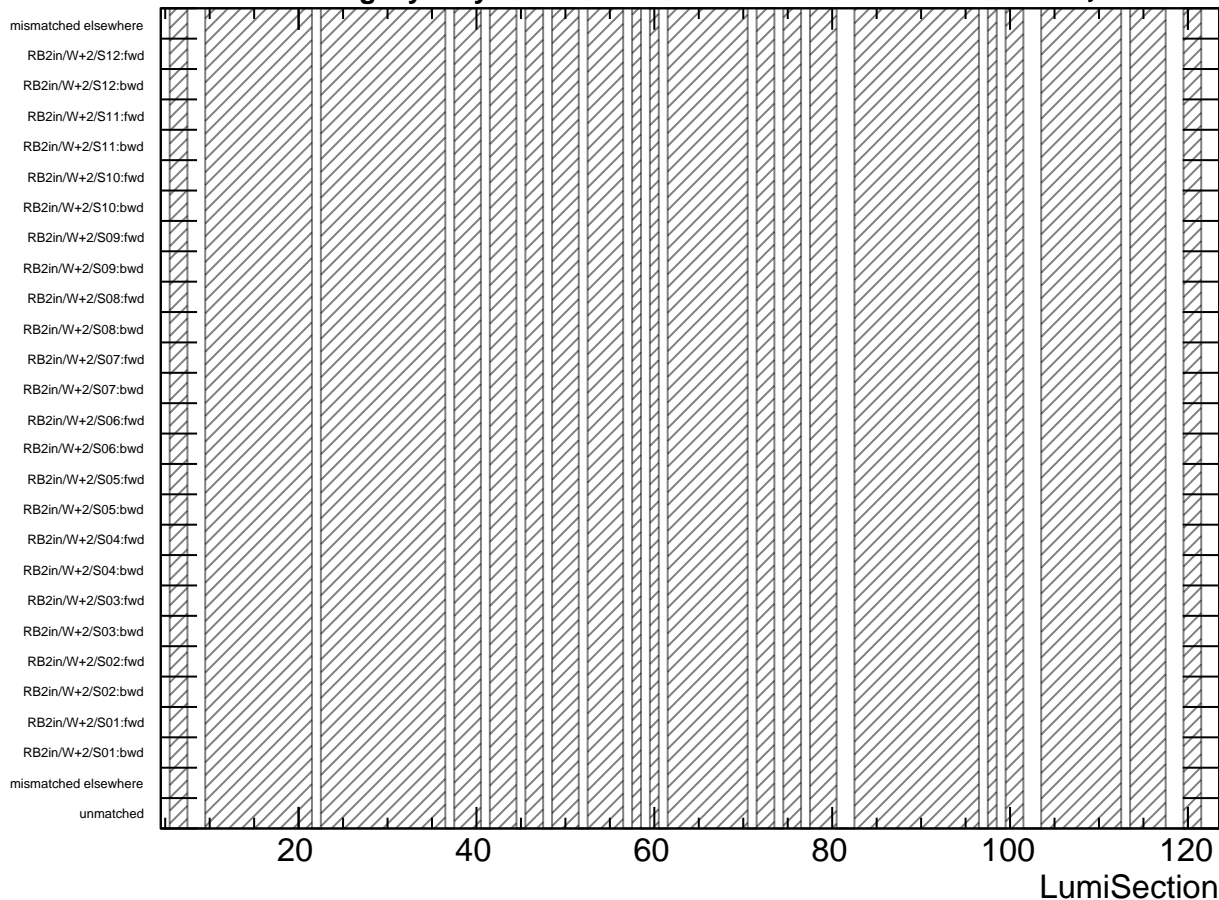
RB2in/W+1: Legacy only

CMS Preliminary - run 306122



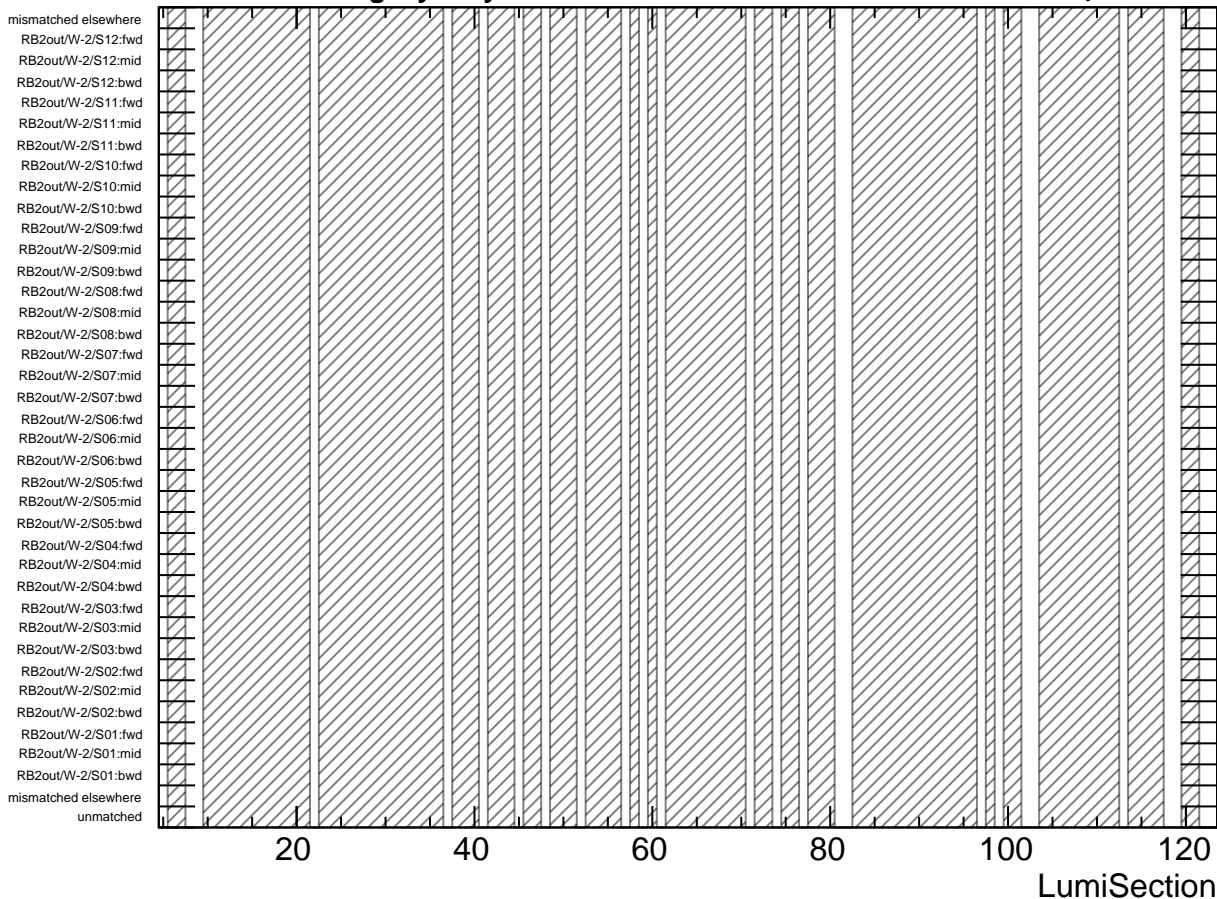
RB2in/W+2: Legacy only

CMS Preliminary - run 306122



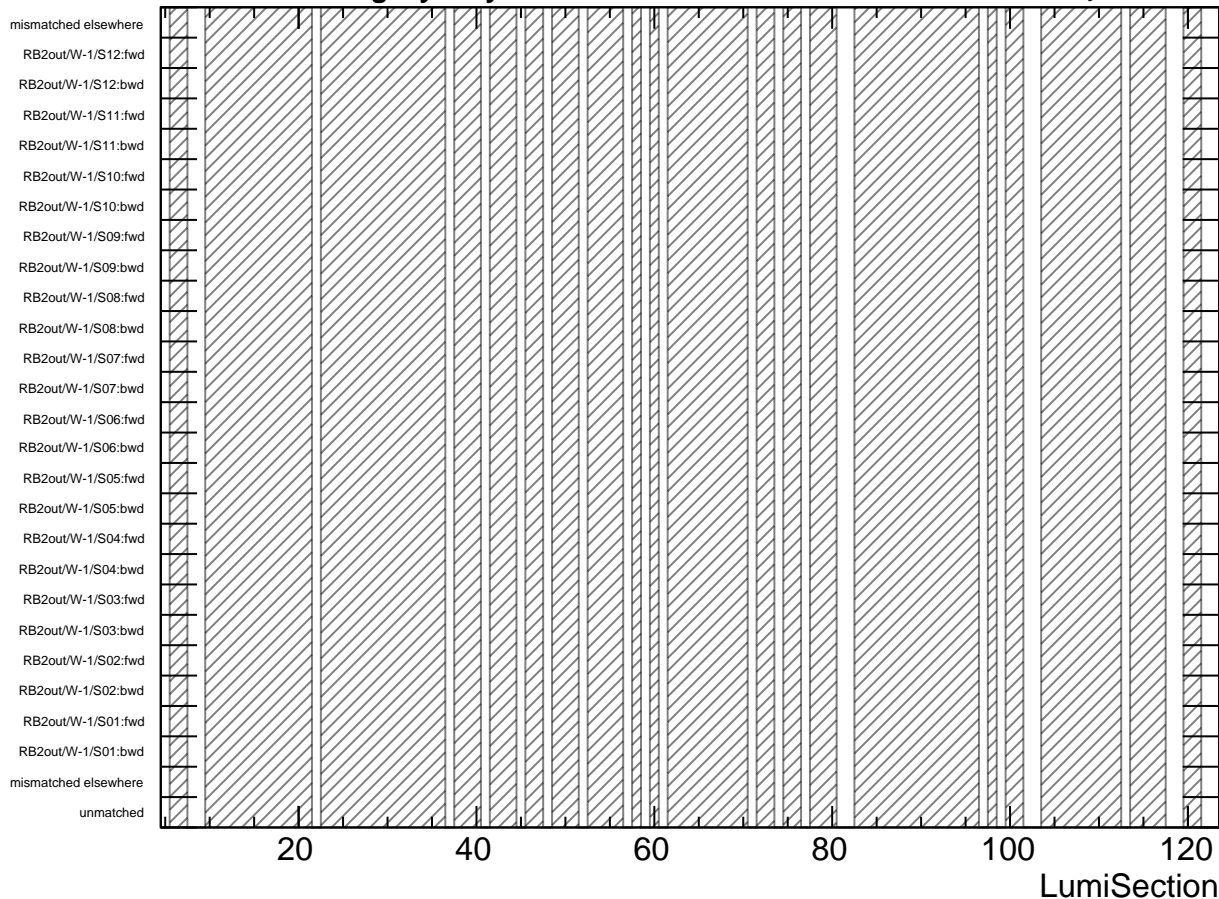
RB2out/W-2: Legacy only

CMS Preliminary - run 306122



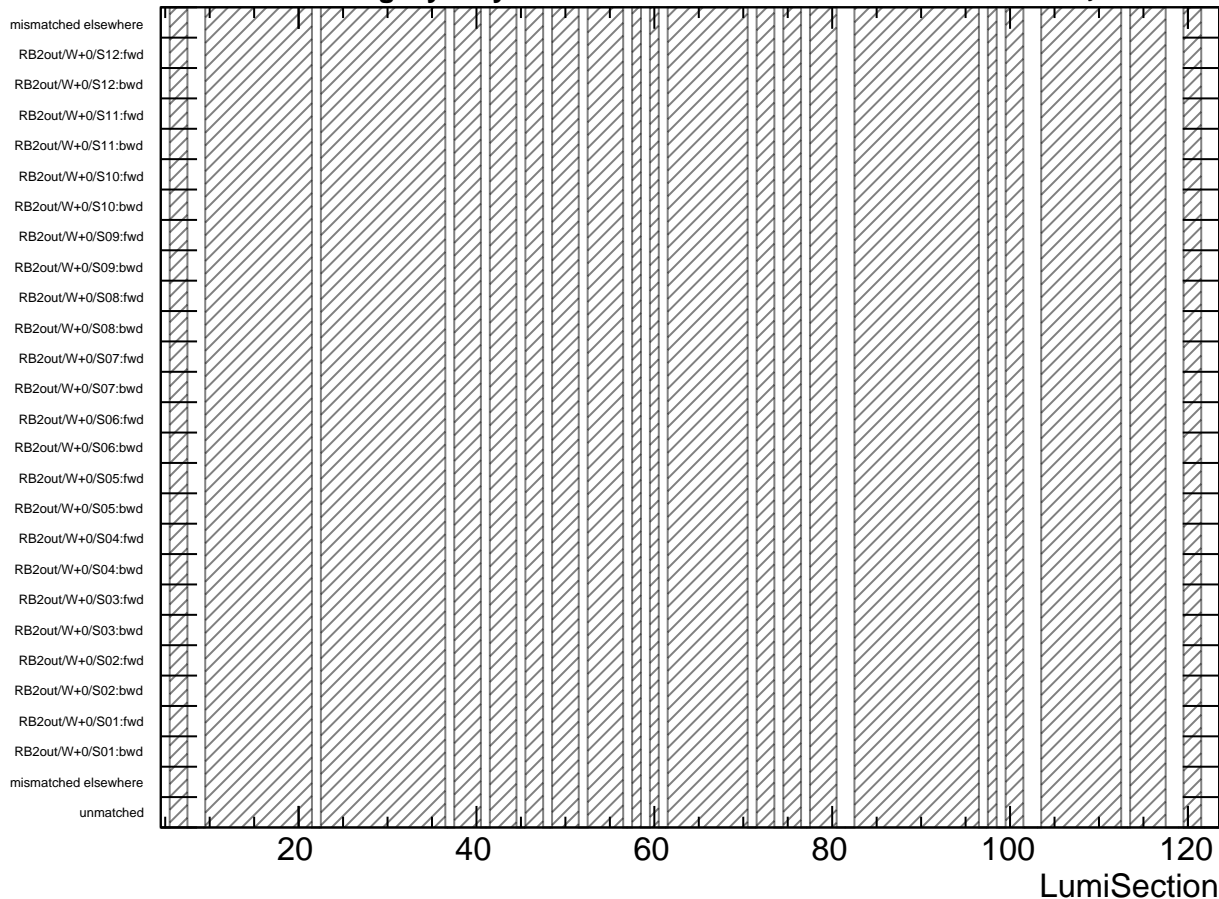
RB2out/W-1: Legacy only

CMS Preliminary - run 306122



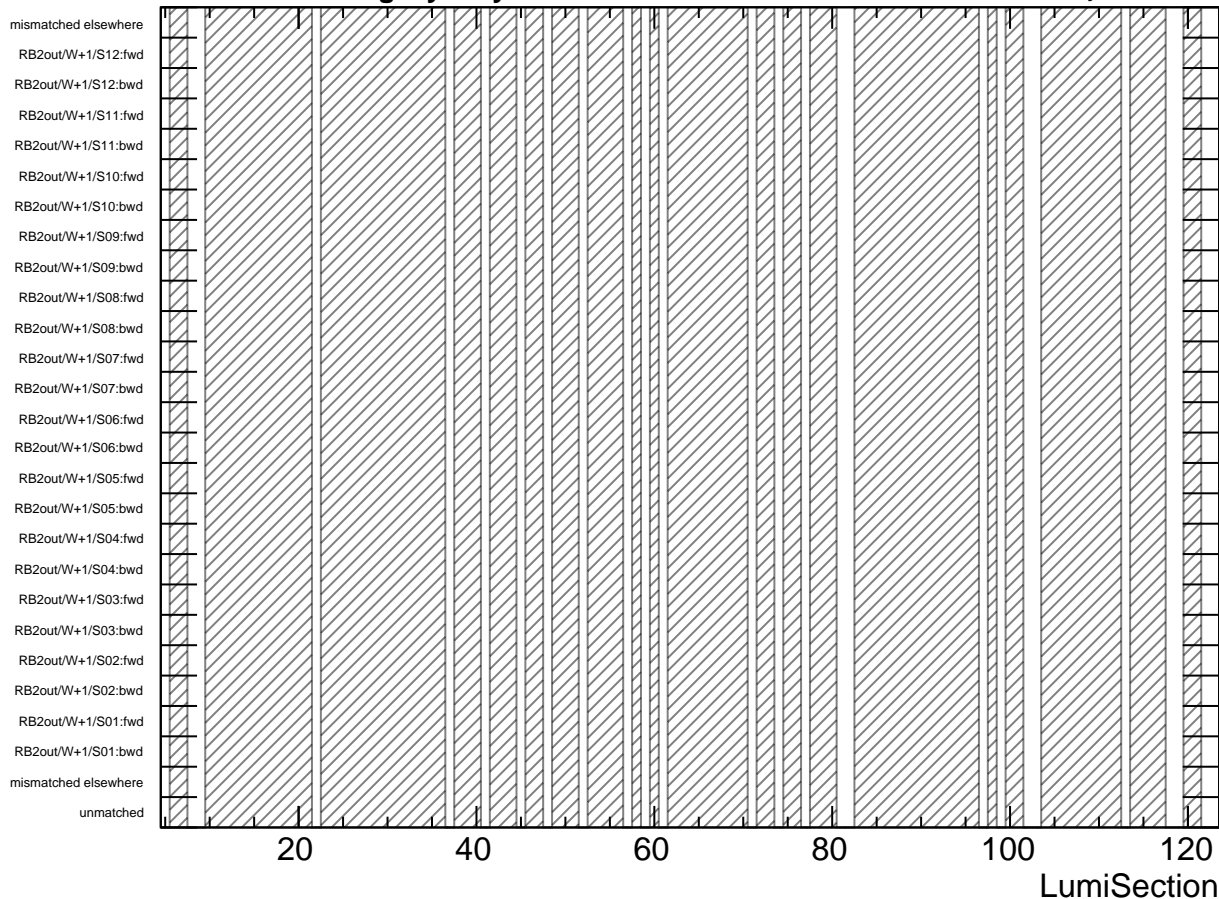
RB2out/W+0: Legacy only

CMS Preliminary - run 306122



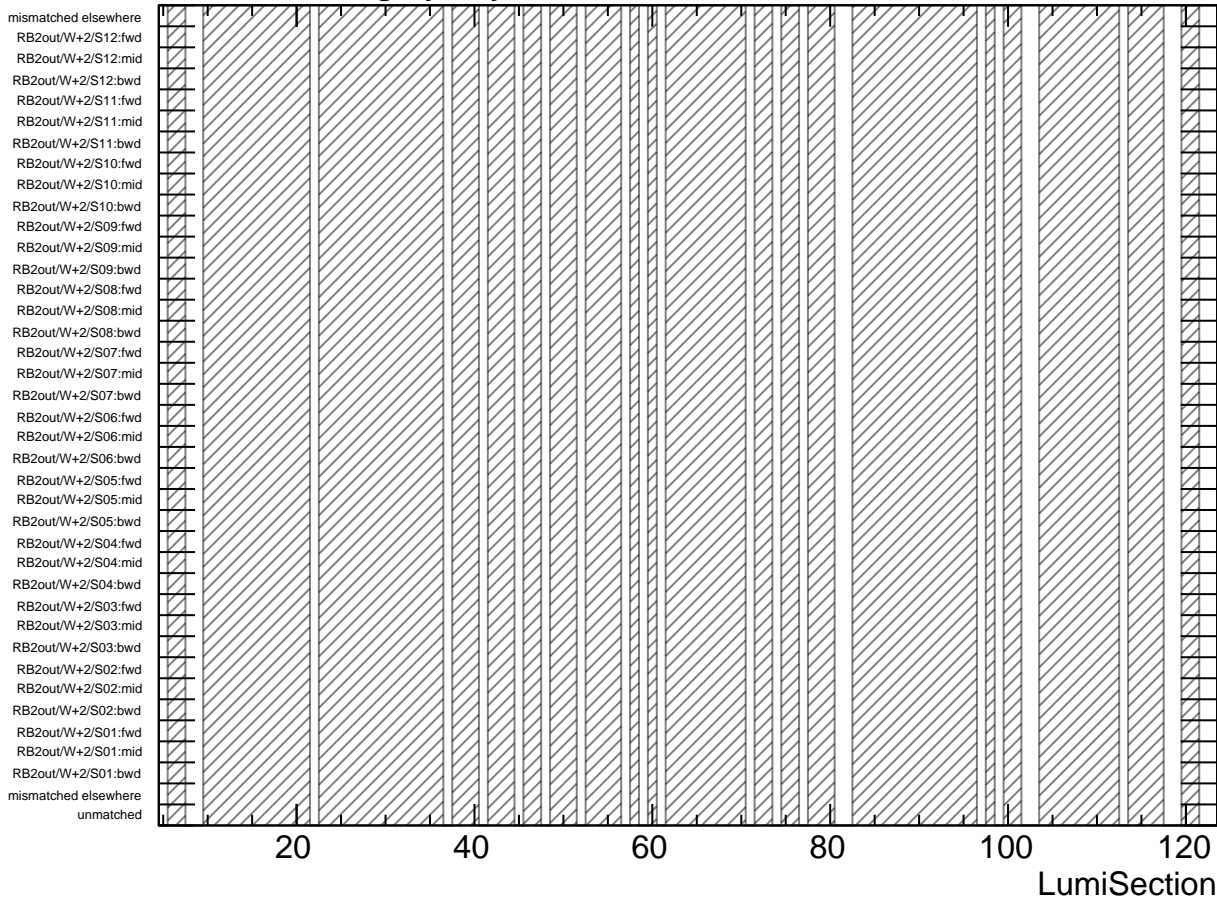
RB2out/W+1: Legacy only

CMS Preliminary - run 306122



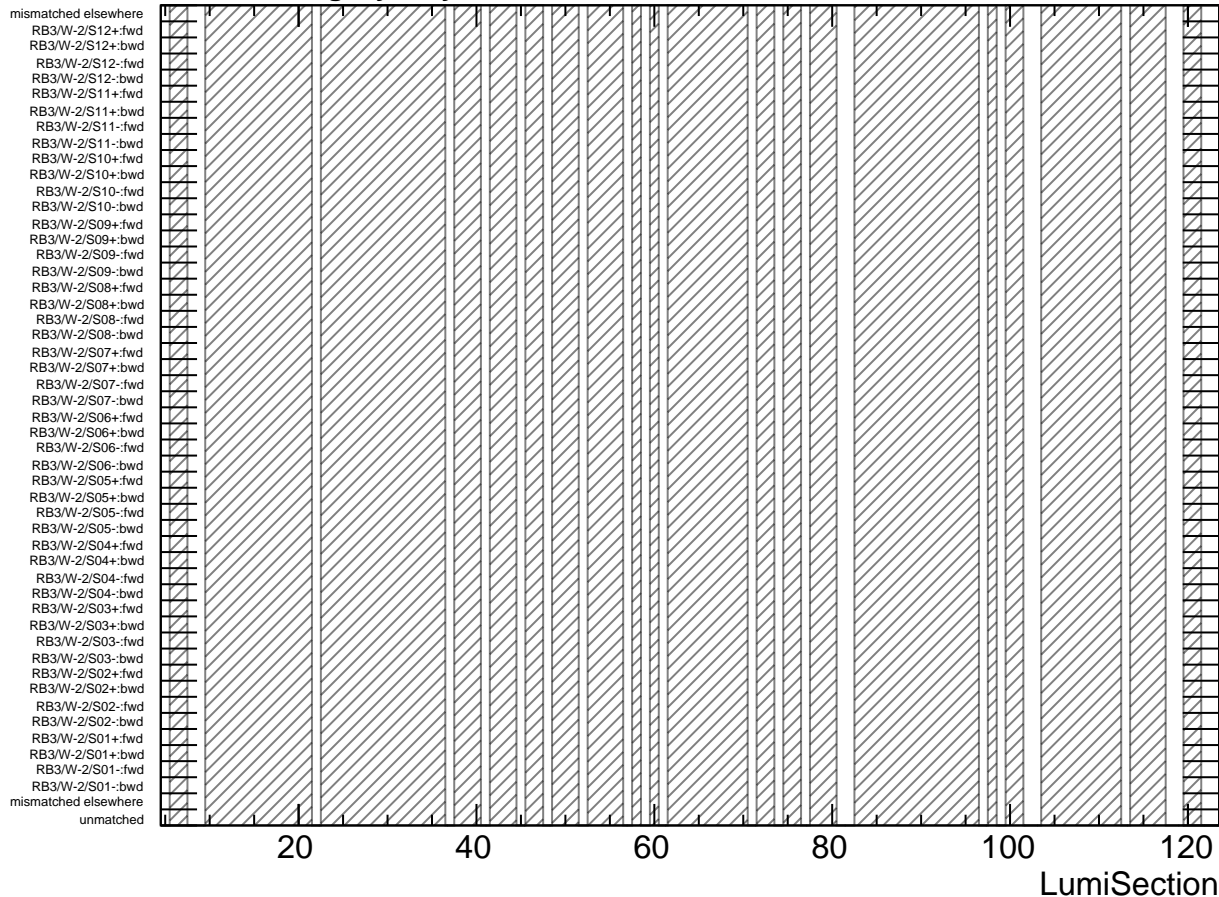
RB2out/W+2: Legacy only

CMS Preliminary - run 306122



RB3/W-2: Legacy only

CMS Preliminary - run 306122



RB3/W-1: Legacy only

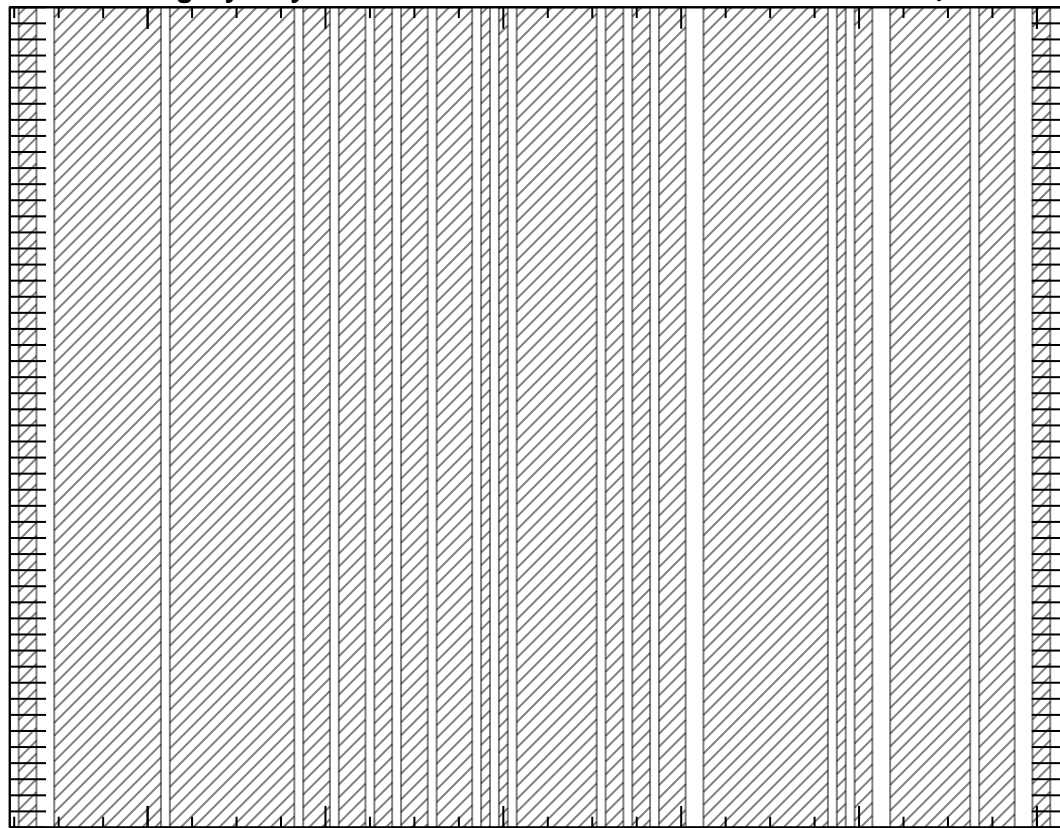
CMS Preliminary - run 306122

mismatched elsewhere

RB3/W-1/S12+ fwd
RB3/W-1/S12+ bwd
RB3/W-1/S12- fwd
RB3/W-1/S12- bwd
RB3/W-1/S11+ fwd
RB3/W-1/S11+ bwd
RB3/W-1/S11- fwd
RB3/W-1/S11- bwd
RB3/W-1/S10+ fwd
RB3/W-1/S10+ bwd
RB3/W-1/S10- fwd
RB3/W-1/S10- bwd
RB3/W-1/S09+ fwd
RB3/W-1/S09+ bwd
RB3/W-1/S09- fwd
RB3/W-1/S09- bwd
RB3/W-1/S08+ fwd
RB3/W-1/S08+ bwd
RB3/W-1/S08- fwd
RB3/W-1/S08- bwd
RB3/W-1/S07+ fwd
RB3/W-1/S07+ bwd
RB3/W-1/S07- fwd
RB3/W-1/S07- bwd
RB3/W-1/S06+ fwd
RB3/W-1/S06+ bwd
RB3/W-1/S06- fwd
RB3/W-1/S06- bwd
RB3/W-1/S05+ fwd
RB3/W-1/S05+ bwd
RB3/W-1/S05- fwd
RB3/W-1/S05- bwd
RB3/W-1/S04+ fwd
RB3/W-1/S04+ bwd
RB3/W-1/S04- fwd
RB3/W-1/S04- bwd
RB3/W-1/S03+ fwd
RB3/W-1/S03+ bwd
RB3/W-1/S03- fwd
RB3/W-1/S03- bwd
RB3/W-1/S02+ fwd
RB3/W-1/S02+ bwd
RB3/W-1/S02- fwd
RB3/W-1/S02- bwd
RB3/W-1/S01+ fwd
RB3/W-1/S01+ bwd
RB3/W-1/S01- fwd
RB3/W-1/S01- bwd

mismatched elsewhere

unmatched



20

40

60

80

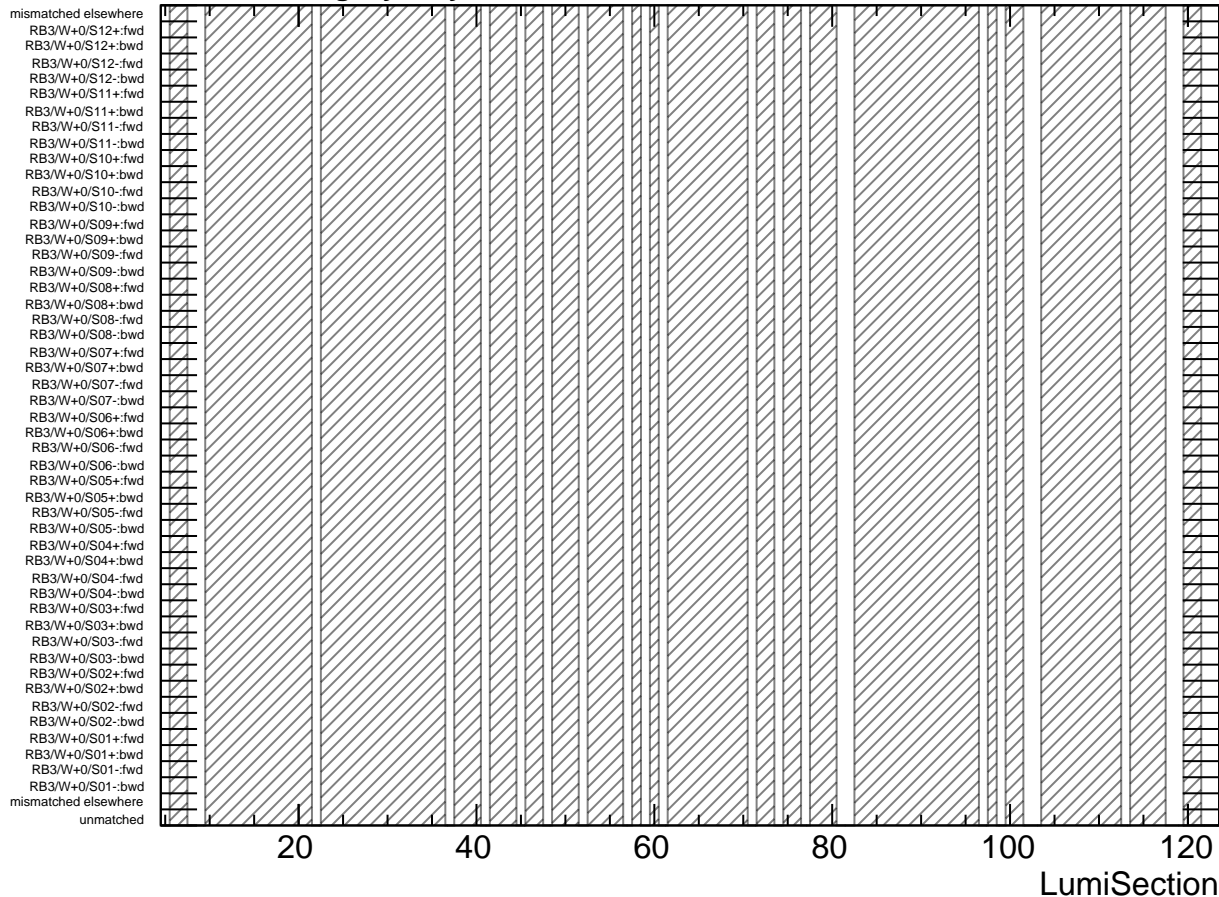
100

120

LumiSection

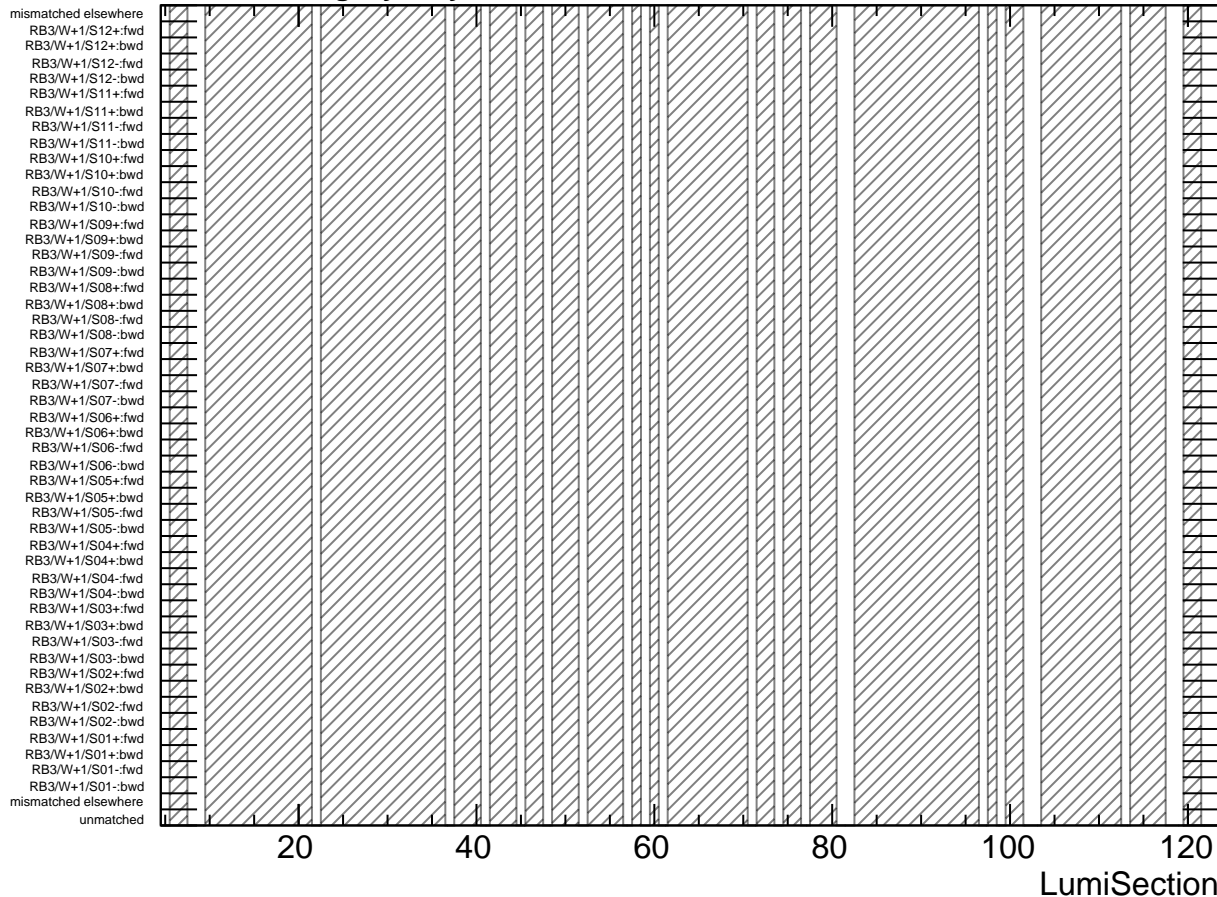
RB3/W+0: Legacy only

CMS Preliminary - run 306122



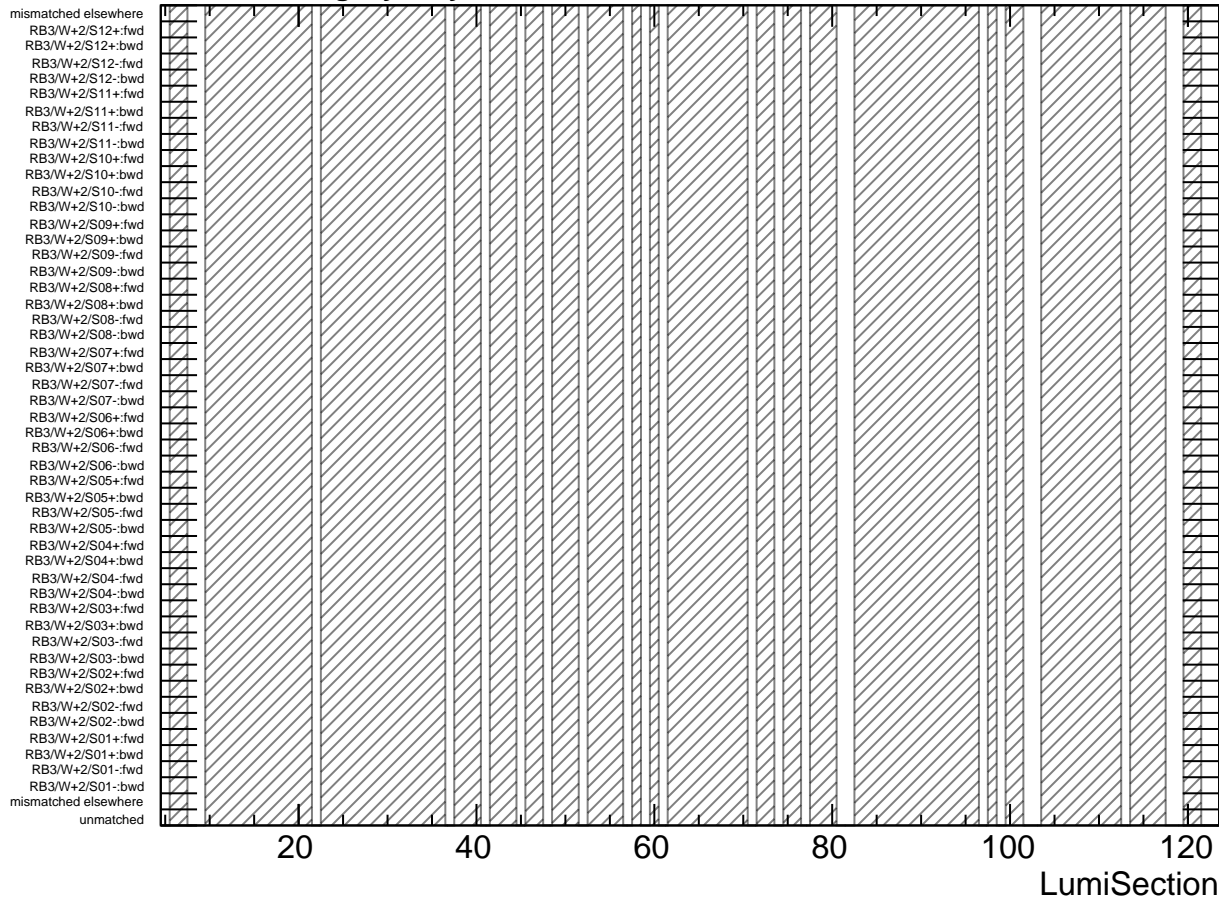
RB3/W+1: Legacy only

CMS Preliminary - run 306122



RB3/W+2: Legacy only

CMS Preliminary - run 306122



RB4/W-2: Legacy only

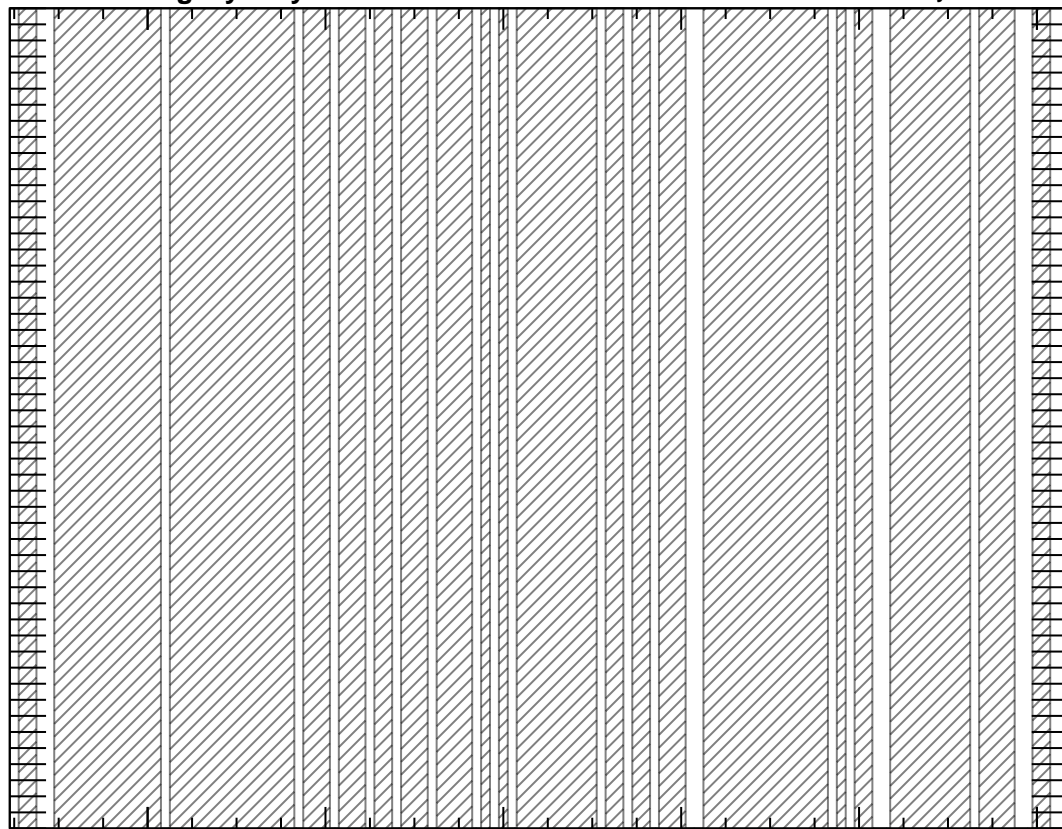
CMS Preliminary - run 306122

mismatched elsewhere

RB4/W-2/S12+ fwd
 RB4/W-2/S12+ bwd
 RB4/W-2/S12- fwd
 RB4/W-2/S12- bwd
 RB4/W-2/S11 fwd
 RB4/W-2/S11 bwd
 RB4/W-2/S10+ fwd
 RB4/W-2/S10+ bwd
 RB4/W-2/S10- fwd
 RB4/W-2/S10- bwd
 RB4/W-2/S09 fwd
 RB4/W-2/S09 bwd
 RB4/W-2/S08+ fwd
 RB4/W-2/S08+ bwd
 RB4/W-2/S08- fwd
 RB4/W-2/S08- bwd
 RB4/W-2/S07+ fwd
 RB4/W-2/S07+ bwd
 RB4/W-2/S07- fwd
 RB4/W-2/S07- bwd
 RB4/W-2/S06+ fwd
 RB4/W-2/S06+ bwd
 RB4/W-2/S06- fwd
 RB4/W-2/S06- bwd
 RB4/W-2/S05+ fwd
 RB4/W-2/S05+ bwd
 RB4/W-2/S05- fwd
 RB4/W-2/S05- bwd
 RB4/W-2/S04++ fwd
 RB4/W-2/S04++ bwd
 RB4/W-2/S04+ fwd
 RB4/W-2/S04+ bwd
 RB4/W-2/S04- fwd
 RB4/W-2/S04- bwd
 RB4/W-2/S04- fwd
 RB4/W-2/S04- bwd
 RB4/W-2/S03+ fwd
 RB4/W-2/S03+ bwd
 RB4/W-2/S03- fwd
 RB4/W-2/S03- bwd
 RB4/W-2/S02+ fwd
 RB4/W-2/S02+ bwd
 RB4/W-2/S02- fwd
 RB4/W-2/S02- bwd
 RB4/W-2/S01+ fwd
 RB4/W-2/S01+ bwd
 RB4/W-2/S01- fwd
 RB4/W-2/S01- bwd

mismatched elsewhere

unmatched



20

40

60

80

100

120

LumiSection

RB4/W-1: Legacy only

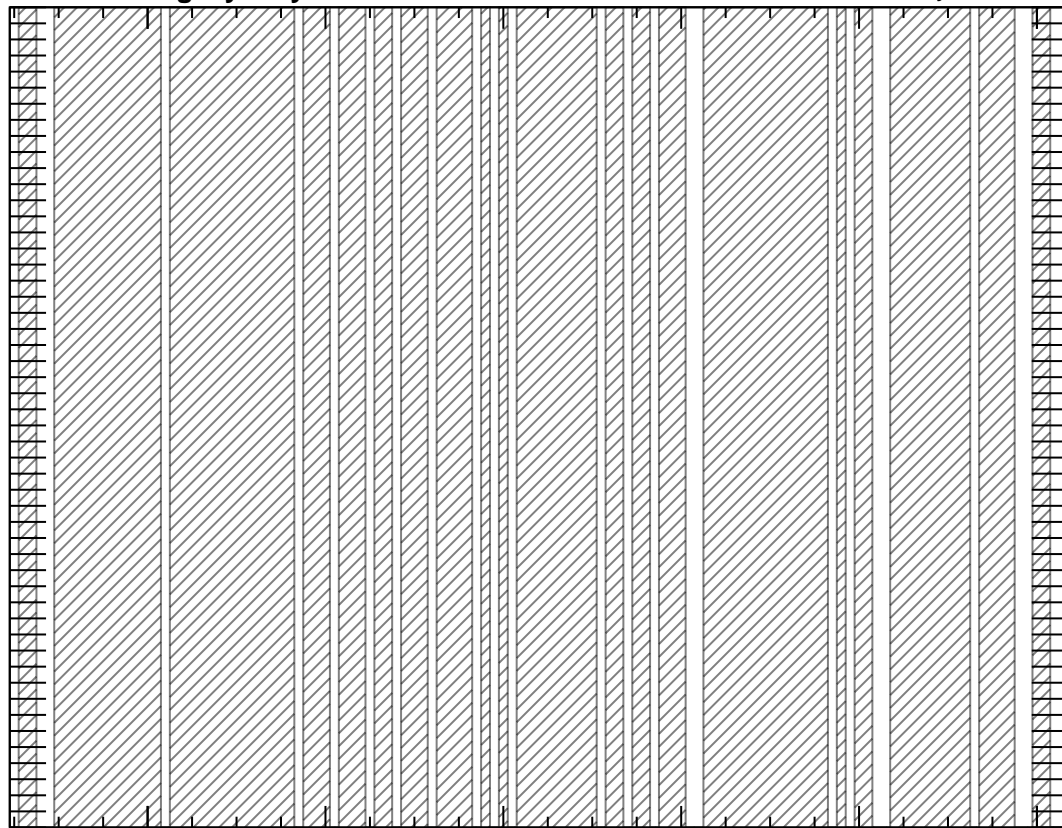
CMS Preliminary - run 306122

mismatched elsewhere

RB4/W-1/S12+-fwd
 RB4/W-1/S12+-bwd
 RB4/W-1/S12+-fwd
 RB4/W-1/S12+-bwd
 RB4/W-1/S11-fwd
 RB4/W-1/S11-bwd
 RB4/W-1/S10+-fwd
 RB4/W-1/S10+-bwd
 RB4/W-1/S10-fwd
 RB4/W-1/S10-bwd
 RB4/W-1/S09-fwd
 RB4/W-1/S09-bwd
 RB4/W-1/S08+-fwd
 RB4/W-1/S08+-bwd
 RB4/W-1/S08-fwd
 RB4/W-1/S08-bwd
 RB4/W-1/S07+-fwd
 RB4/W-1/S07+-bwd
 RB4/W-1/S07-fwd
 RB4/W-1/S07-bwd
 RB4/W-1/S06+-fwd
 RB4/W-1/S06+-bwd
 RB4/W-1/S06-fwd
 RB4/W-1/S06-bwd
 RB4/W-1/S05+-fwd
 RB4/W-1/S05+-bwd
 RB4/W-1/S05-fwd
 RB4/W-1/S05-bwd
 RB4/W-1/S04+-fwd
 RB4/W-1/S04+-bwd
 RB4/W-1/S04+-fwd
 RB4/W-1/S04+-bwd
 RB4/W-1/S04+-fwd
 RB4/W-1/S04+-bwd
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 RB4/W-1/S04+-bwd
 RB4/W-1/S03+-fwd
 RB4/W-1/S03+-bwd
 RB4/W-1/S03-fwd
 RB4/W-1/S03-bwd
 RB4/W-1/S02+-fwd
 RB4/W-1/S02+-bwd
 RB4/W-1/S02-fwd
 RB4/W-1/S02-bwd
 RB4/W-1/S01+-fwd
 RB4/W-1/S01+-bwd
 RB4/W-1/S01-fwd
 RB4/W-1/S01-bwd

mismatched elsewhere

unmatched



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40

60

80

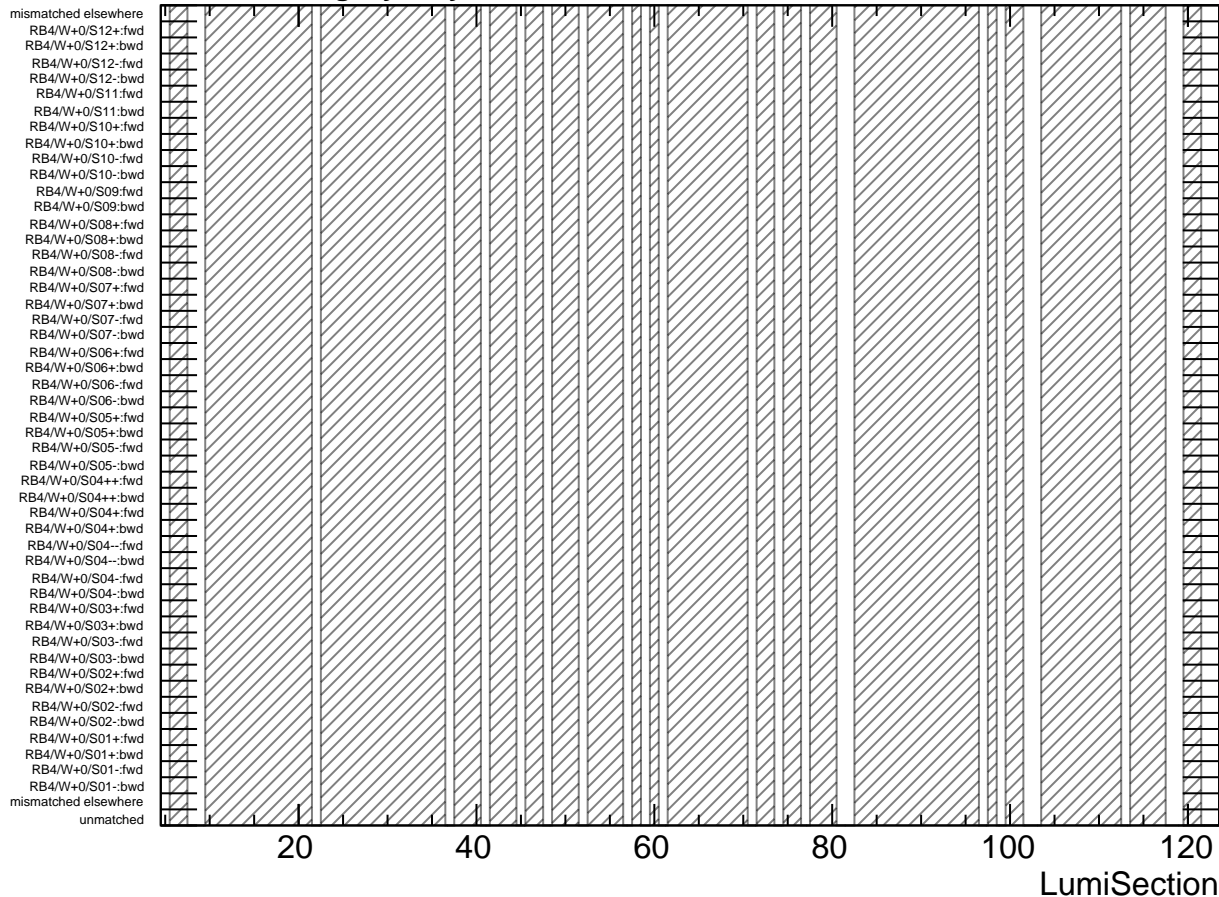
100

120

LumiSection

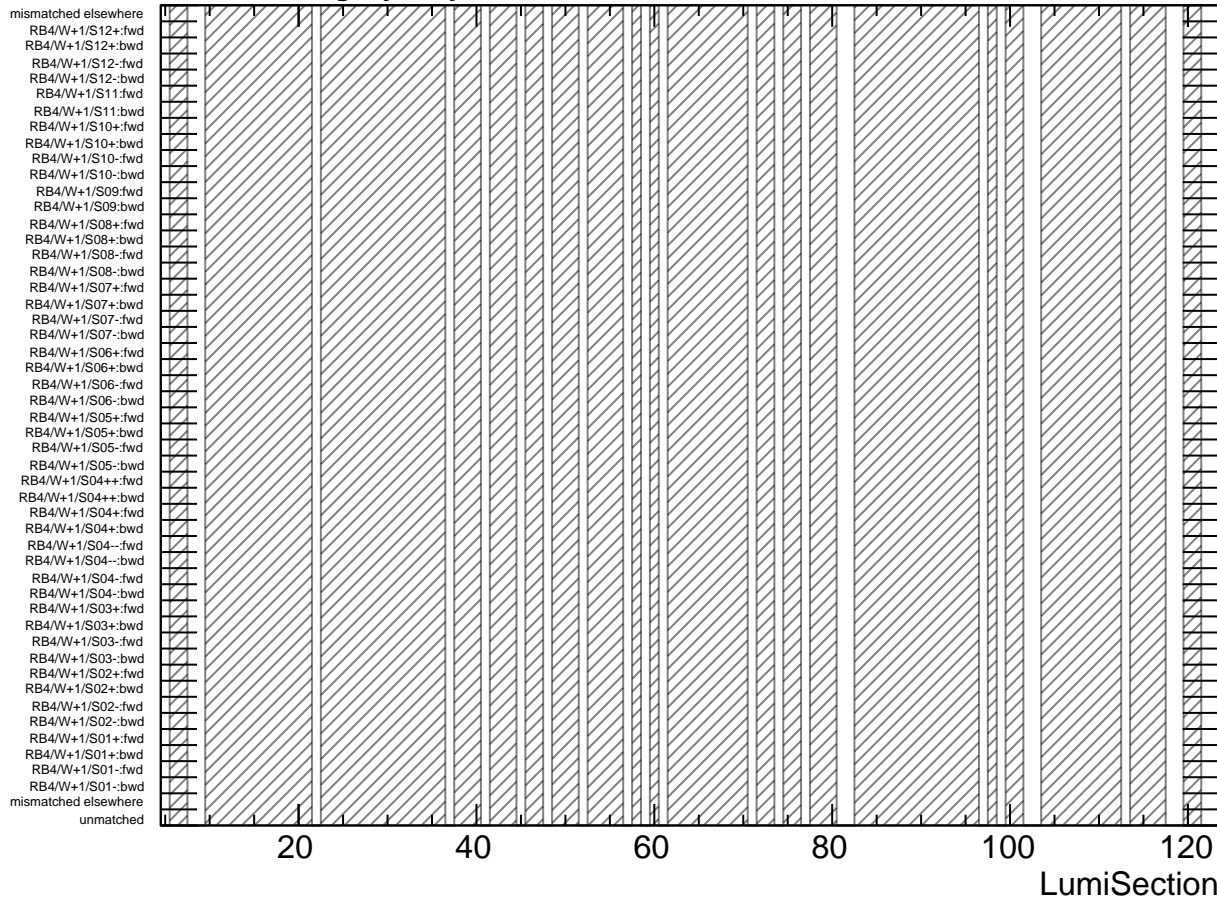
RB4/W+0: Legacy only

CMS Preliminary - run 306122



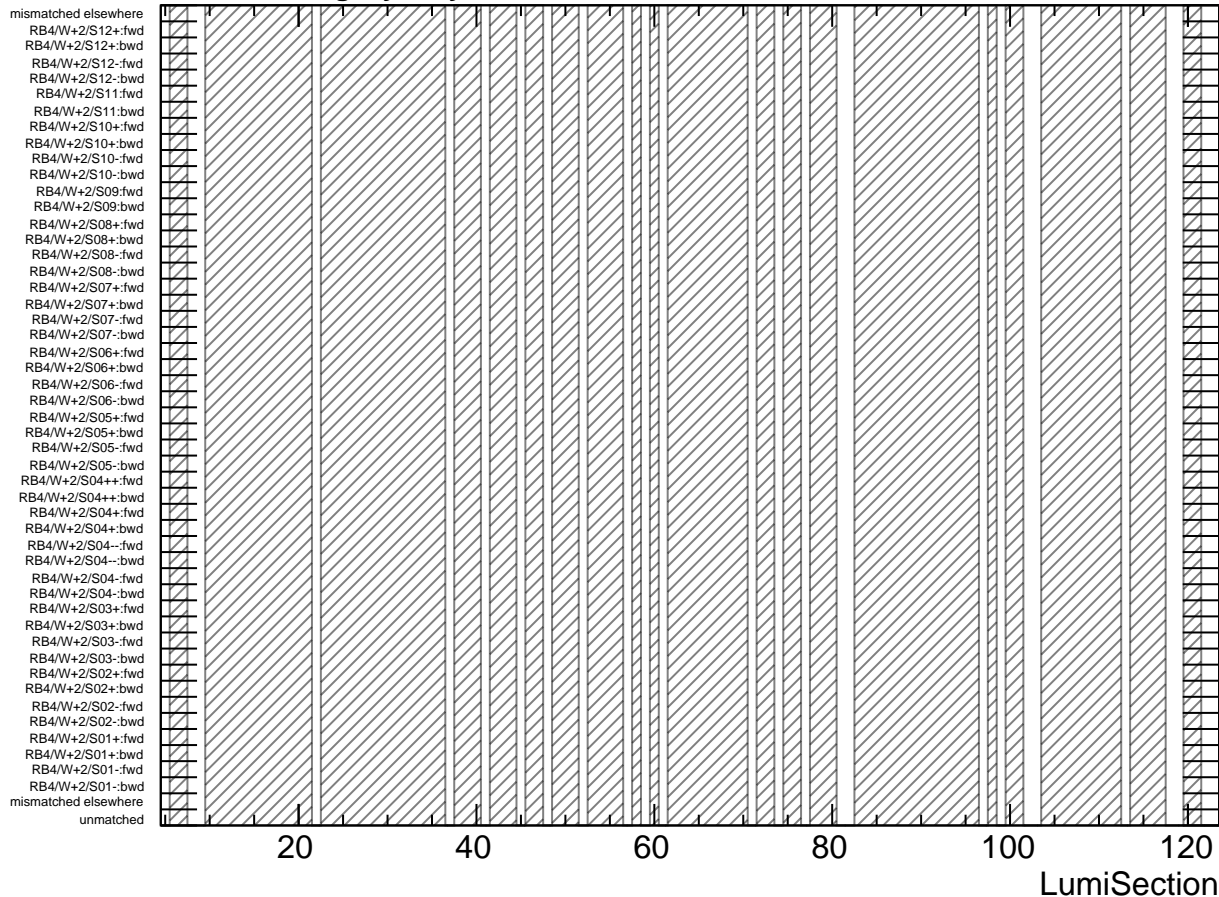
RB4/W+1: Legacy only

CMS Preliminary - run 306122



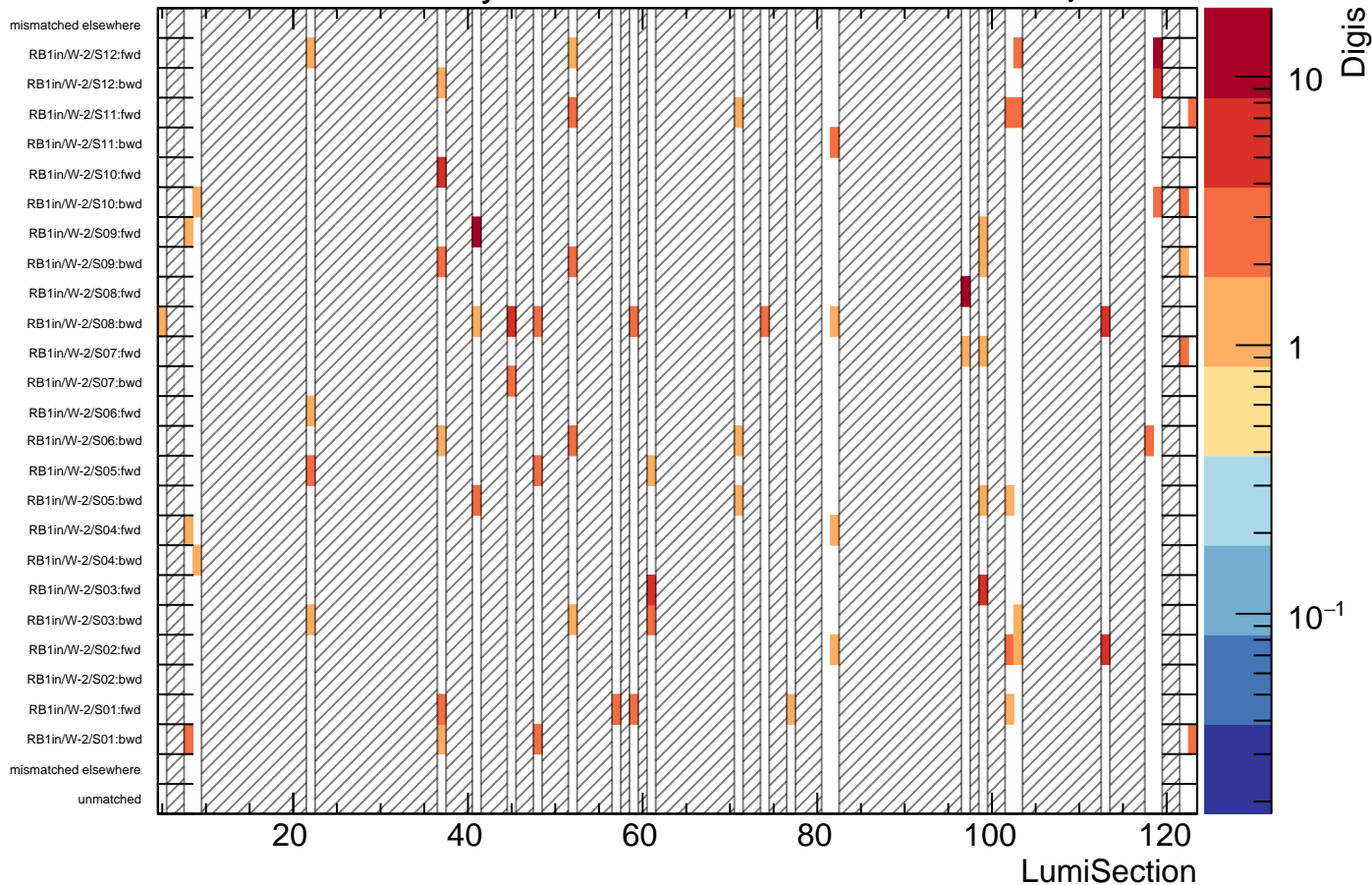
RB4/W+2: Legacy only

CMS Preliminary - run 306122



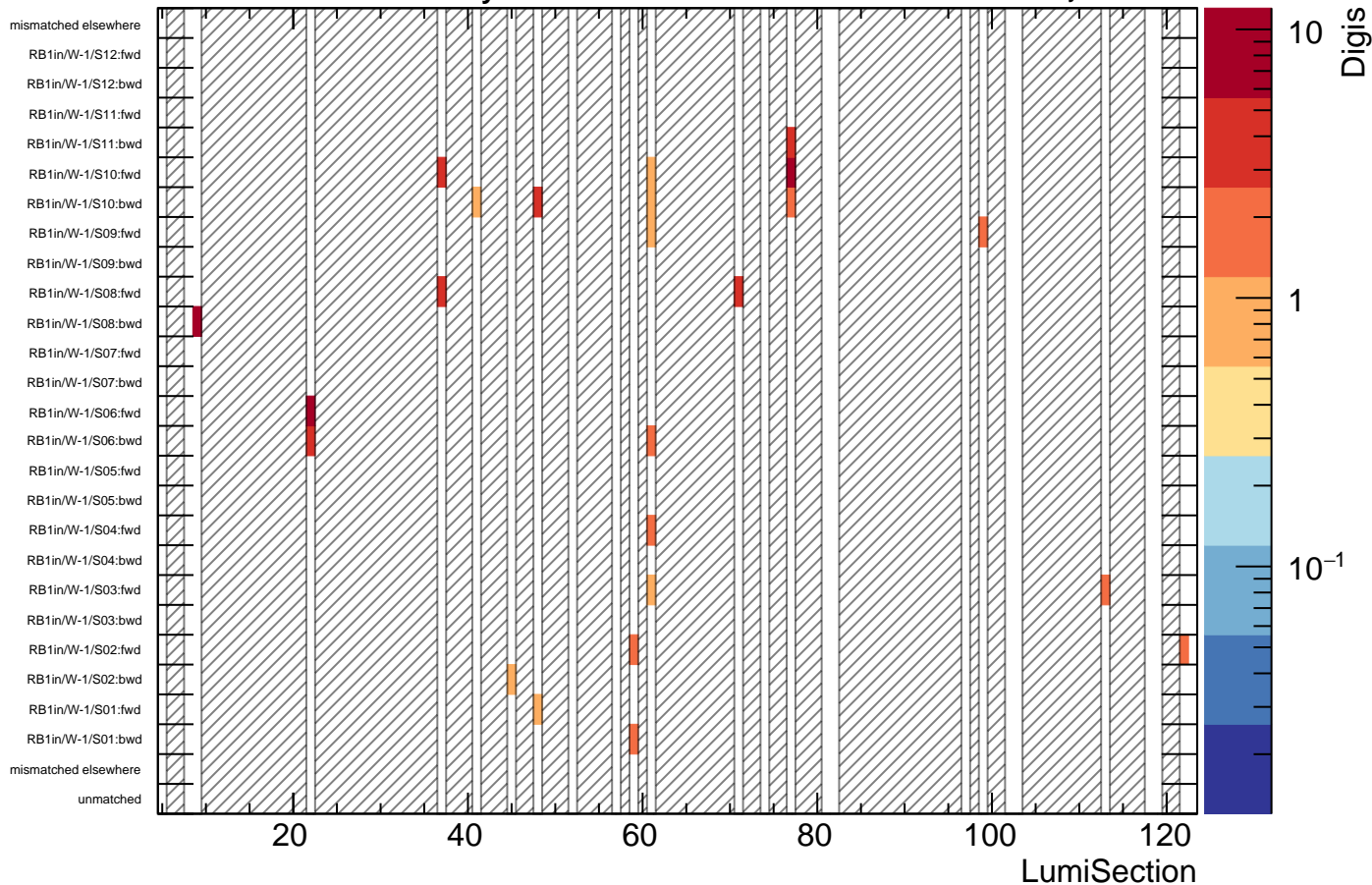
RB1in/W-2: TwinMux only

CMS Preliminary - run 306122



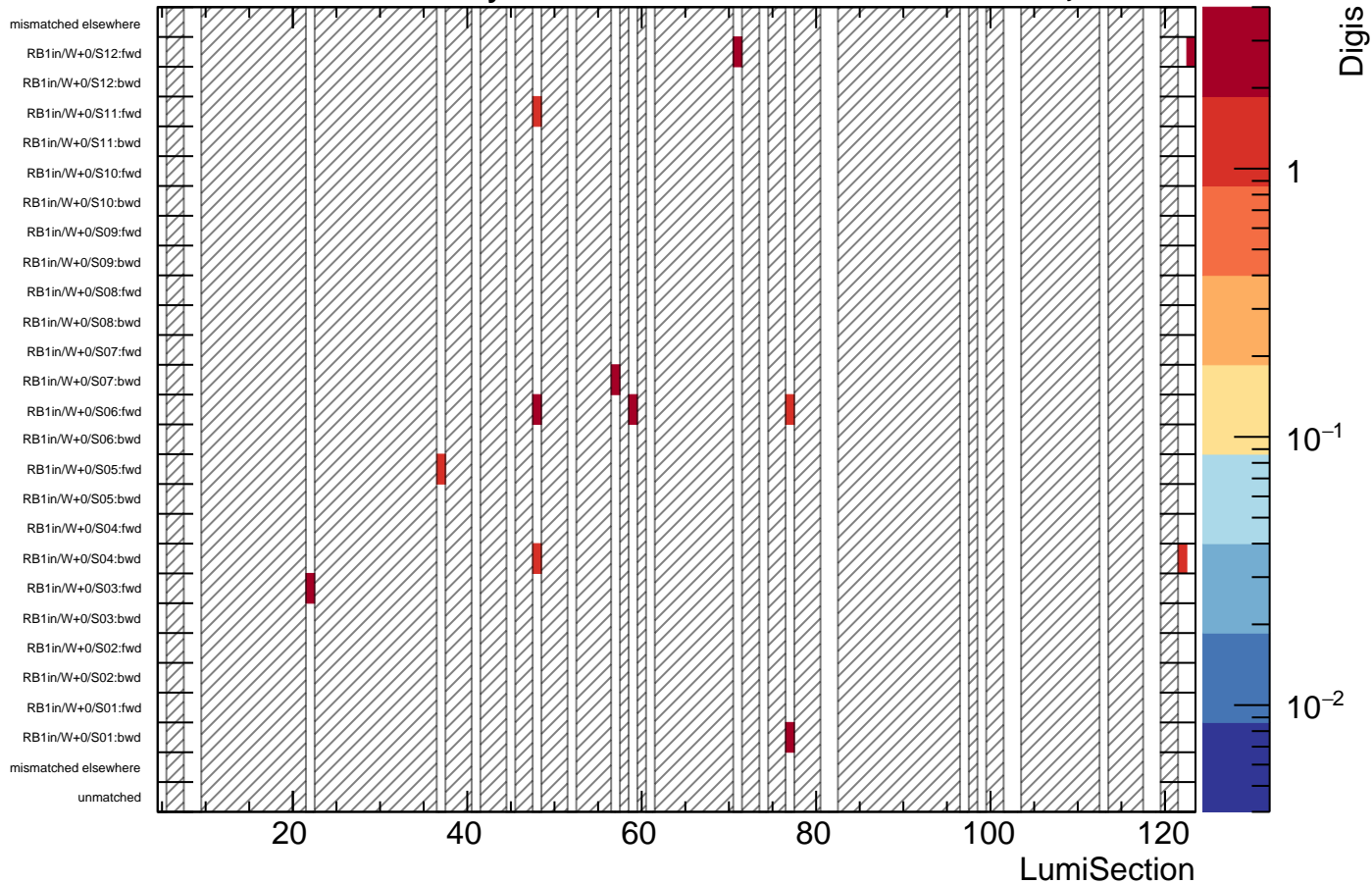
RB1in/W-1: TwinMux only

CMS Preliminary - run 306122



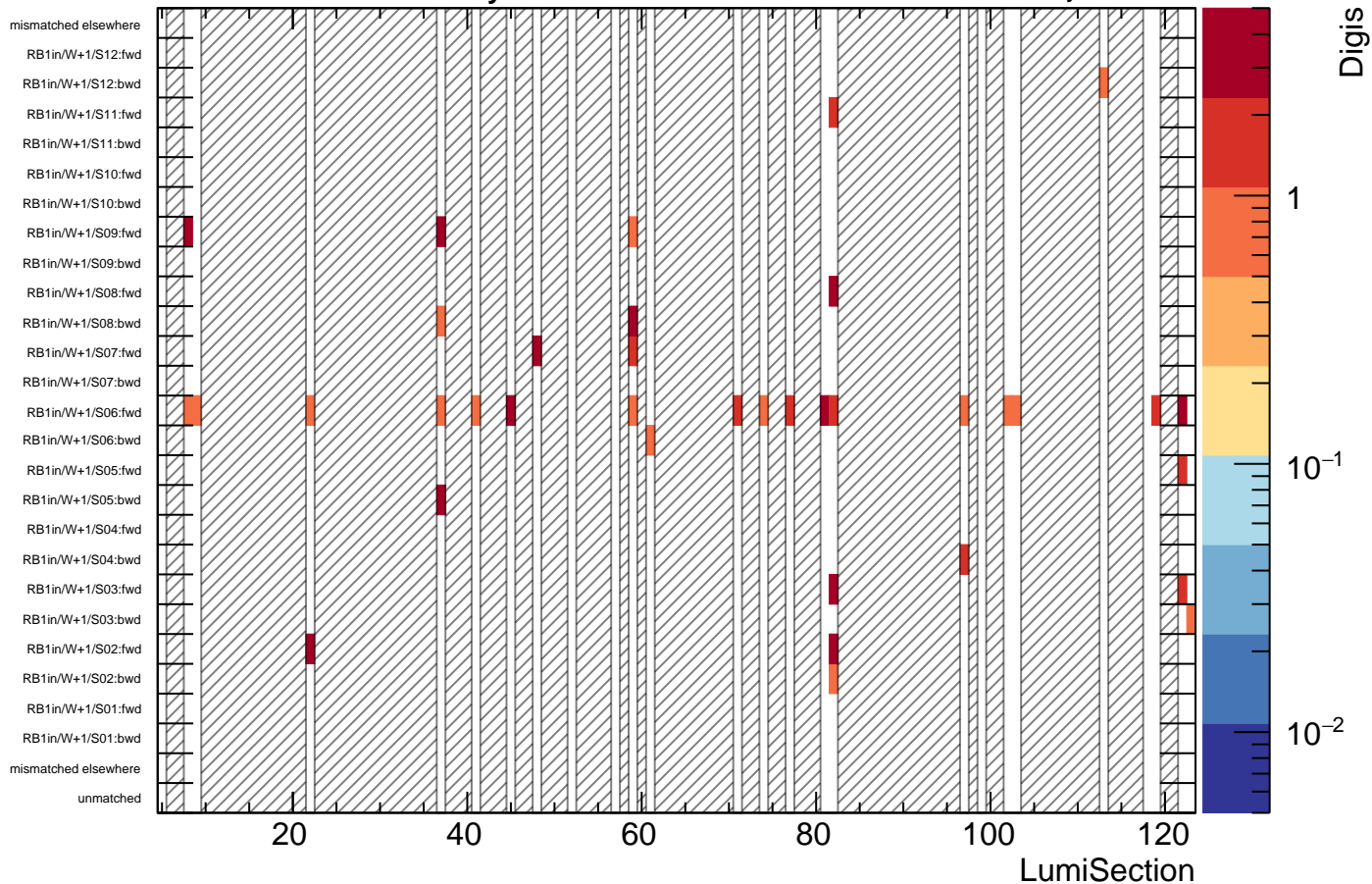
RB1in/W+0: TwinMux only

CMS Preliminary - run 306122



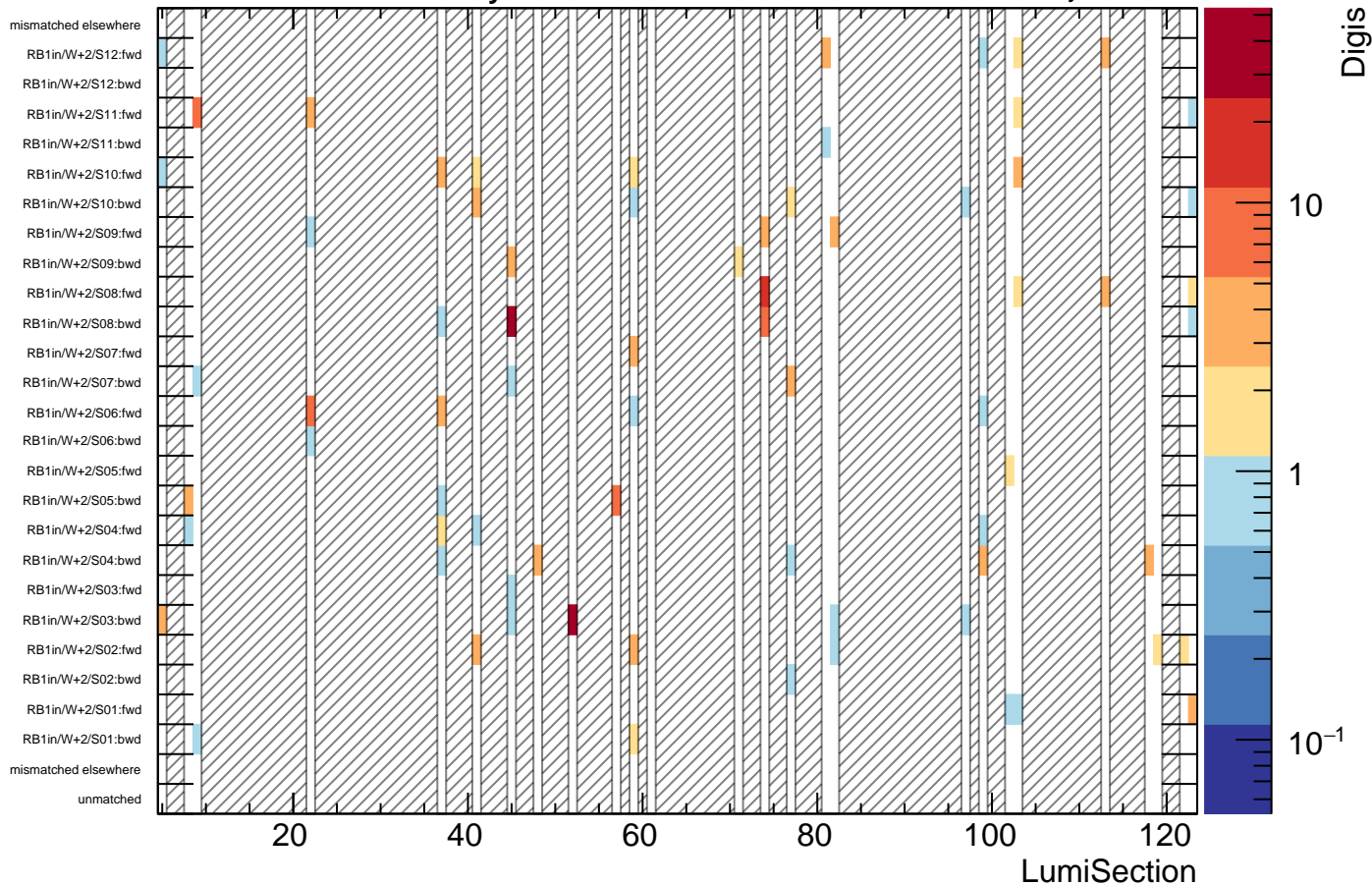
RB1in/W+1: TwinMux only

CMS Preliminary - run 306122



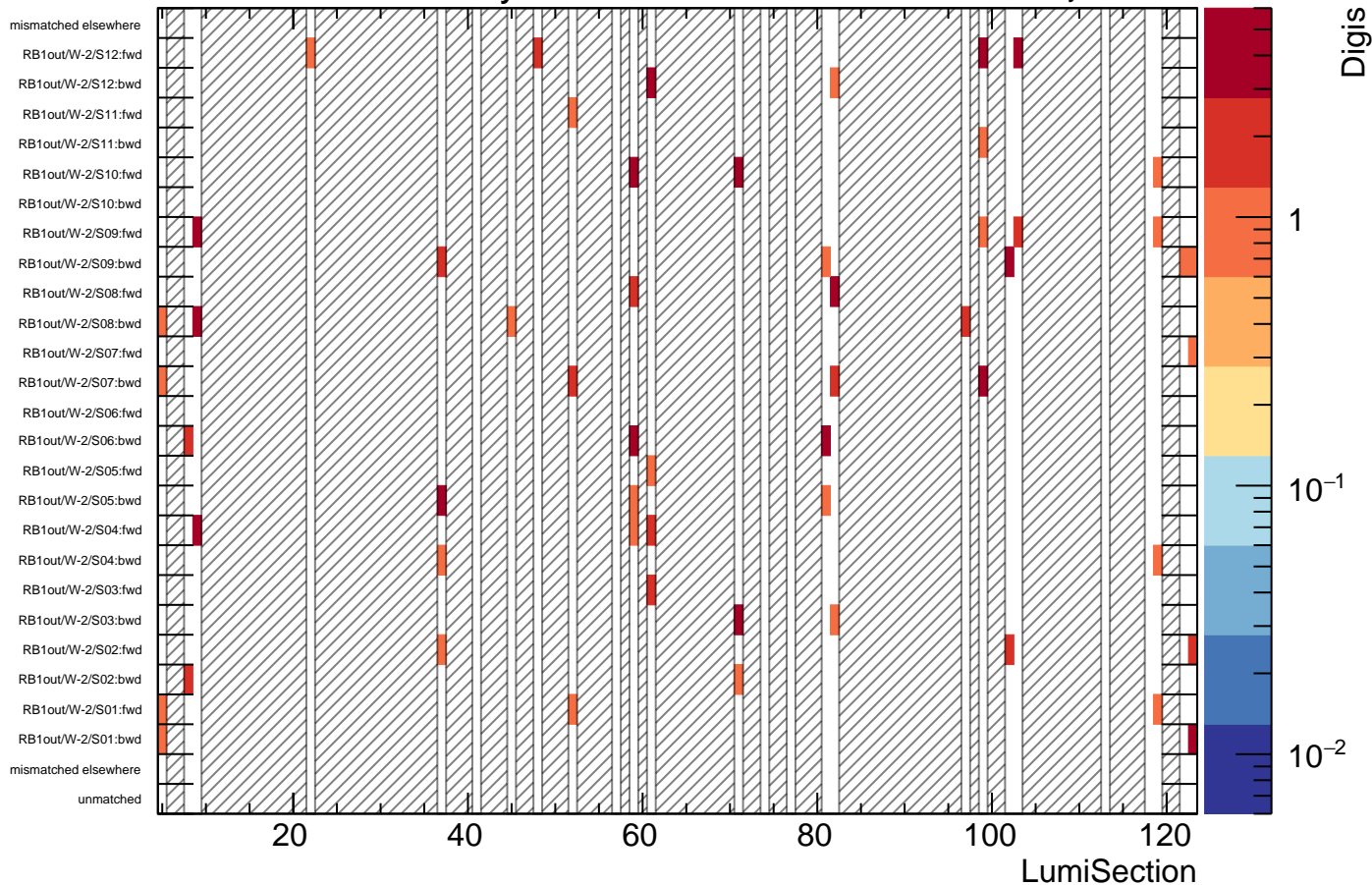
RB1in/W+2: TwinMux only

CMS Preliminary - run 306122



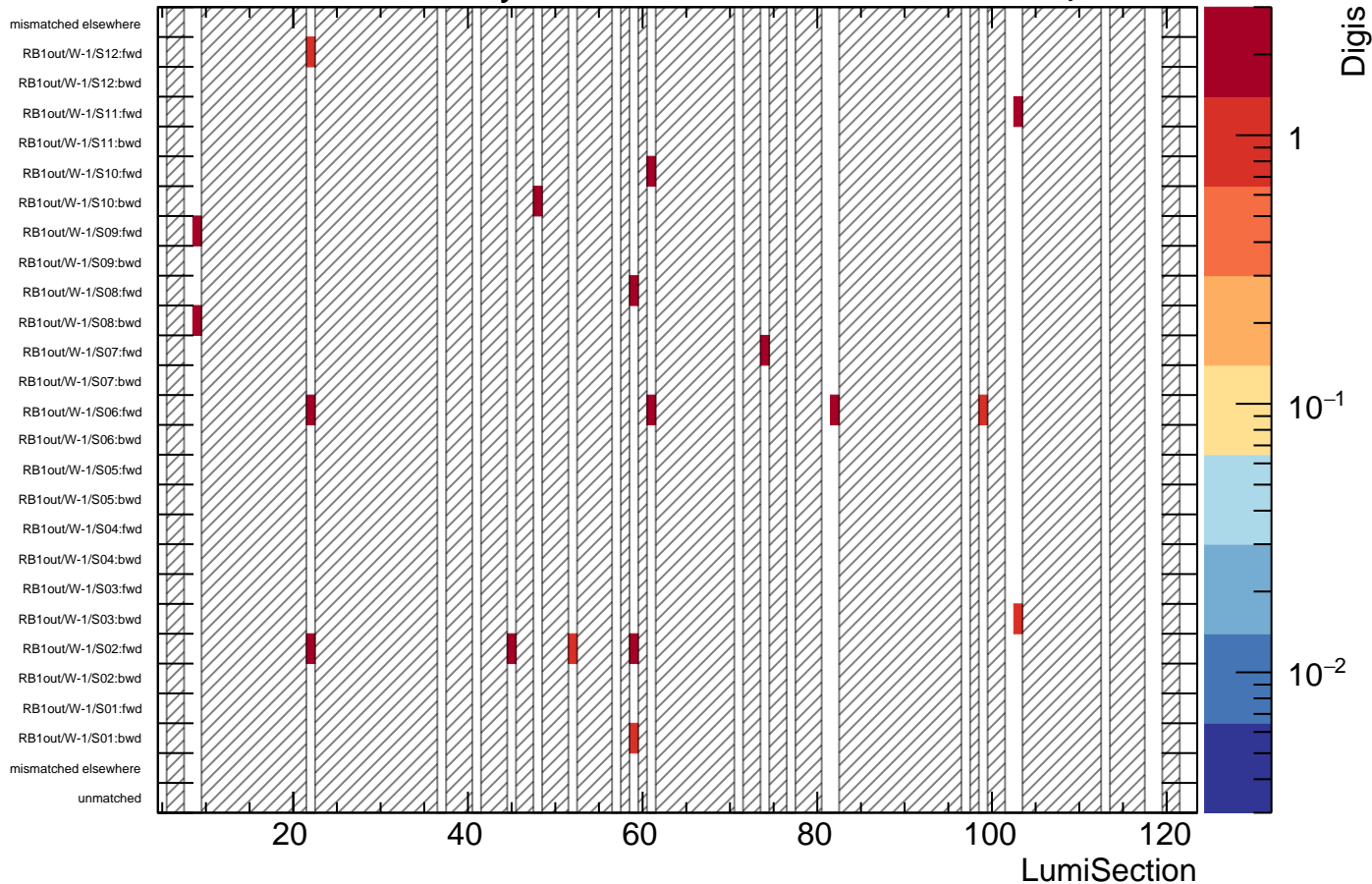
RB1out/W-2: TwinMux only

CMS Preliminary - run 306122



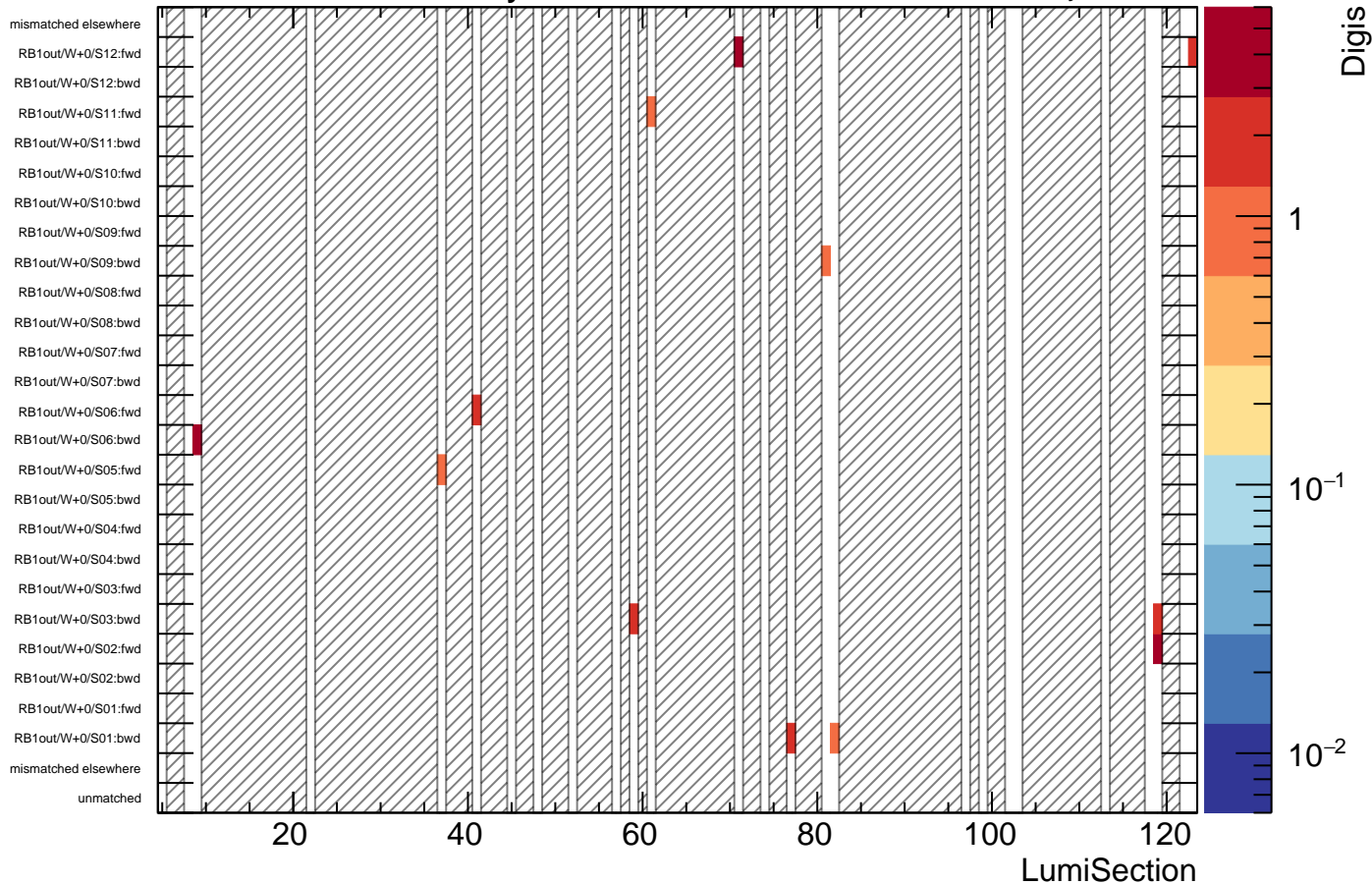
RB1out/W-1: TwinMux only

CMS Preliminary - run 306122



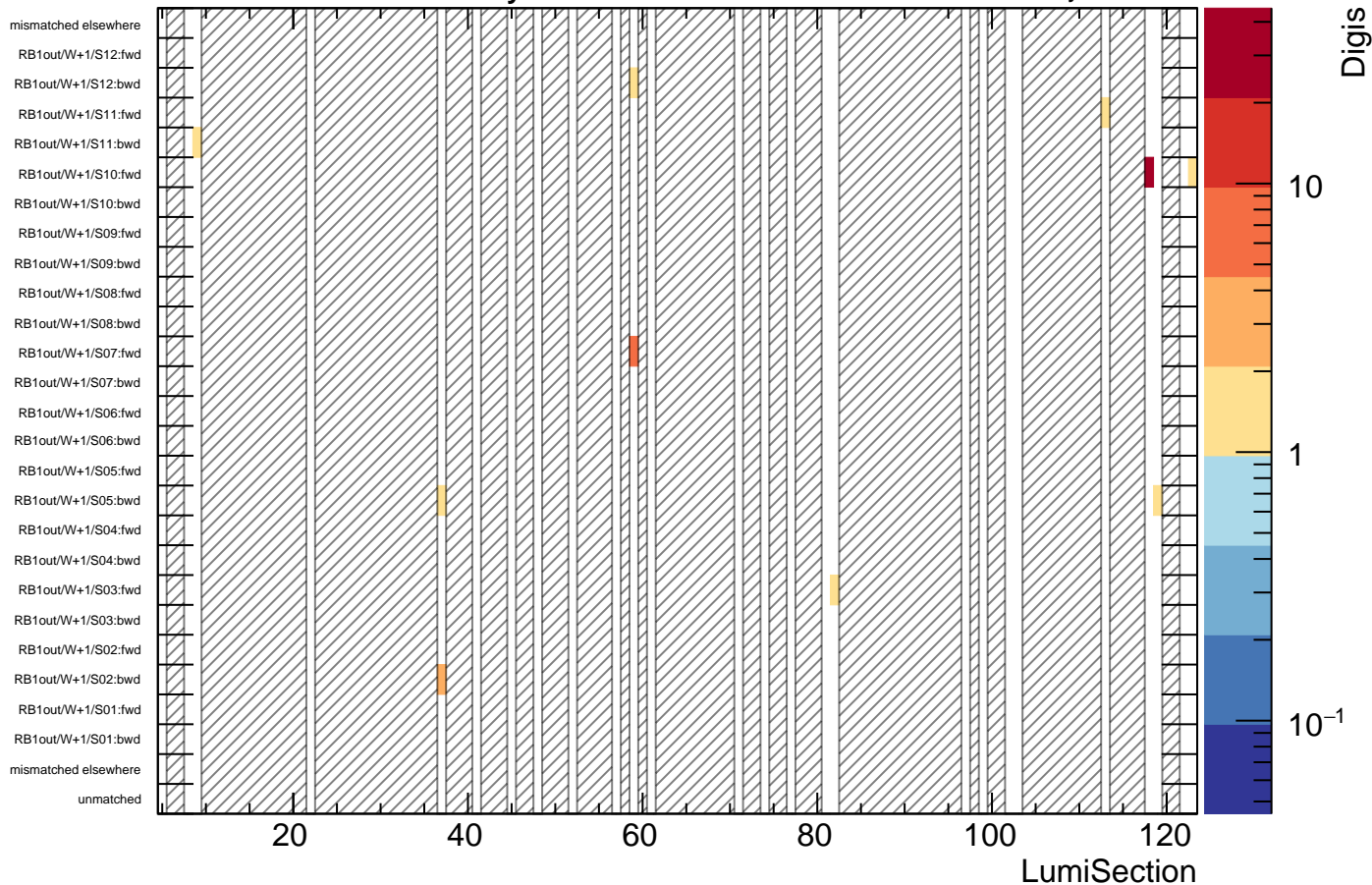
RB1out/W+0: TwinMux only

CMS Preliminary - run 306122



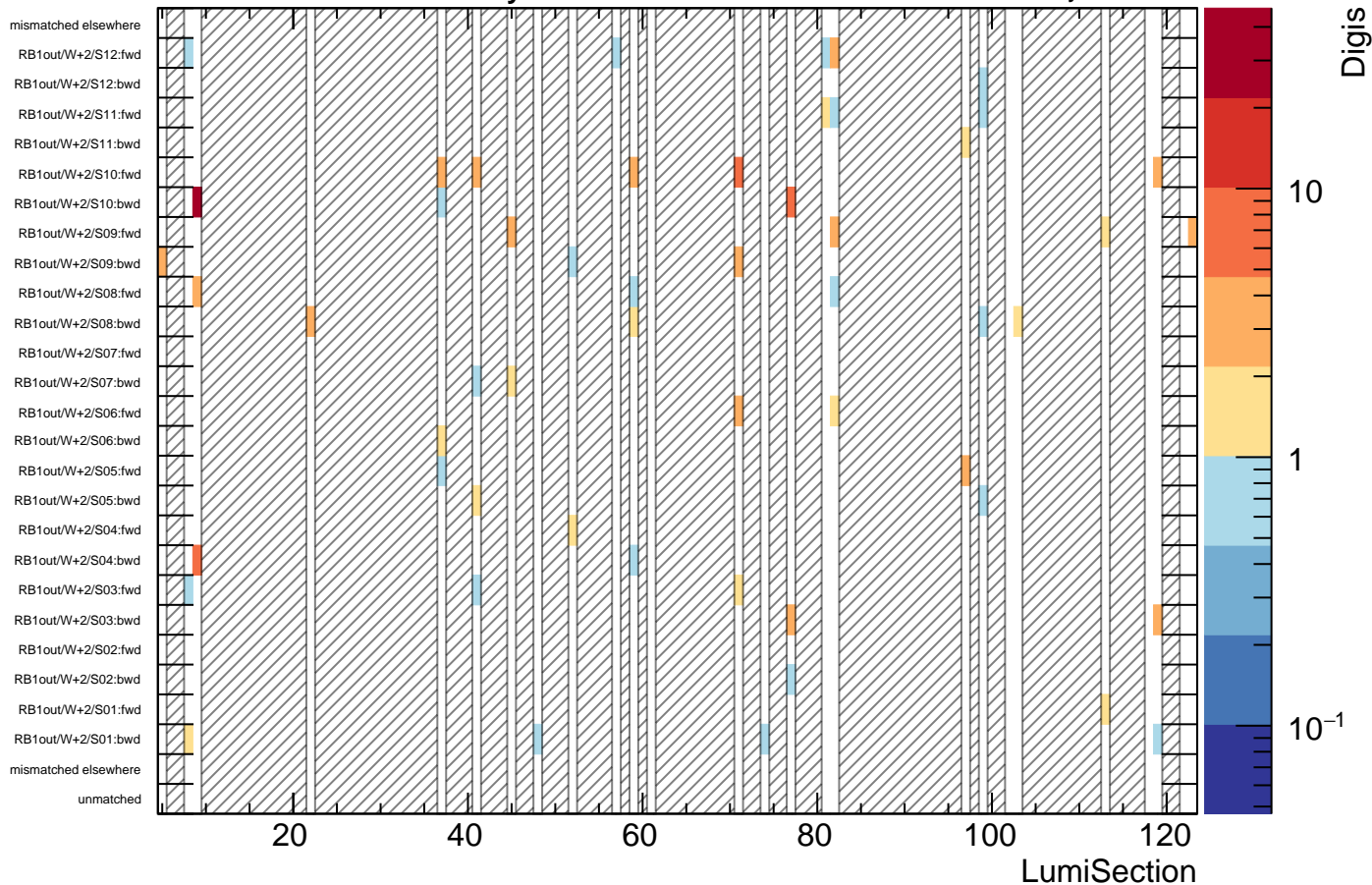
RB1out/W+1: TwinMux only

CMS Preliminary - run 306122



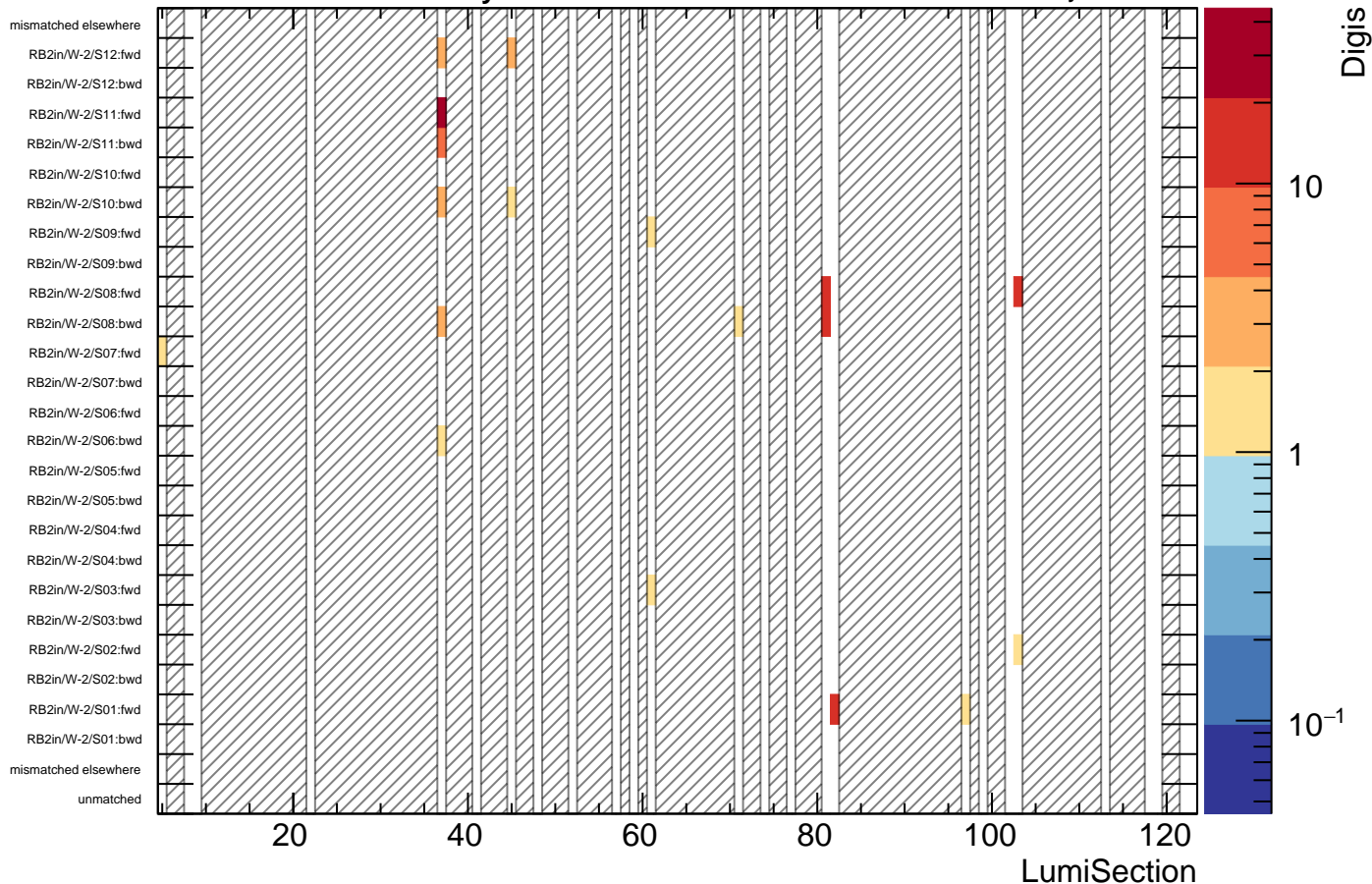
RB1out/W+2: TwinMux only

CMS Preliminary - run 306122



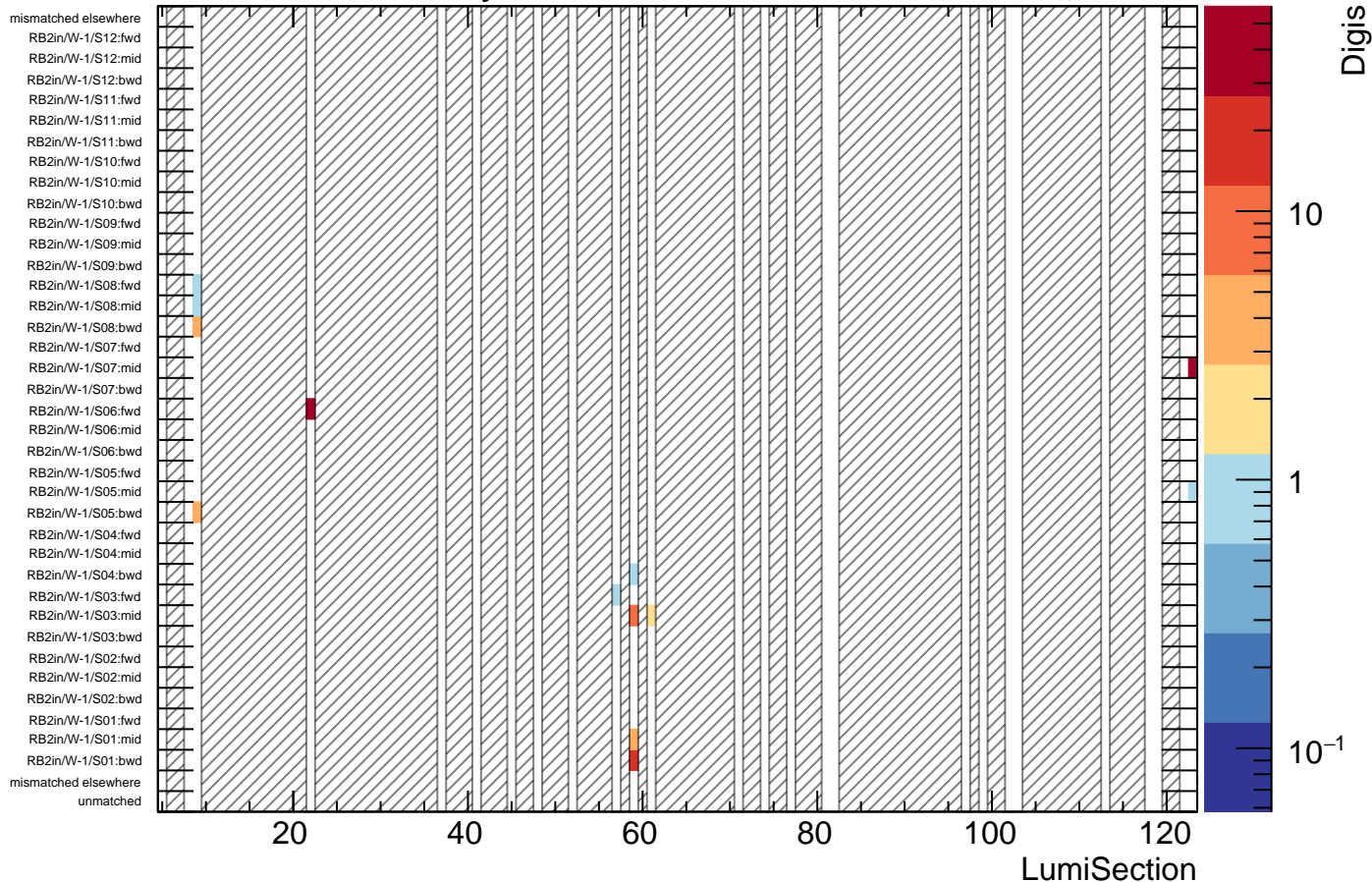
RB2in/W-2: TwinMux only

CMS Preliminary - run 306122



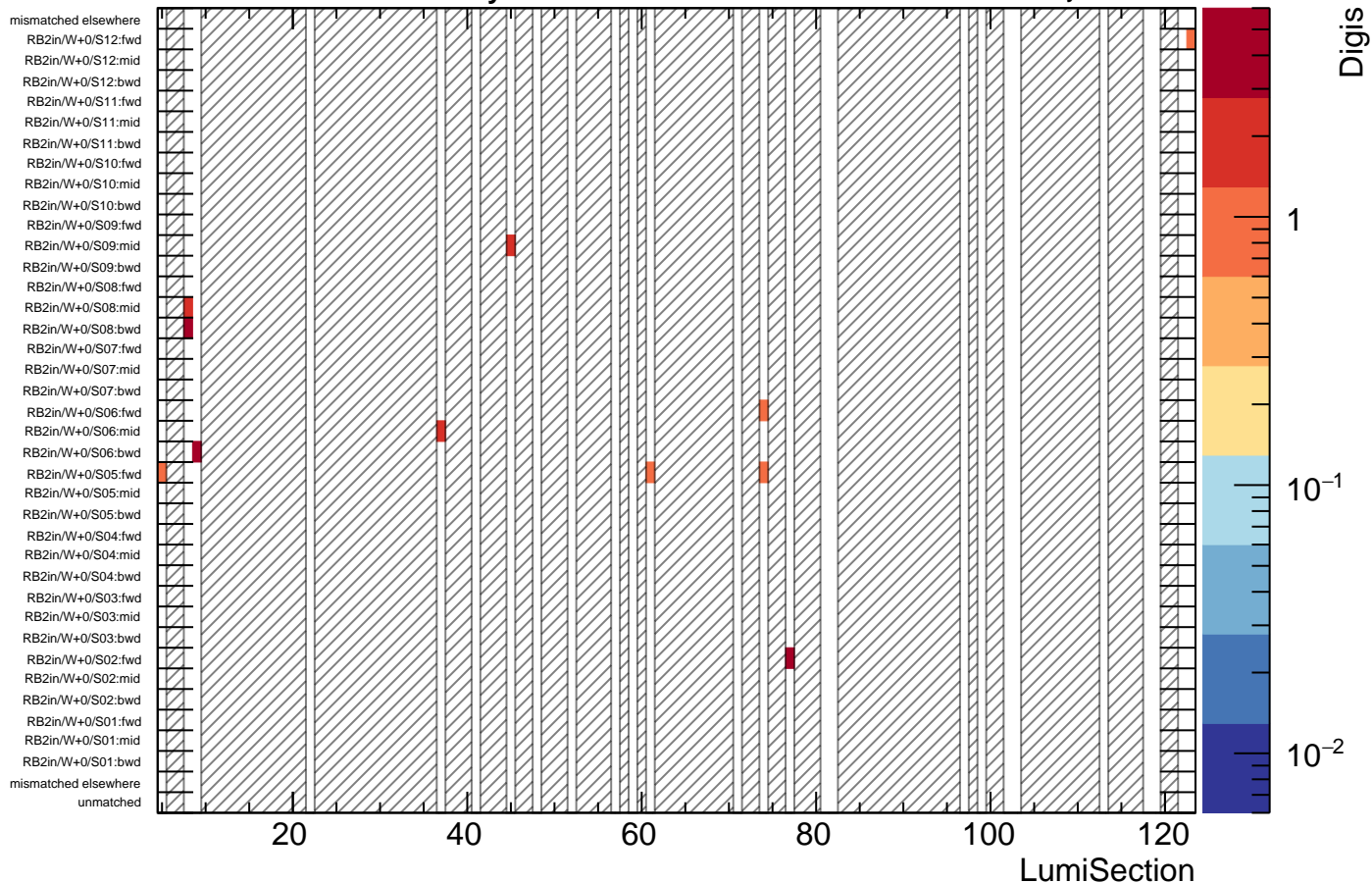
RB2in/W-1: TwinMux only

CMS Preliminary - run 306122



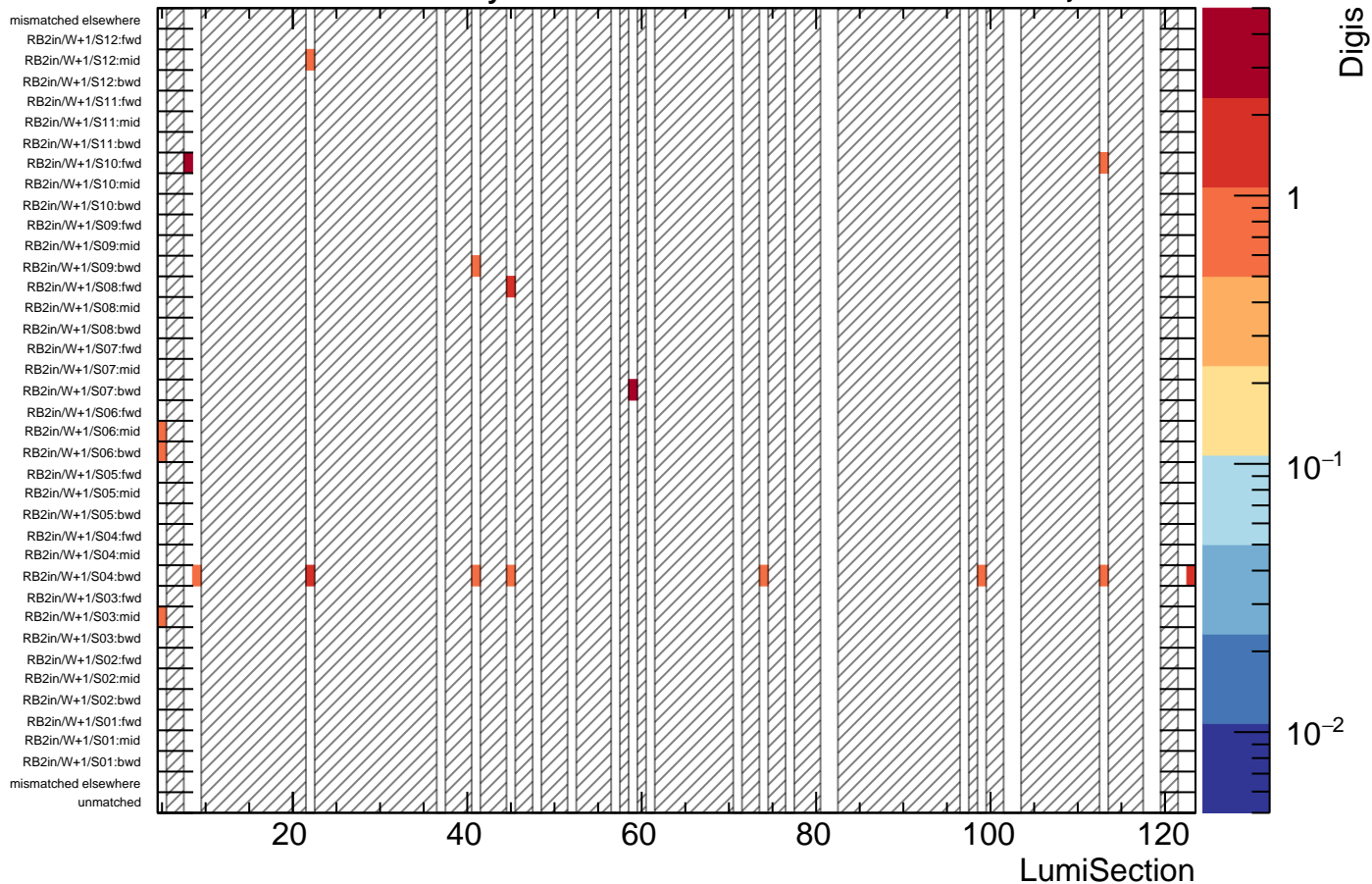
RB2in/W+0: TwinMux only

CMS Preliminary - run 306122



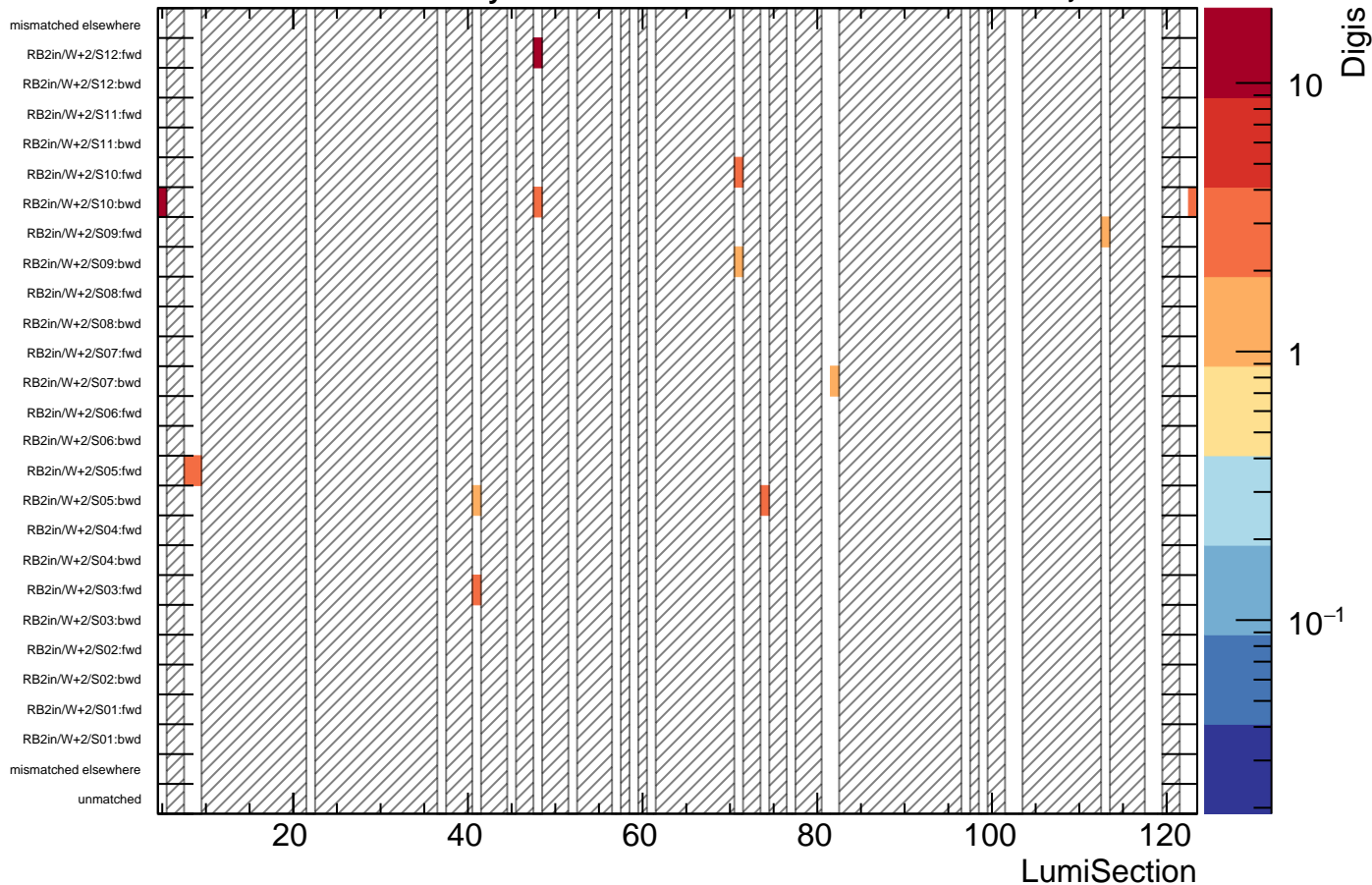
RB2in/W+1: TwinMux only

CMS Preliminary - run 306122



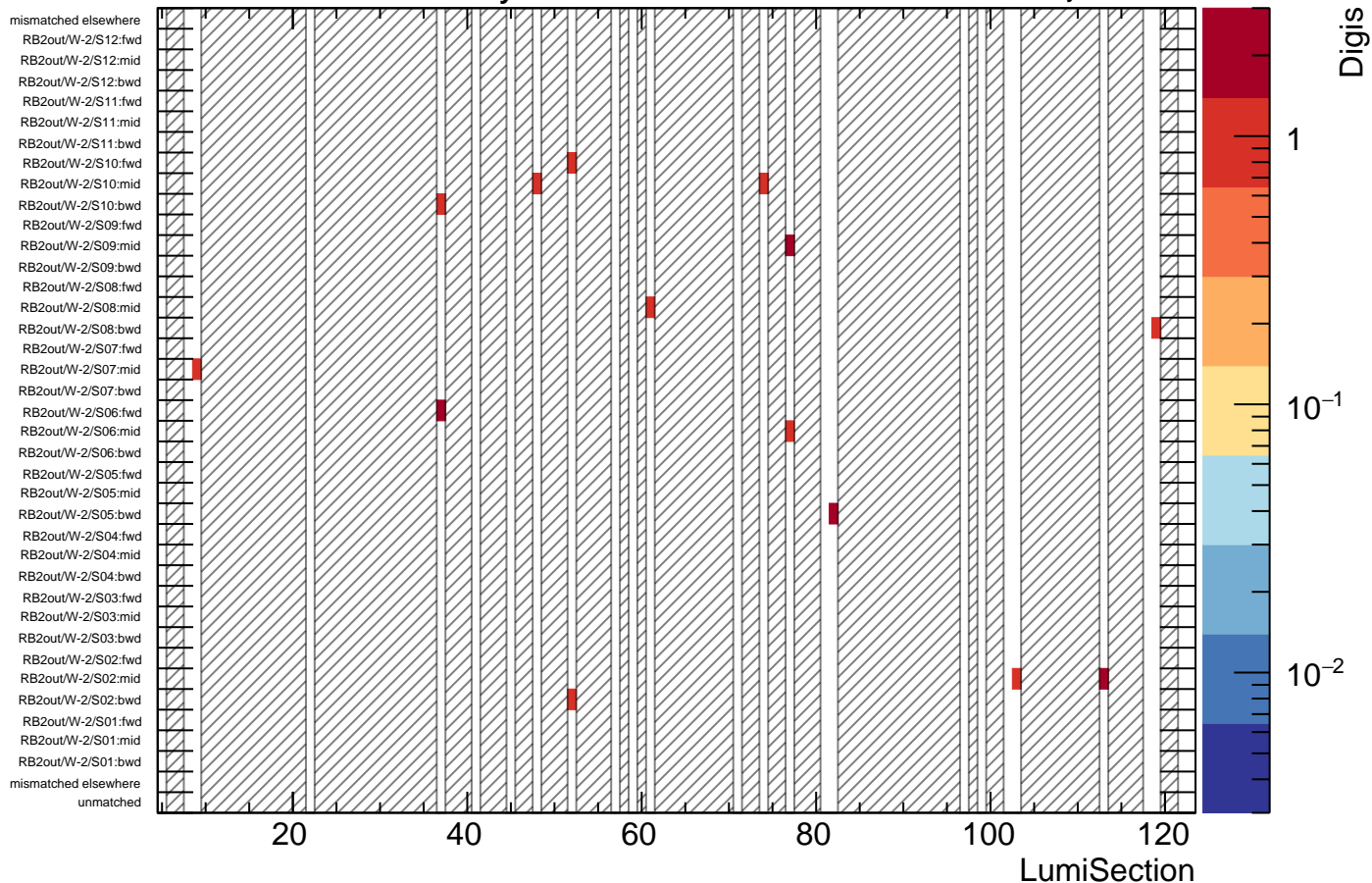
RB2in/W+2: TwinMux only

CMS Preliminary - run 306122



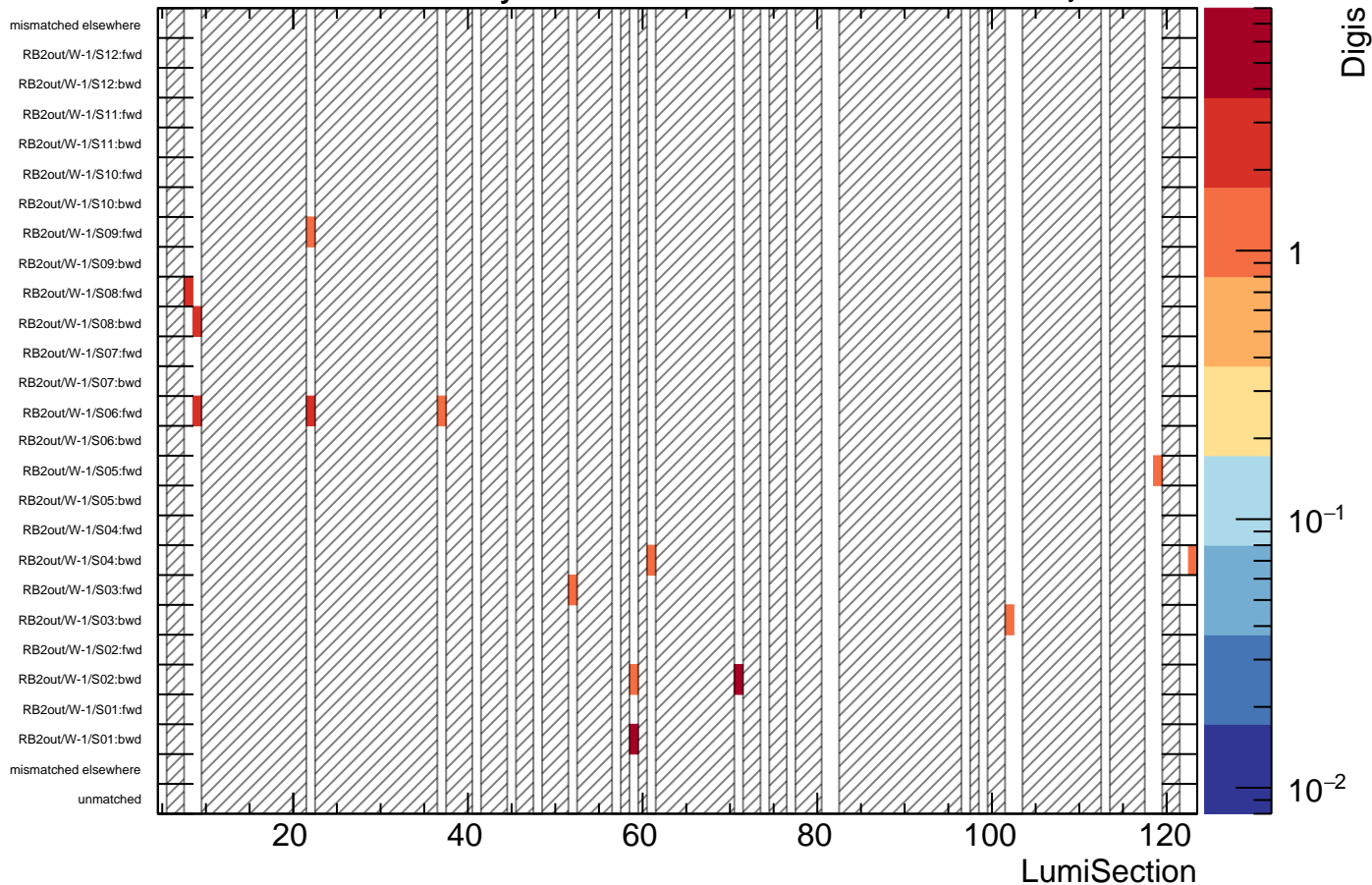
RB2out/W-2: TwinMux only

CMS Preliminary - run 306122



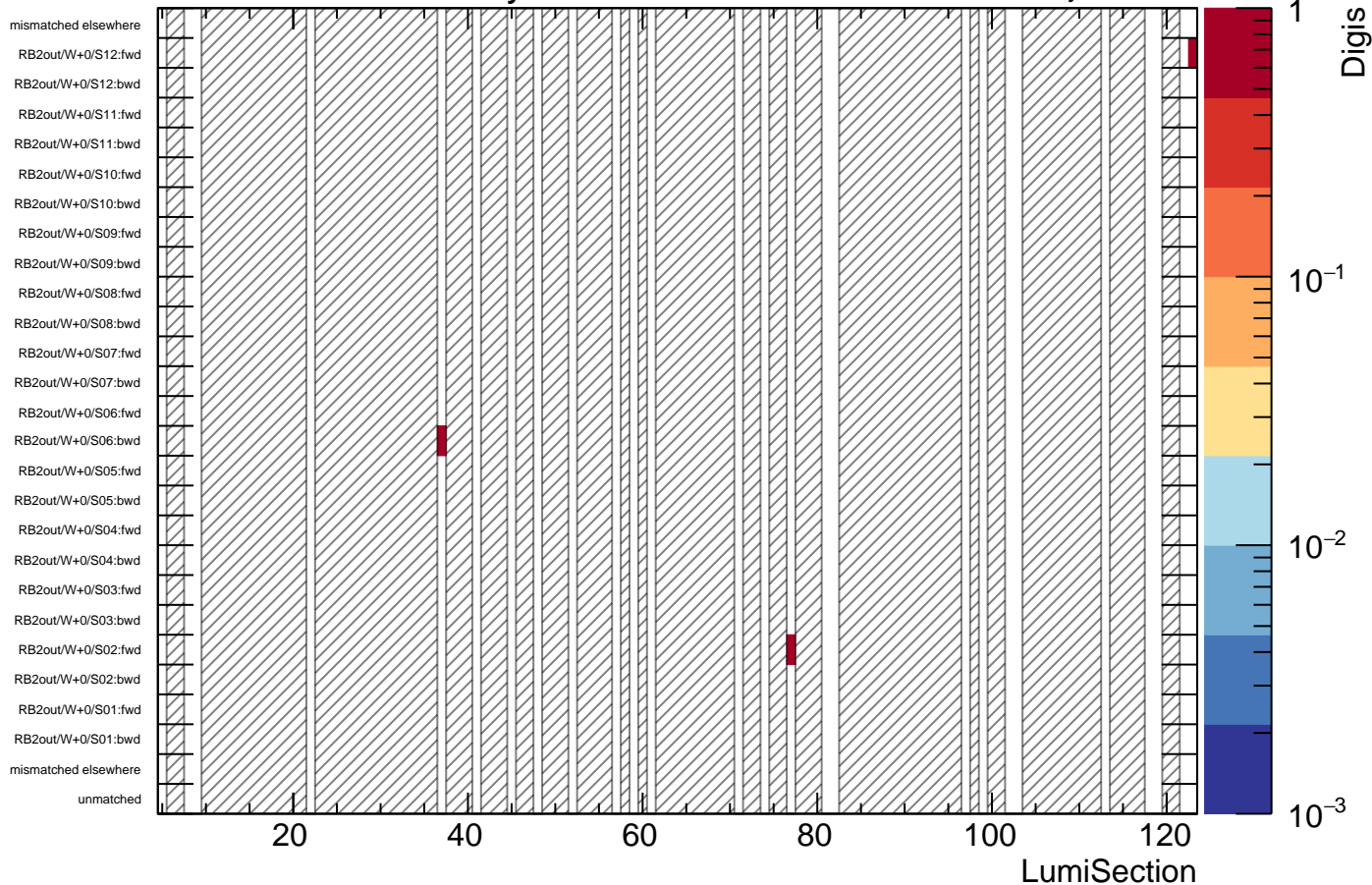
RB2out/W-1: TwinMux only

CMS Preliminary - run 306122



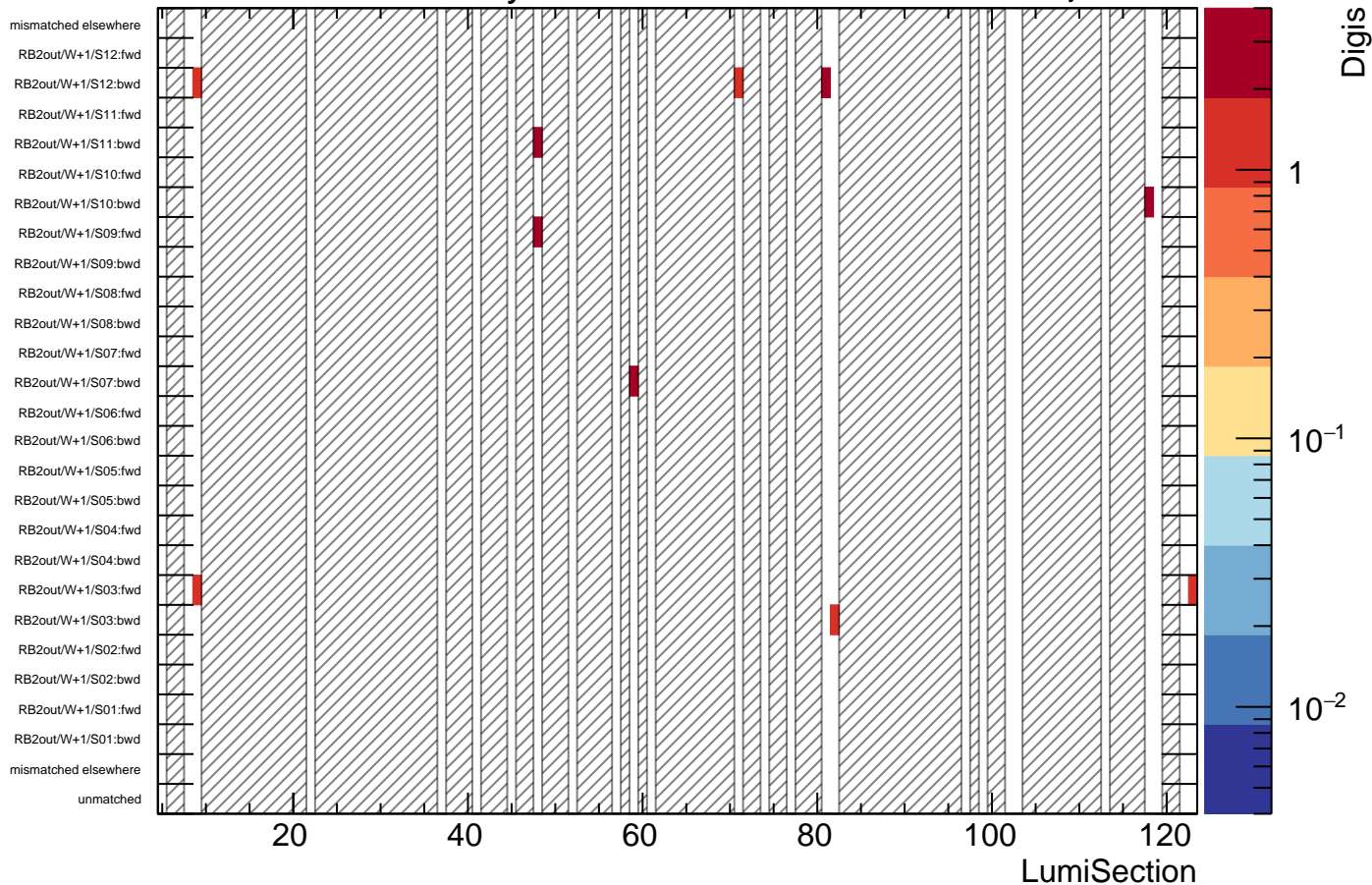
RB2out/W+0: TwinMux only

CMS Preliminary - run 306122



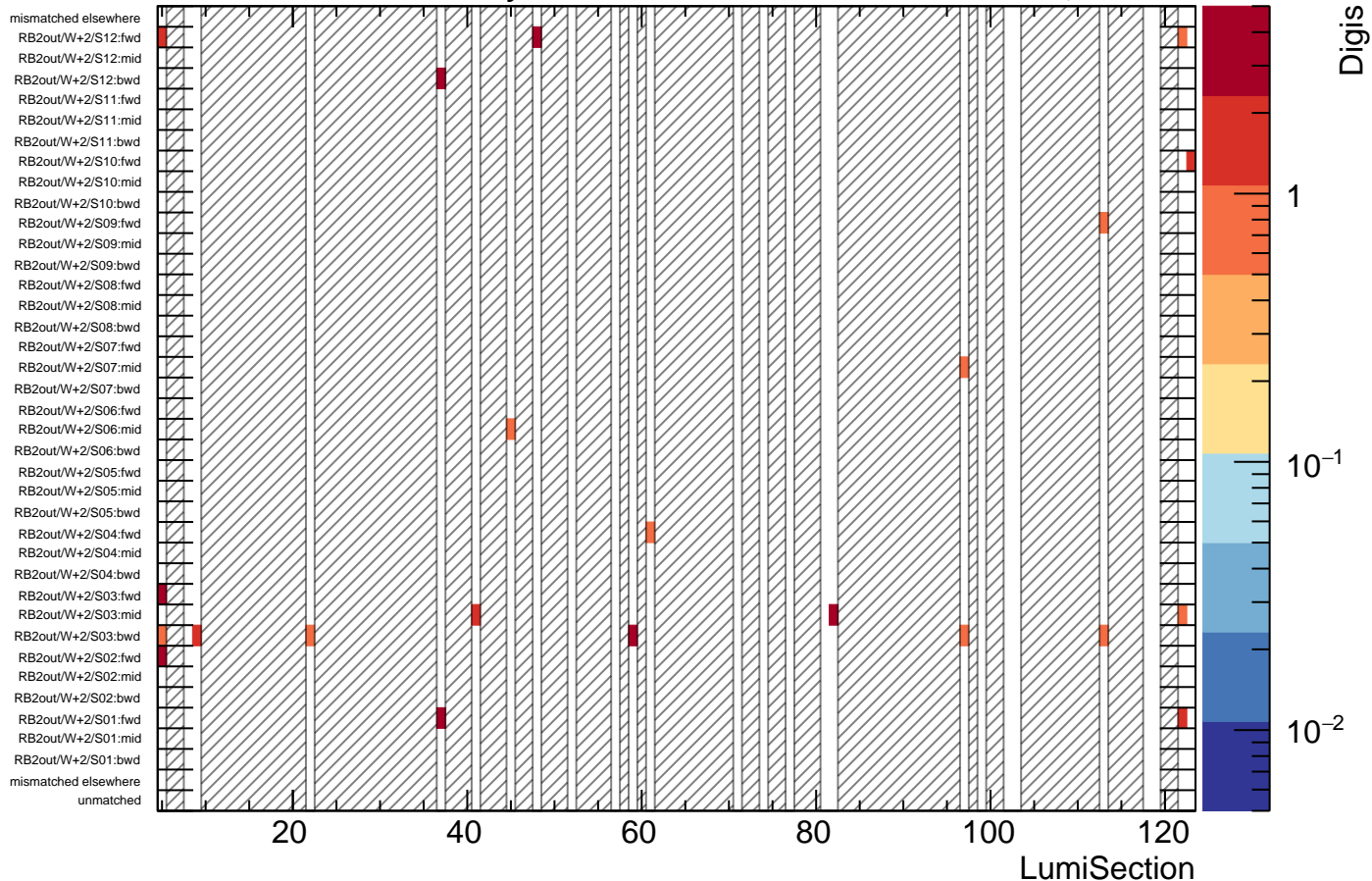
RB2out/W+1: TwinMux only

CMS Preliminary - run 306122



RB2out/W+2: TwinMux only

CMS Preliminary - run 306122



RB3/W-2: TwinMux only

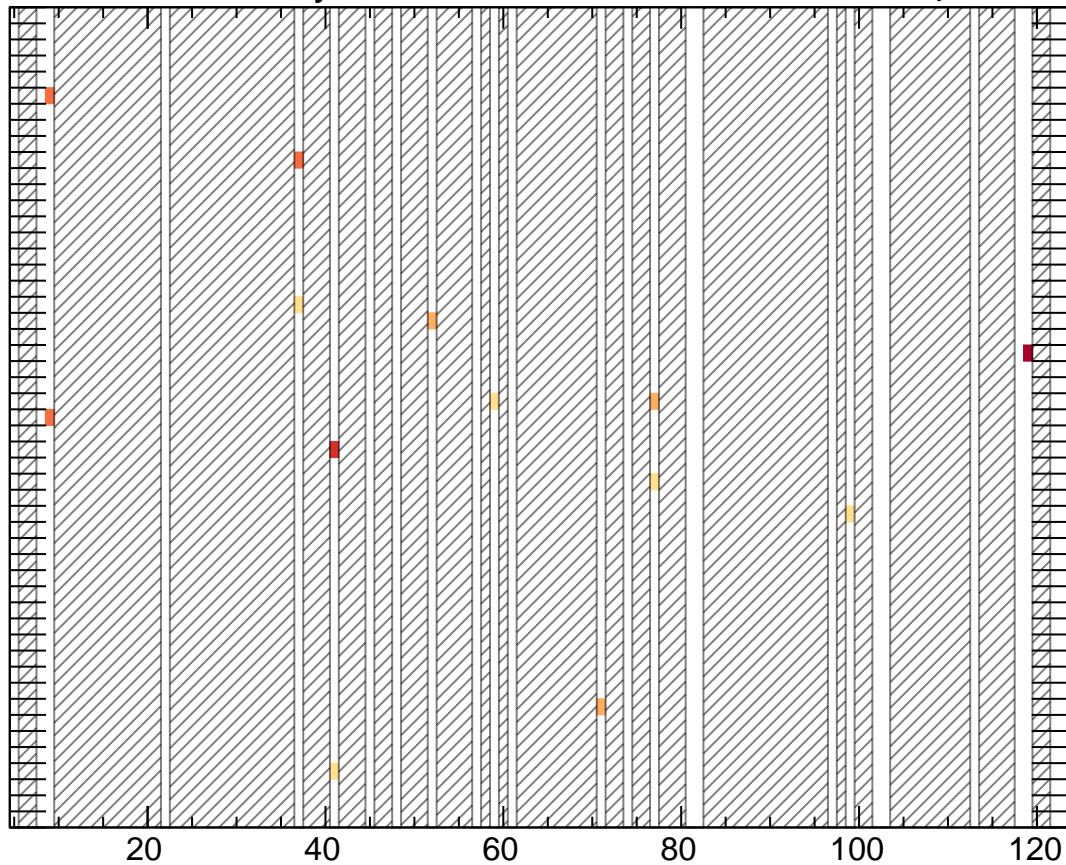
CMS Preliminary - run 306122

mismatched elsewhere

RB3/W-2/S12+*fwd*
 RB3/W-2/S12+*bwd*
 RB3/W-2/S12-*fwd*
 RB3/W-2/S12-*bwd*
 RB3/W-2/S11+*fwd*
 RB3/W-2/S11+*bwd*
 RB3/W-2/S11-*fwd*
 RB3/W-2/S11-*bwd*
 RB3/W-2/S10+*fwd*
 RB3/W-2/S10+*bwd*
 RB3/W-2/S10-*fwd*
 RB3/W-2/S10-*bwd*
 RB3/W-2/S09+*fwd*
 RB3/W-2/S09+*bwd*
 RB3/W-2/S09-*fwd*
 RB3/W-2/S09-*bwd*
 RB3/W-2/S08+*fwd*
 RB3/W-2/S08+*bwd*
 RB3/W-2/S08-*fwd*
 RB3/W-2/S08-*bwd*
 RB3/W-2/S07+*fwd*
 RB3/W-2/S07+*bwd*
 RB3/W-2/S07-*fwd*
 RB3/W-2/S07-*bwd*
 RB3/W-2/S06+*fwd*
 RB3/W-2/S06+*bwd*
 RB3/W-2/S06-*fwd*
 RB3/W-2/S06-*bwd*
 RB3/W-2/S05+*fwd*
 RB3/W-2/S05+*bwd*
 RB3/W-2/S05-*fwd*
 RB3/W-2/S05-*bwd*
 RB3/W-2/S04+*fwd*
 RB3/W-2/S04+*bwd*
 RB3/W-2/S04-*fwd*
 RB3/W-2/S04-*bwd*
 RB3/W-2/S03+*fwd*
 RB3/W-2/S03+*bwd*
 RB3/W-2/S03-*fwd*
 RB3/W-2/S03-*bwd*
 RB3/W-2/S02+*fwd*
 RB3/W-2/S02+*bwd*
 RB3/W-2/S02-*fwd*
 RB3/W-2/S02-*bwd*
 RB3/W-2/S01+*fwd*
 RB3/W-2/S01+*bwd*
 RB3/W-2/S01-*fwd*
 RB3/W-2/S01-*bwd*

mismatched elsewhere

unmatched



LumiSection

Digis

10

1

10⁻¹

RB3/W-1: TwinMux only

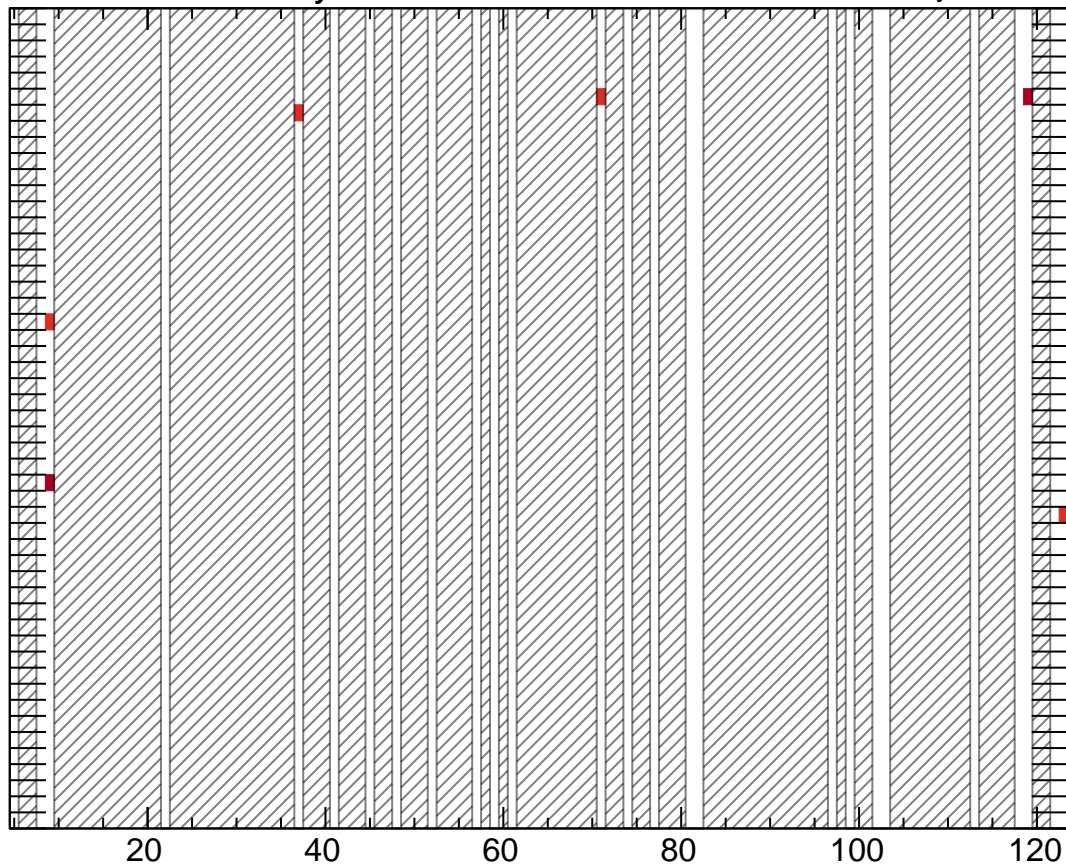
CMS Preliminary - run 306122

mismatched elsewhere

RB3/W-1/S12+*fwd*
 RB3/W-1/S12+*bwd*
 RB3/W-1/S12-*fwd*
 RB3/W-1/S12-*bwd*
 RB3/W-1/S11+*fwd*
 RB3/W-1/S11+*bwd*
 RB3/W-1/S11-*fwd*
 RB3/W-1/S11-*bwd*
 RB3/W-1/S10+*fwd*
 RB3/W-1/S10+*bwd*
 RB3/W-1/S10-*fwd*
 RB3/W-1/S10-*bwd*
 RB3/W-1/S09+*fwd*
 RB3/W-1/S09+*bwd*
 RB3/W-1/S09-*fwd*
 RB3/W-1/S09-*bwd*
 RB3/W-1/S08+*fwd*
 RB3/W-1/S08+*bwd*
 RB3/W-1/S08-*fwd*
 RB3/W-1/S08-*bwd*
 RB3/W-1/S07+*fwd*
 RB3/W-1/S07+*bwd*
 RB3/W-1/S07-*fwd*
 RB3/W-1/S07-*bwd*
 RB3/W-1/S06+*fwd*
 RB3/W-1/S06+*bwd*
 RB3/W-1/S06-*fwd*
 RB3/W-1/S06-*bwd*
 RB3/W-1/S05+*fwd*
 RB3/W-1/S05+*bwd*
 RB3/W-1/S05-*fwd*
 RB3/W-1/S05-*bwd*
 RB3/W-1/S04+*fwd*
 RB3/W-1/S04+*bwd*
 RB3/W-1/S04-*fwd*
 RB3/W-1/S04-*bwd*
 RB3/W-1/S03+*fwd*
 RB3/W-1/S03+*bwd*
 RB3/W-1/S03-*fwd*
 RB3/W-1/S03-*bwd*
 RB3/W-1/S02+*fwd*
 RB3/W-1/S02+*bwd*
 RB3/W-1/S02-*fwd*
 RB3/W-1/S02-*bwd*
 RB3/W-1/S01+*fwd*
 RB3/W-1/S01+*bwd*
 RB3/W-1/S01-*fwd*
 RB3/W-1/S01-*bwd*

mismatched elsewhere

unmatched



Digis

1

10^{-1}

10^{-2}

LumiSection

RB3/W+0: TwinMux only

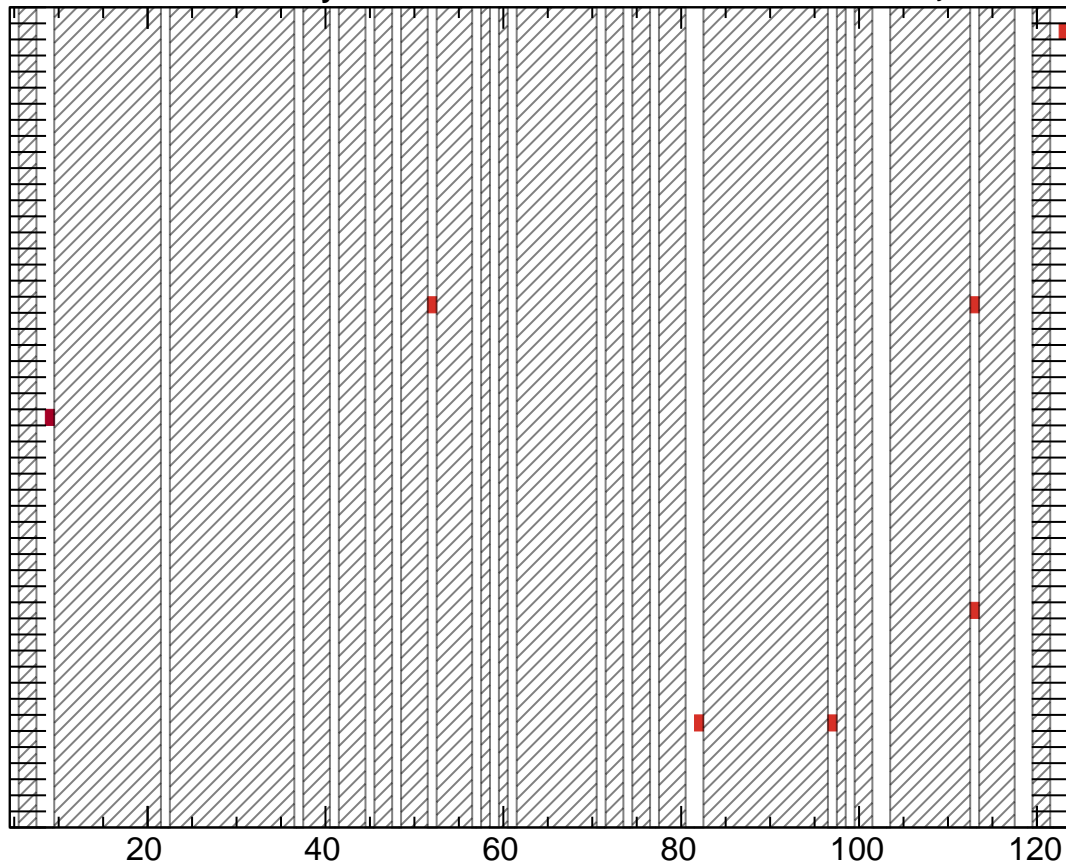
CMS Preliminary - run 306122

mismatched elsewhere

RB3/W+0/S12+-fwd
RB3/W+0/S12+-bwd
RB3/W+0/S12+-fwd
RB3/W+0/S12+-bwd
RB3/W+0/S11+-fwd
RB3/W+0/S11+-bwd
RB3/W+0/S11+-fwd
RB3/W+0/S11+-bwd
RB3/W+0/S10+-fwd
RB3/W+0/S10+-bwd
RB3/W+0/S10+-fwd
RB3/W+0/S10+-bwd
RB3/W+0/S09+-fwd
RB3/W+0/S09+-bwd
RB3/W+0/S09+-fwd
RB3/W+0/S09+-bwd
RB3/W+0/S08+-fwd
RB3/W+0/S08+-bwd
RB3/W+0/S08+-fwd
RB3/W+0/S08+-bwd
RB3/W+0/S07+-fwd
RB3/W+0/S07+-bwd
RB3/W+0/S07+-fwd
RB3/W+0/S07+-bwd
RB3/W+0/S06+-fwd
RB3/W+0/S06+-bwd
RB3/W+0/S06+-fwd
RB3/W+0/S06+-bwd
RB3/W+0/S05+-fwd
RB3/W+0/S05+-bwd
RB3/W+0/S05+-fwd
RB3/W+0/S05+-bwd
RB3/W+0/S04+-fwd
RB3/W+0/S04+-bwd
RB3/W+0/S04+-fwd
RB3/W+0/S04+-bwd
RB3/W+0/S03+-fwd
RB3/W+0/S03+-bwd
RB3/W+0/S03+-fwd
RB3/W+0/S03+-bwd
RB3/W+0/S02+-fwd
RB3/W+0/S02+-bwd
RB3/W+0/S02+-fwd
RB3/W+0/S02+-bwd
RB3/W+0/S01+-fwd
RB3/W+0/S01+-bwd
RB3/W+0/S01+-fwd
RB3/W+0/S01+-bwd

mismatched elsewhere

unmatched



Digis

1

10^{-1}

10^{-2}

20

40

60

80

100

120

LumiSection

RB3/W+1: TwinMux only

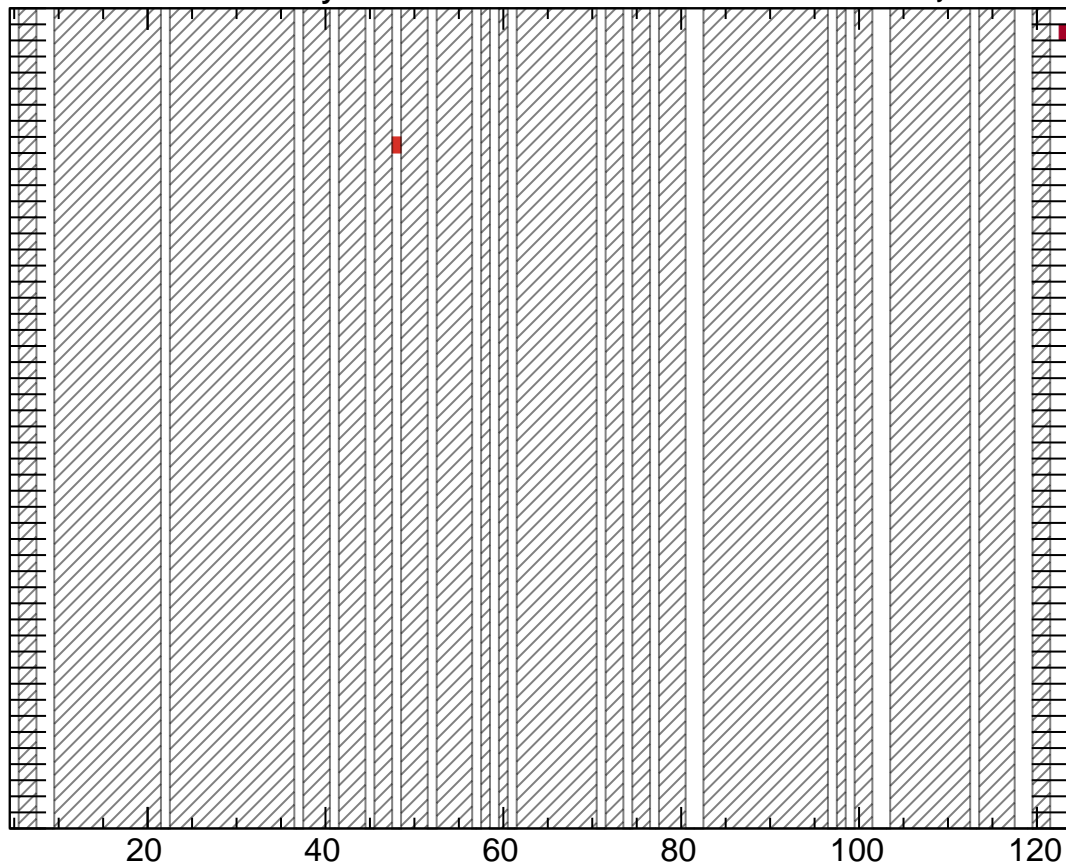
CMS Preliminary - run 306122

mismatched elsewhere

RB3/W+1/S12+-fwd
RB3/W+1/S12+-bwd
RB3/W+1/S12--fwd
RB3/W+1/S12--bwd
RB3/W+1/S11+-fwd
RB3/W+1/S11+-bwd
RB3/W+1/S11--fwd
RB3/W+1/S10+-fwd
RB3/W+1/S10+-bwd
RB3/W+1/S10--fwd
RB3/W+1/S10--bwd
RB3/W+1/S09+-fwd
RB3/W+1/S09+-bwd
RB3/W+1/S09--fwd
RB3/W+1/S09--bwd
RB3/W+1/S08+-fwd
RB3/W+1/S08+-bwd
RB3/W+1/S08--fwd
RB3/W+1/S08--bwd
RB3/W+1/S07+-fwd
RB3/W+1/S07+-bwd
RB3/W+1/S07--fwd
RB3/W+1/S07--bwd
RB3/W+1/S06+-fwd
RB3/W+1/S06+-bwd
RB3/W+1/S06--fwd
RB3/W+1/S06--bwd
RB3/W+1/S05+-fwd
RB3/W+1/S05+-bwd
RB3/W+1/S05--fwd
RB3/W+1/S05--bwd
RB3/W+1/S04+-fwd
RB3/W+1/S04+-bwd
RB3/W+1/S04--fwd
RB3/W+1/S04--bwd
RB3/W+1/S03+-fwd
RB3/W+1/S03+-bwd
RB3/W+1/S03--fwd
RB3/W+1/S03--bwd
RB3/W+1/S02+-fwd
RB3/W+1/S02+-bwd
RB3/W+1/S02--fwd
RB3/W+1/S02--bwd
RB3/W+1/S01+-fwd
RB3/W+1/S01+-bwd
RB3/W+1/S01--fwd
RB3/W+1/S01--bwd

mismatched elsewhere

unmatched



Digis

1

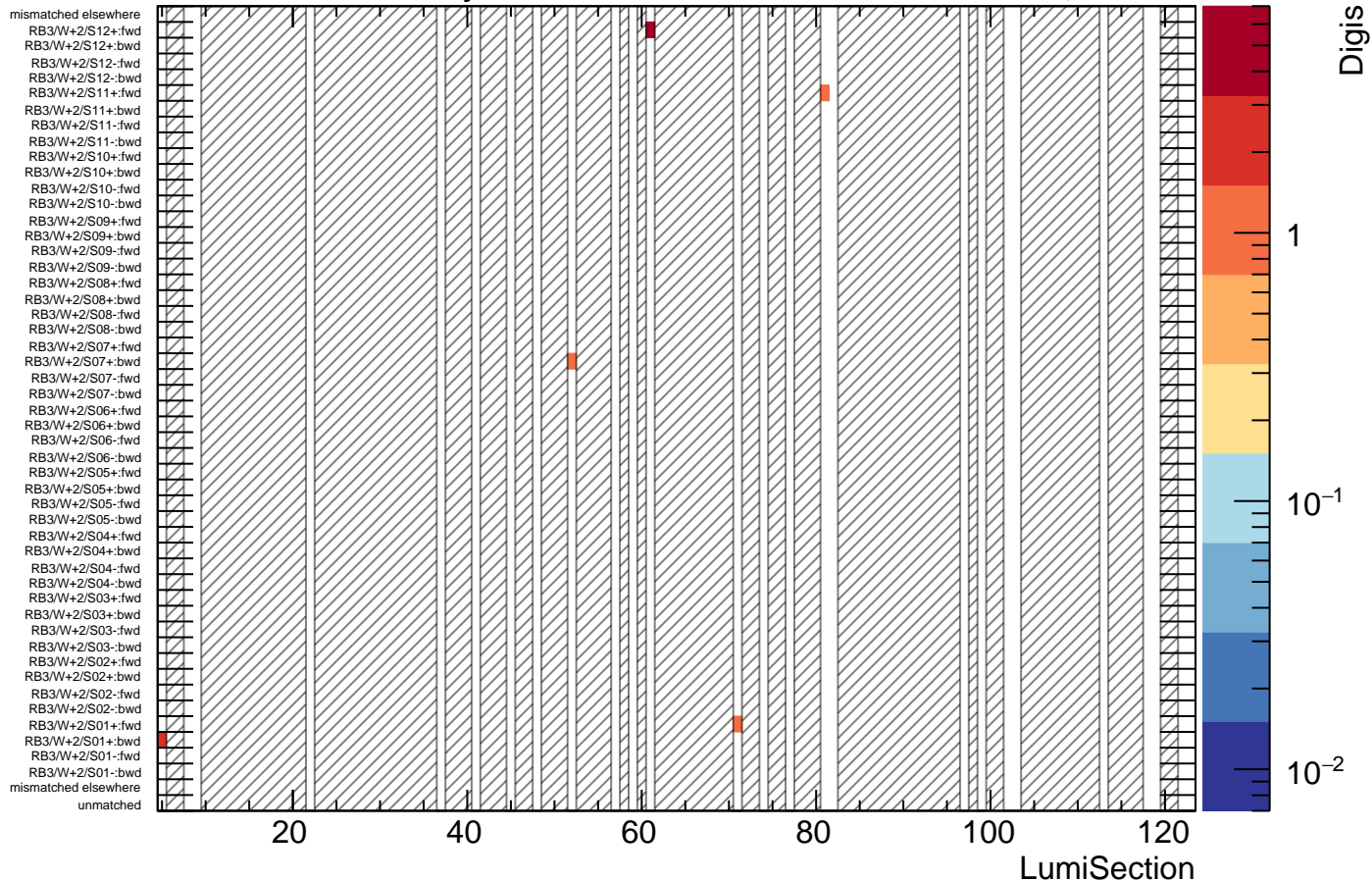
10^{-1}

10^{-2}

LumiSection

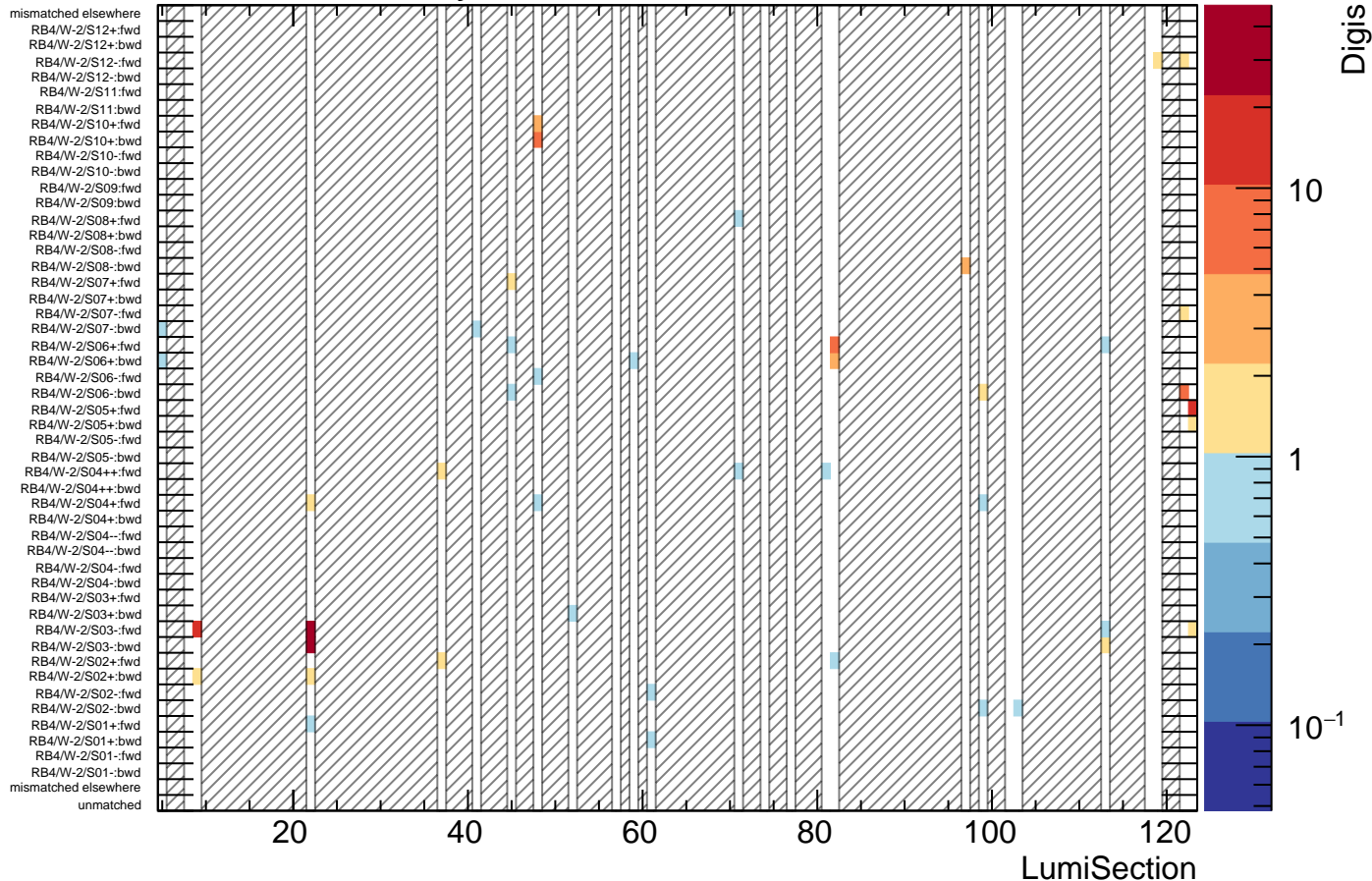
RB3/W+2: TwinMux only

CMS Preliminary - run 306122



RB4/W-2: TwinMux only

CMS Preliminary - run 306122



RB4/W-1: TwinMux only

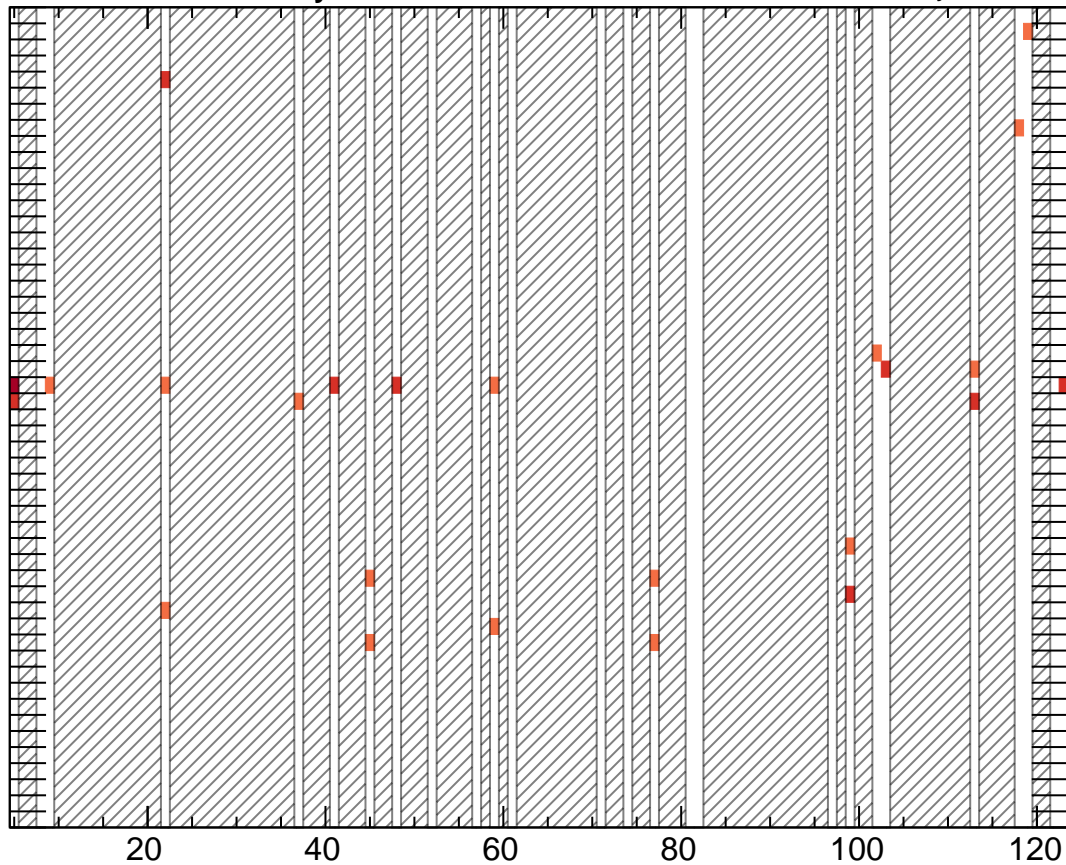
CMS Preliminary - run 306122

mismatched elsewhere

RB4/W-1/S12+..fwd
RB4/W-1/S12+..bwd
RB4/W-1/S12-..fwd
RB4/W-1/S12-..bwd
RB4/W-1/S11.fwd
RB4/W-1/S11.bwd
RB4/W-1/S10+..fwd
RB4/W-1/S10+..bwd
RB4/W-1/S10-..fwd
RB4/W-1/S10-..bwd
RB4/W-1/S09.fwd
RB4/W-1/S09.bwd
RB4/W-1/S08+..fwd
RB4/W-1/S08+..bwd
RB4/W-1/S08-..fwd
RB4/W-1/S08-..bwd
RB4/W-1/S07+..fwd
RB4/W-1/S07+..bwd
RB4/W-1/S07-..fwd
RB4/W-1/S07-..bwd
RB4/W-1/S06+..fwd
RB4/W-1/S06+..bwd
RB4/W-1/S06-..fwd
RB4/W-1/S06-..bwd
RB4/W-1/S05+..fwd
RB4/W-1/S05+..bwd
RB4/W-1/S05-..fwd
RB4/W-1/S05-..bwd
RB4/W-1/S04+..fwd
RB4/W-1/S04+..bwd
RB4/W-1/S04+..fwd
RB4/W-1/S04+..bwd
RB4/W-1/S04-..fwd
RB4/W-1/S04-..bwd
RB4/W-1/S04-..fwd
RB4/W-1/S04-..bwd
RB4/W-1/S03+..fwd
RB4/W-1/S03+..bwd
RB4/W-1/S03-..fwd
RB4/W-1/S03-..bwd
RB4/W-1/S02+..fwd
RB4/W-1/S02+..bwd
RB4/W-1/S02-..fwd
RB4/W-1/S02-..bwd
RB4/W-1/S01+..fwd
RB4/W-1/S01+..bwd
RB4/W-1/S01-..fwd
RB4/W-1/S01-..bwd

mismatched elsewhere

unmatched



Digis

1

10⁻¹

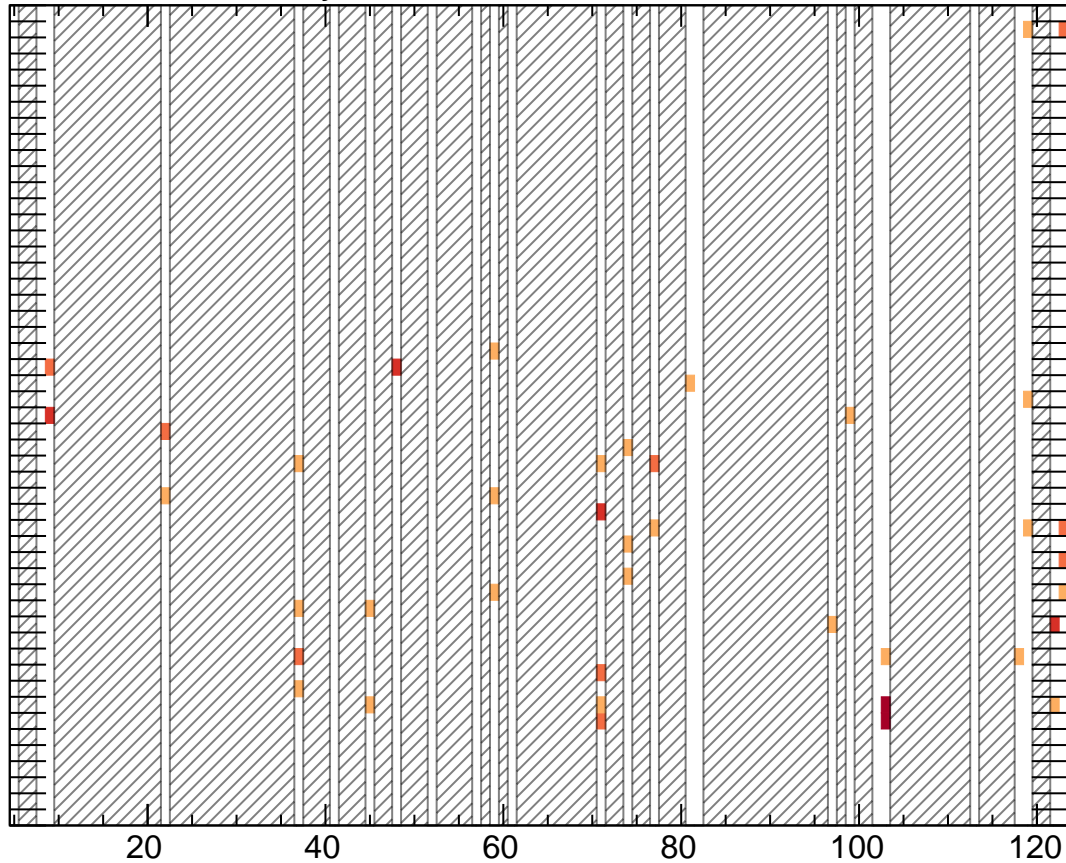
10⁻²

LumiSection

RB4/W+0: TwinMux only

CMS Preliminary - run 306122

mismatched elsewhere
 RB4/W+0/S12+~fwd
 RB4/W+0/S12+~bwd
 RB4/W+0/S12-~fwd
 RB4/W+0/S12-~bwd
 RB4/W+0/S11~fwd
 RB4/W+0/S11~bwd
 RB4/W+0/S10+~fwd
 RB4/W+0/S10+~bwd
 RB4/W+0/S10-~fwd
 RB4/W+0/S10-~bwd
 RB4/W+0/S09~fwd
 RB4/W+0/S09~bwd
 RB4/W+0/S08+~fwd
 RB4/W+0/S08+~bwd
 RB4/W+0/S08-~fwd
 RB4/W+0/S08-~bwd
 RB4/W+0/S07+~fwd
 RB4/W+0/S07+~bwd
 RB4/W+0/S07-~fwd
 RB4/W+0/S07-~bwd
 RB4/W+0/S06+~fwd
 RB4/W+0/S06+~bwd
 RB4/W+0/S06-~fwd
 RB4/W+0/S06-~bwd
 RB4/W+0/S05+~fwd
 RB4/W+0/S05+~bwd
 RB4/W+0/S05-~fwd
 RB4/W+0/S05-~bwd
 RB4/W+0/S04++~fwd
 RB4/W+0/S04++~bwd
 RB4/W+0/S04+~fwd
 RB4/W+0/S04+~bwd
 RB4/W+0/S04-~fwd
 RB4/W+0/S04-~bwd
 RB4/W+0/S04-~fwd
 RB4/W+0/S04-~bwd
 RB4/W+0/S03+~bwd
 RB4/W+0/S03-~fwd
 RB4/W+0/S03-~bwd
 RB4/W+0/S02+~fwd
 RB4/W+0/S02+~bwd
 RB4/W+0/S02-~fwd
 RB4/W+0/S02-~bwd
 RB4/W+0/S01+~fwd
 RB4/W+0/S01+~bwd
 RB4/W+0/S01-~fwd
 RB4/W+0/S01-~bwd
 mismatched elsewhere
 unmatched



10 Digis

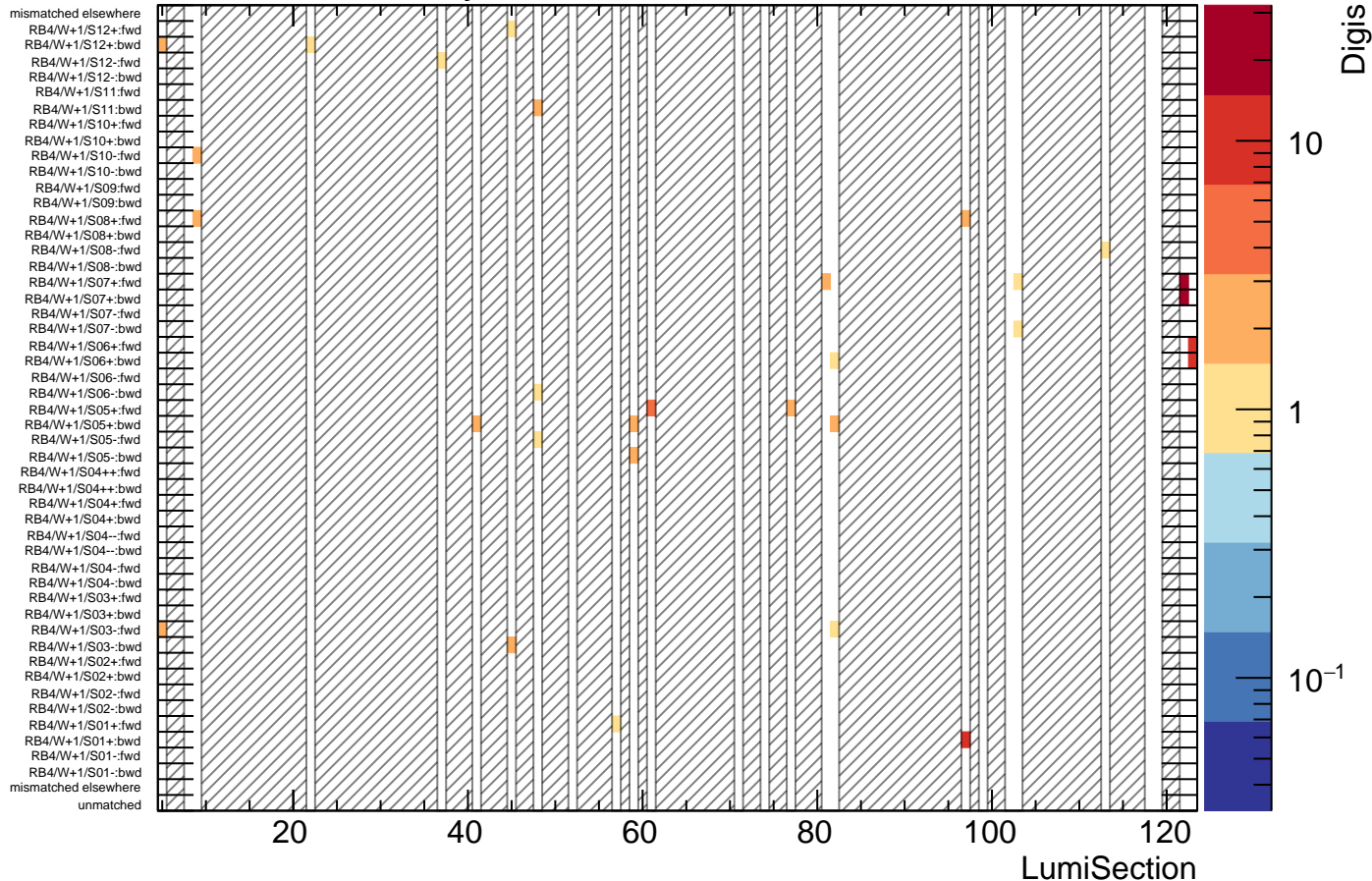
1

10⁻¹

LumiSection

RB4/W+1: TwinMux only

CMS Preliminary - run 306122



RB4/W+2: TwinMux only

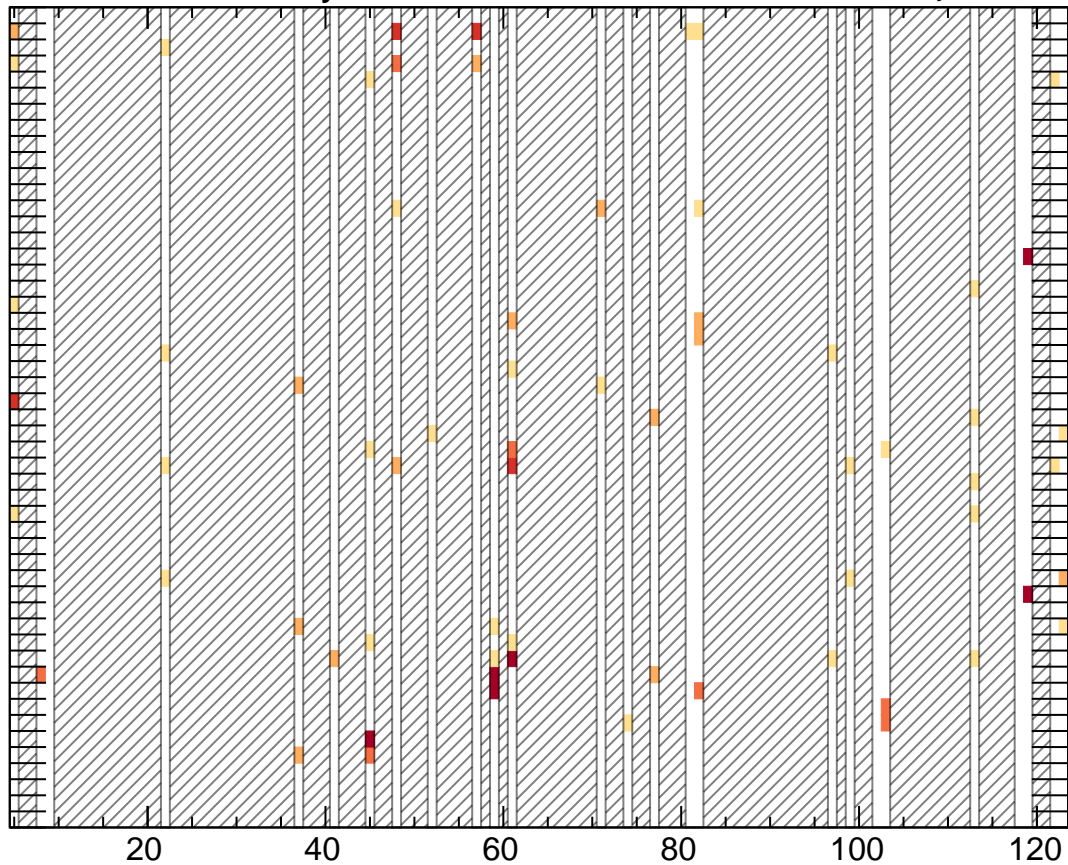
CMS Preliminary - run 306122

mismatched elsewhere

RB4/W+2/S12+ fwd
RB4/W+2/S12+ bwd
RB4/W+2/S12- fwd
RB4/W+2/S12- bwd
RB4/W+2/S11 fwd
RB4/W+2/S11 bwd
RB4/W+2/S10+ fwd
RB4/W+2/S10+ bwd
RB4/W+2/S10- fwd
RB4/W+2/S10- bwd
RB4/W+2/S09 fwd
RB4/W+2/S09 bwd
RB4/W+2/S08+ fwd
RB4/W+2/S08+ bwd
RB4/W+2/S08- fwd
RB4/W+2/S08- bwd
RB4/W+2/S07+ fwd
RB4/W+2/S07+ bwd
RB4/W+2/S07- fwd
RB4/W+2/S07- bwd
RB4/W+2/S06+ fwd
RB4/W+2/S06+ bwd
RB4/W+2/S06- fwd
RB4/W+2/S06- bwd
RB4/W+2/S05+ fwd
RB4/W+2/S05+ bwd
RB4/W+2/S05- fwd
RB4/W+2/S05- bwd
RB4/W+2/S04++ fwd
RB4/W+2/S04++ bwd
RB4/W+2/S04+ fwd
RB4/W+2/S04+ bwd
RB4/W+2/S04- fwd
RB4/W+2/S04- bwd
RB4/W+2/S04-- fwd
RB4/W+2/S04-- bwd
RB4/W+2/S03+ fwd
RB4/W+2/S03+ bwd
RB4/W+2/S03- fwd
RB4/W+2/S03- bwd
RB4/W+2/S02+ fwd
RB4/W+2/S02+ bwd
RB4/W+2/S02- fwd
RB4/W+2/S02- bwd
RB4/W+2/S01+ fwd
RB4/W+2/S01+ bwd
RB4/W+2/S01- fwd
RB4/W+2/S01- bwd

mismatched elsewhere

unmatched



Digis

10

1

10⁻¹

LumiSection