



Politecnico di Torino

III Facoltà di Ingegneria

Design and Implementation of a Digital Filter Integrated Systems Architecture

Master's degree in Electronic Engineering

Authors: group 24

Campanella Andrea, Iacovelli Gianluca, Pala Stefano

Contents

1	Reference model development	1
2	VLSI implementation	2

CHAPTER 1

Reference model development

CHAPTER 2

VLSI implementation