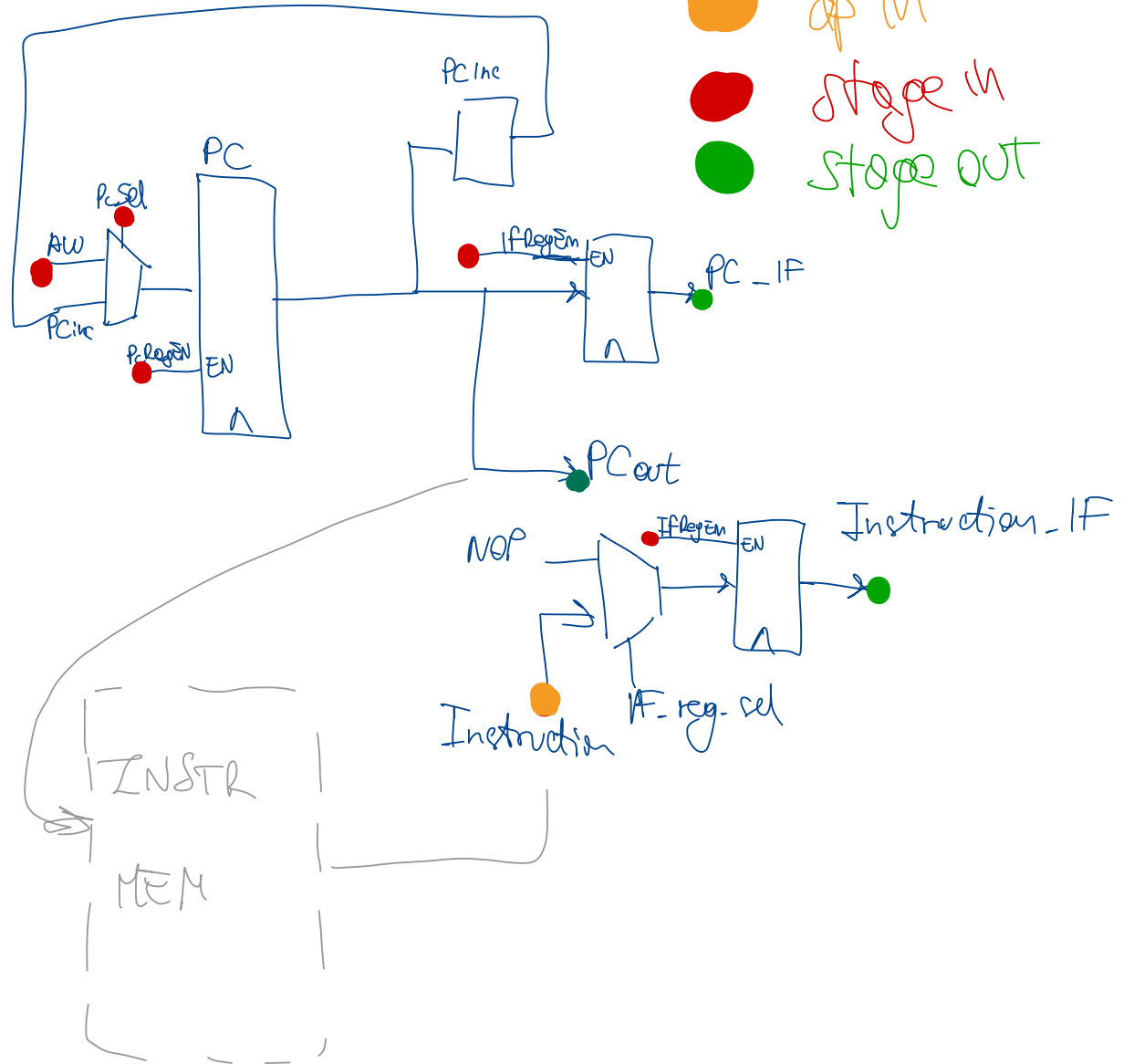
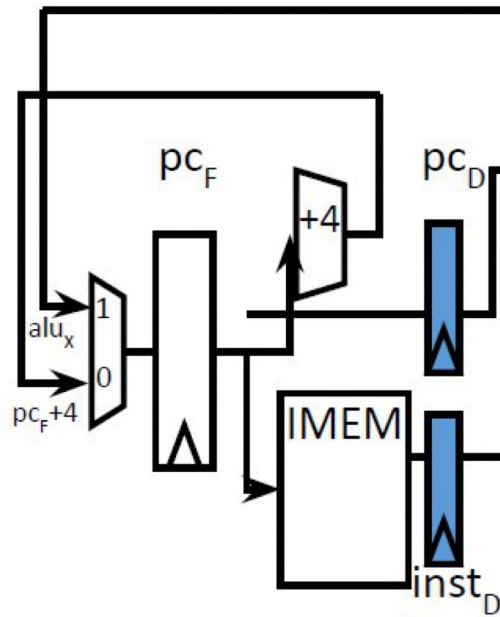


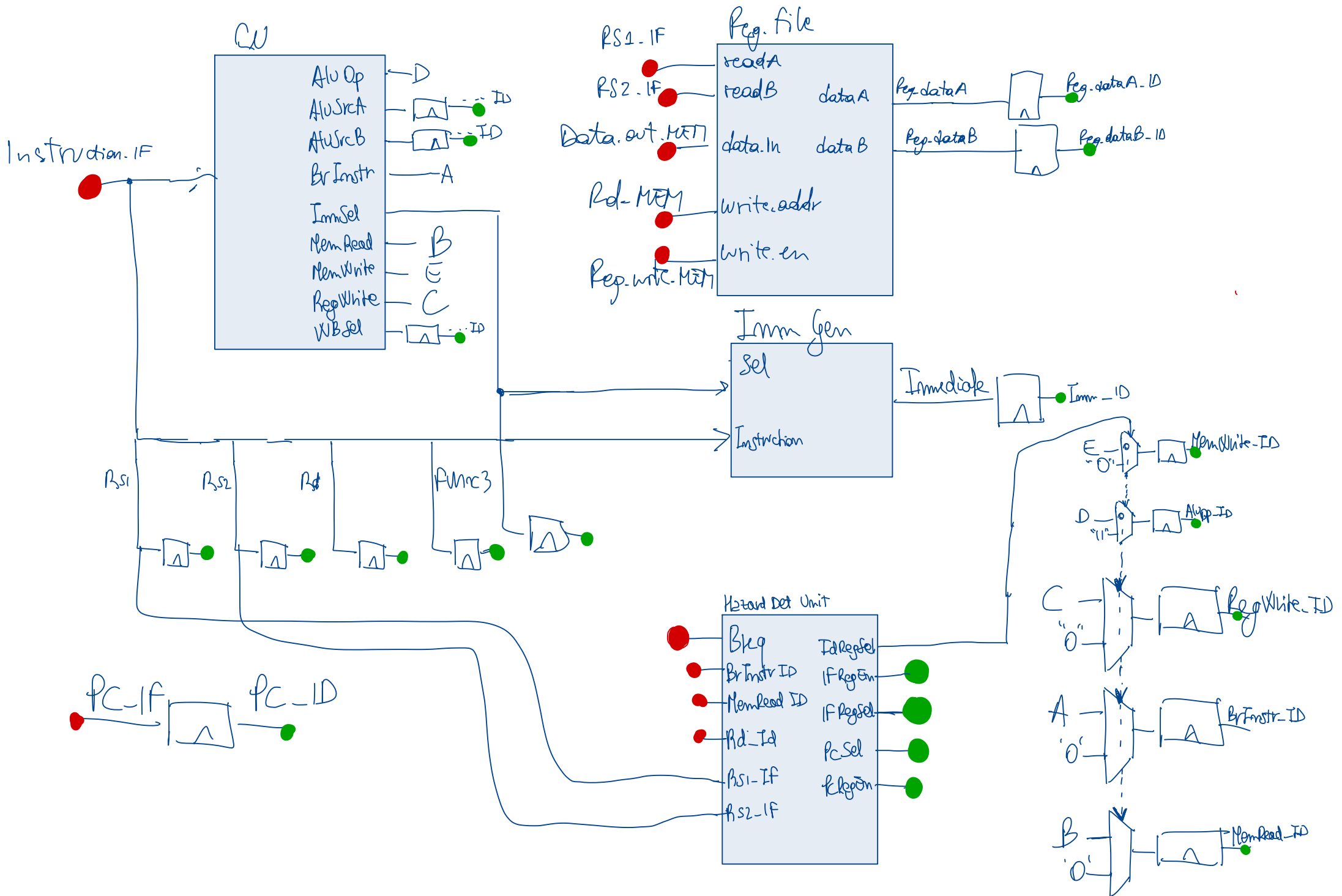
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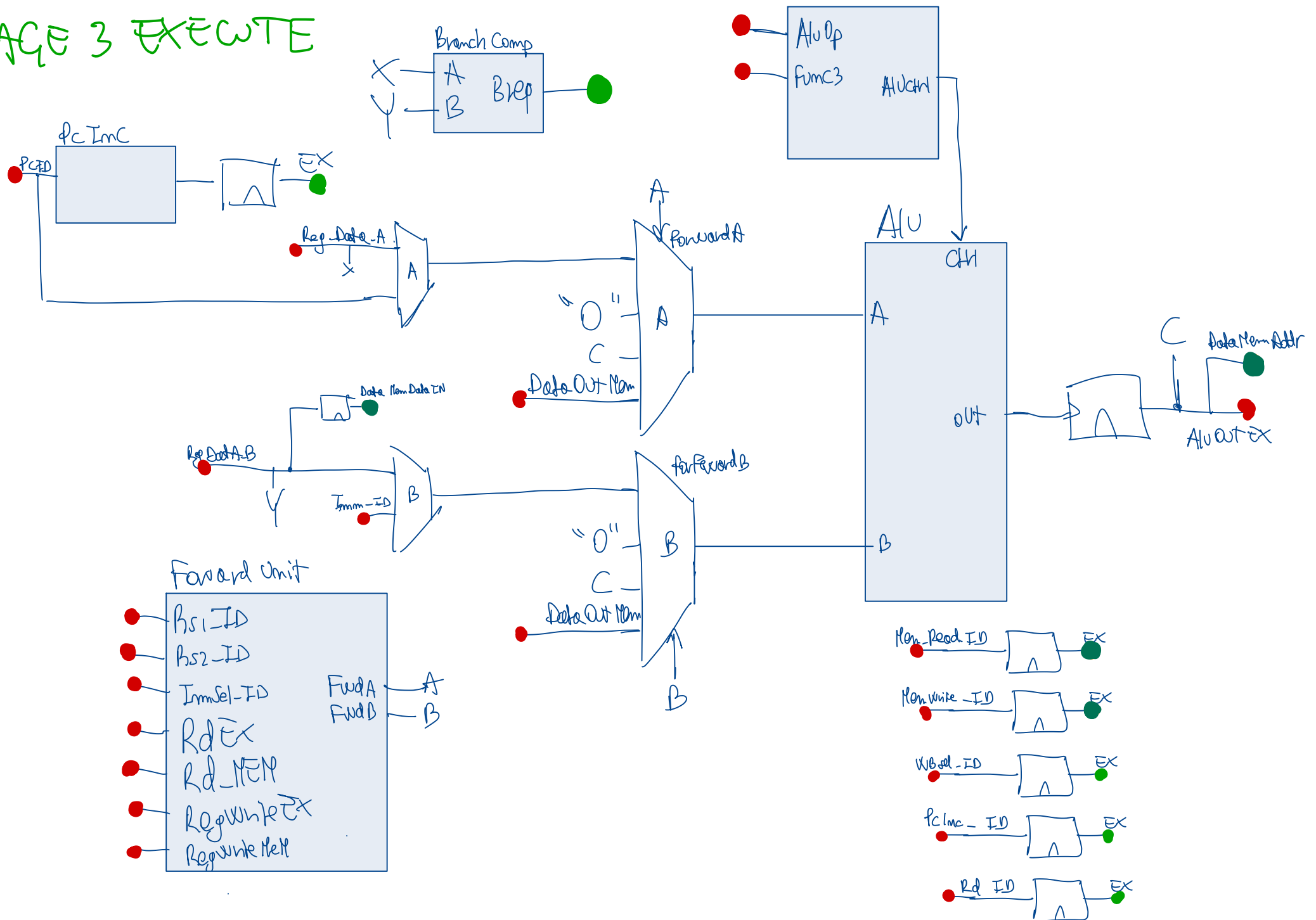
STA 6E 1 fetch



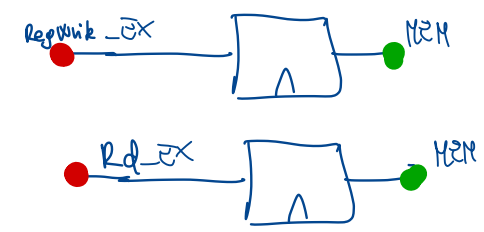
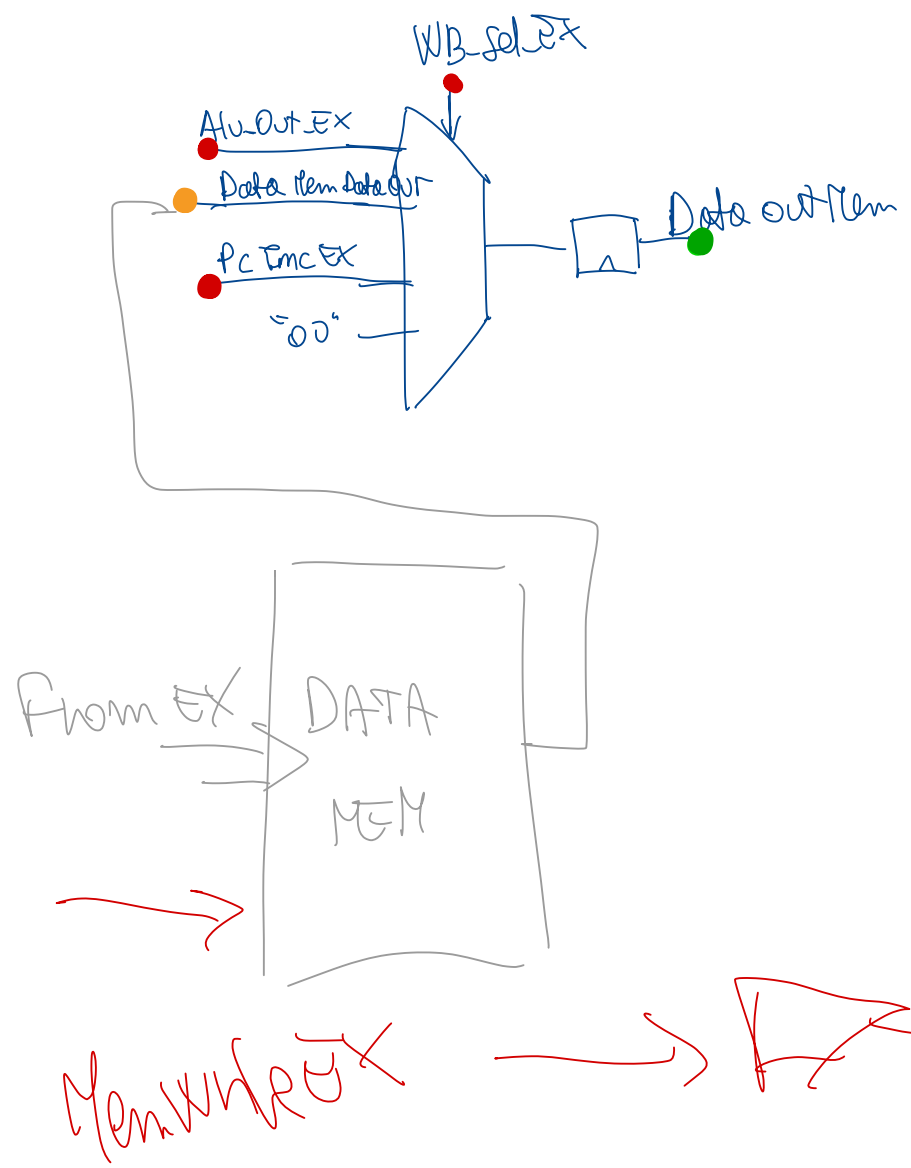
STAGE 2 decode



STAGE 3 EXECUTE



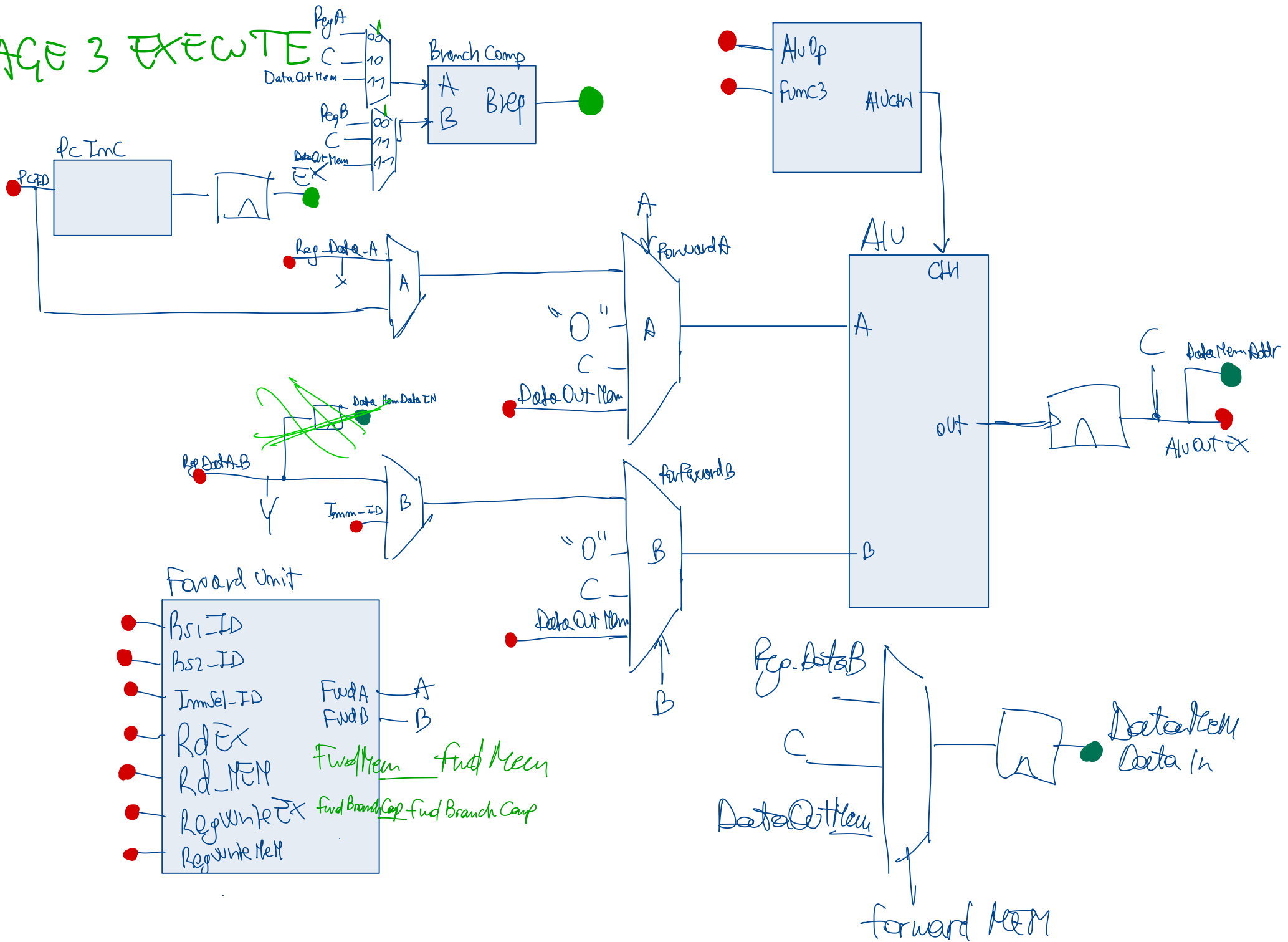
STAGE 4 MEMORY



KB

↓
MEM

STAGE 3 EXECUTE



ADD R1, R2, R3
 SW R4, R1, 5

Mem(RegFile(R4)+5) \Leftarrow RegFile(R1)

ADD R1, R2, R3
 ADD - - -
 SW R4, R1, 5
RS

ADD R1, R2, R3

ADD R4, R1, R5

Sw RS2, imm(RS1)
 forward mem forward A