Start Frame 32 ns

FADC

Drop last 4 bits

17 bits Σ → 13 bits word + 3 bits time in frame → 16 bit per channel

17 bits Σ → 13 bits word + 3 bits time in frame → 16 bit per channel

17 bits Σ → 13 bits word + 3 bits time in frame → 16 bit per channel

17 bits Σ → 13 bits word + 3 bits time in frame → 16 bit per channel

do for all 16 FADC channels

send 2 channels to CTP in one 32 bit word every 4 ns @ 8 Gbps