2) Optical link 10 Gbps
1250 MB/s
ADC neigboring channels sent to next CTP

1) FADC ADC channels to CTP
VXS 2 x 4 Gbps
1000 MB/s
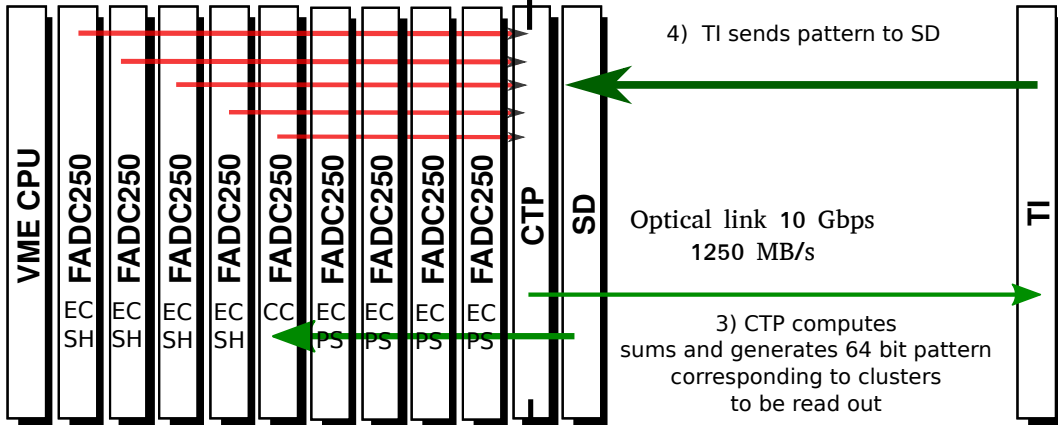
4) TI sends pattern to SD

Optical link 10 Gbps
1250 MB/s

3) CTP computes
sums and generates 64 bit pattern
corresponding to clusters
to be read out

5) Each FADC receives
a 16 bit trigger word
and puts the relevant
channels in the
event buffer

2) Optical link 10 Gbps
1250 MB/s
ADC neigboring channels sent to next CTP

VME CPU

FADC250 (EC SH) ×4, FADC250 (CC), FADC250 (EC PS) ×4

CTP

SD

TI