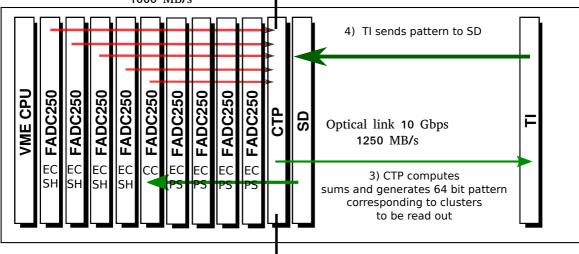


1) FADC ADC channels to CTP VXS 2 x 4 Gbps 1000 MB/s



5) Each FADC receives a 16 bit trigger word and puts the relevant

channels in the event buffer

2) Optical link 10 Gbps 1250 MB/s ADC neighboring channels sent to next CTP