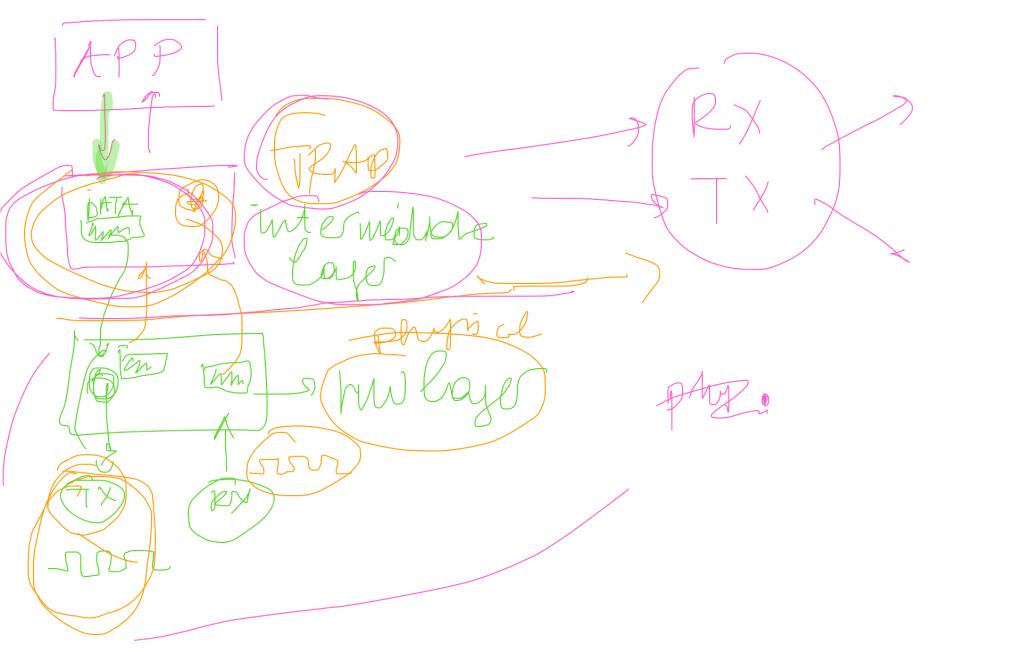
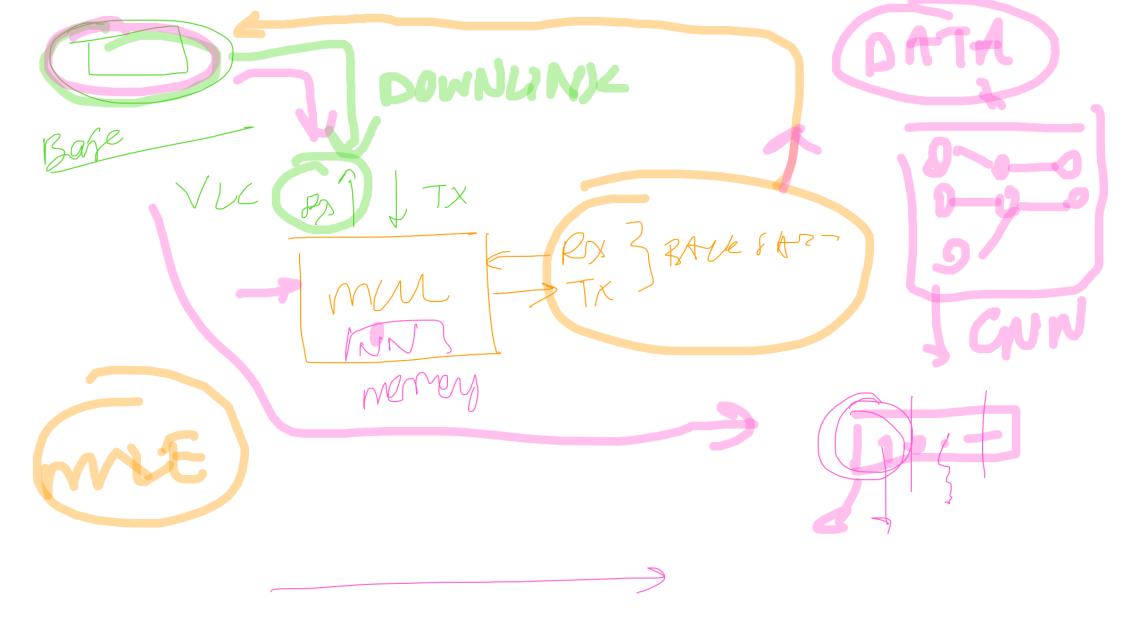
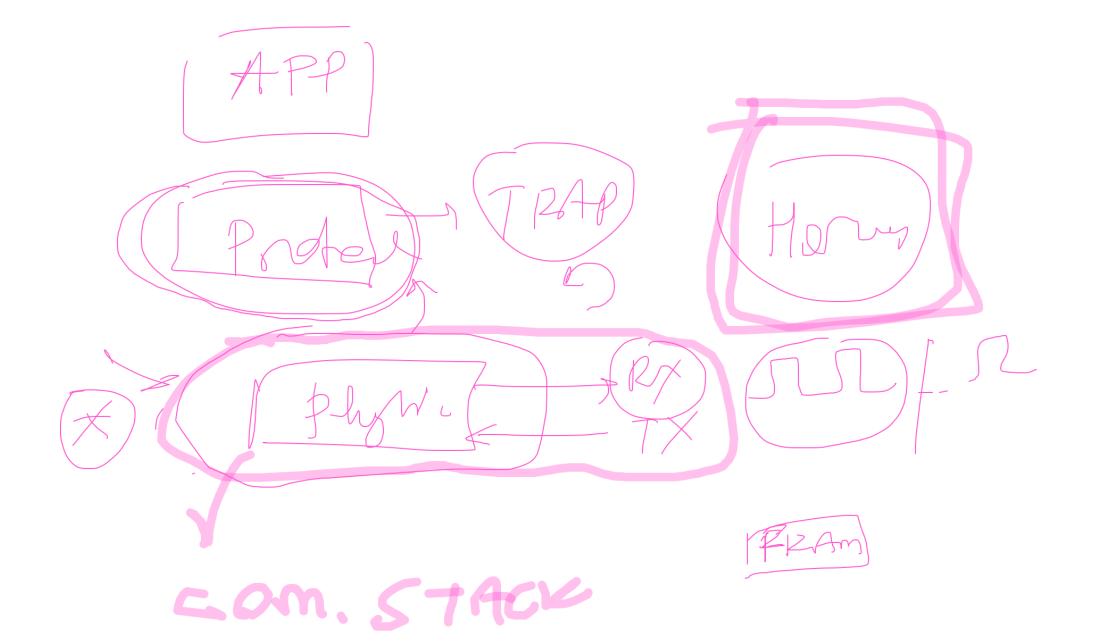
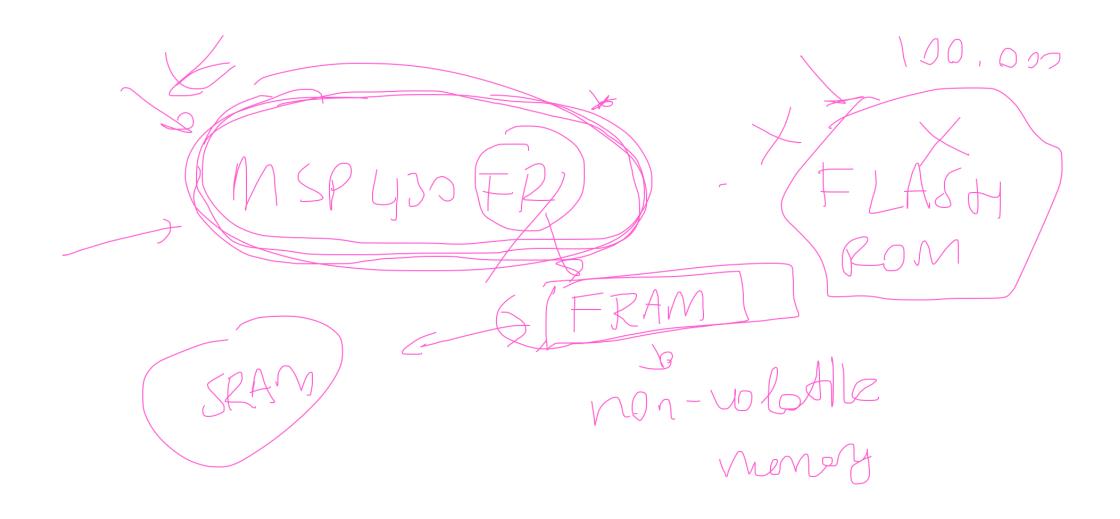


10 TTA But









Phys layor bould c. C -> RX, TX -> GPIO VIC. C -) RX, TX => JM10