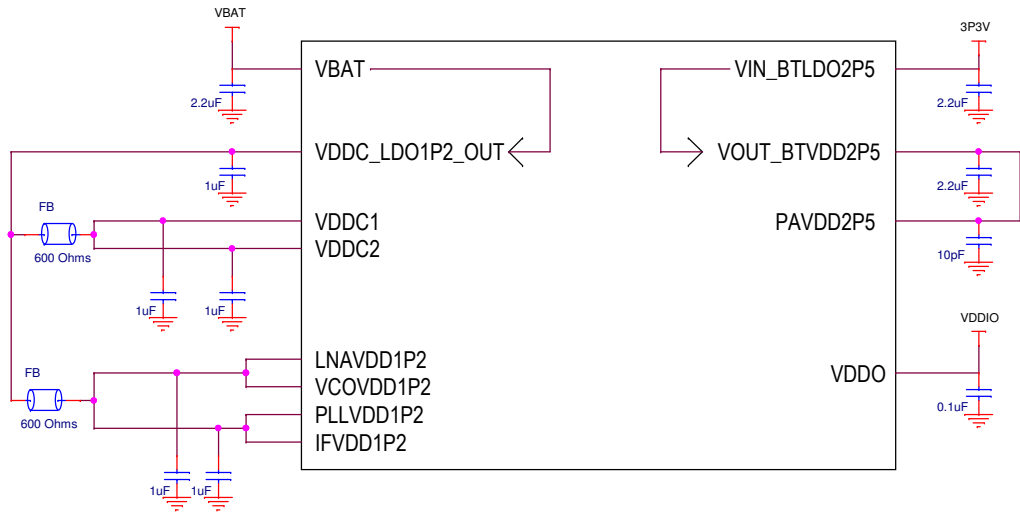


Revision History:

Rev 00 (08/12/16)

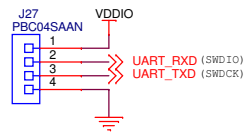
- 1) Initial release



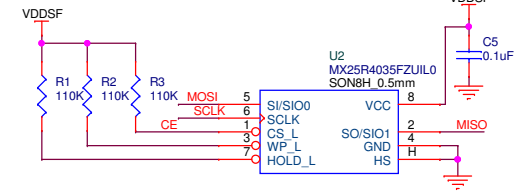
PAGE1: COVER PAGE
PAGE2: BASEBAND & MEMORY
PAGE3: RADIO
PAGE4: POWER & IO
PAGE5: UART

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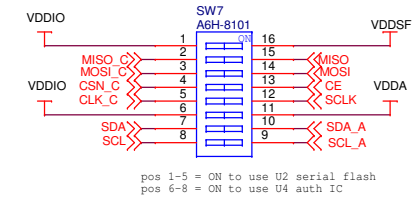
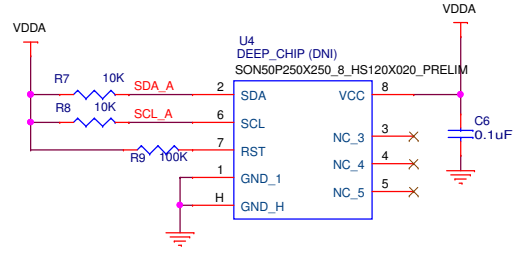
DEBUG INTERFACE



SERIAL FLASH

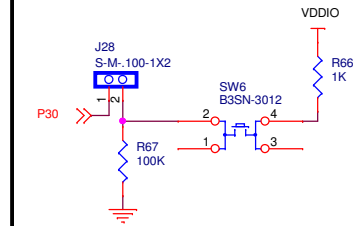


AUTH IC

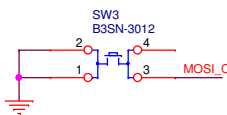


pos 1-5 = ON to use U2 serial flash
pos 6-8 = ON to use U4 auth IC

GENERIC BUTTON

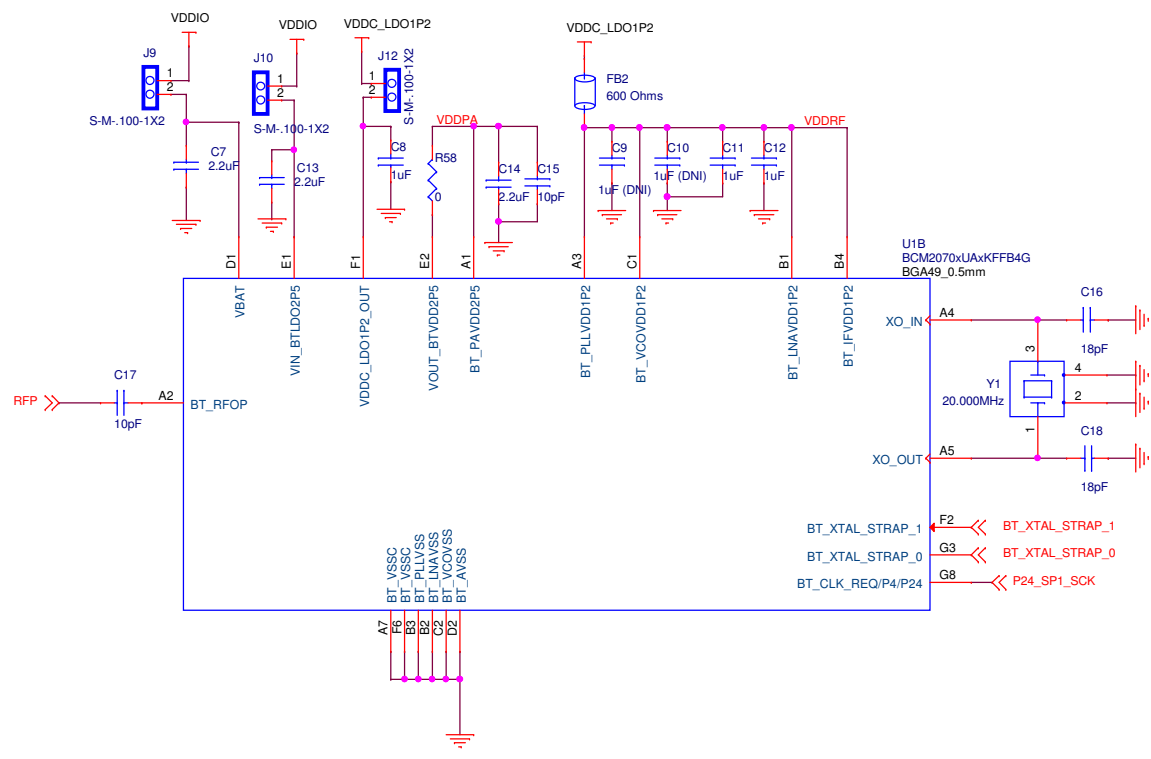


RECOVERY BUTTON

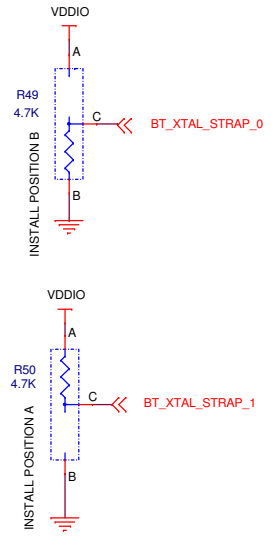
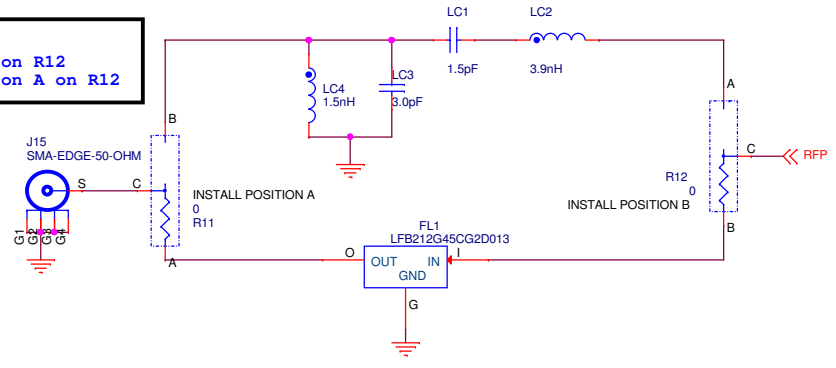


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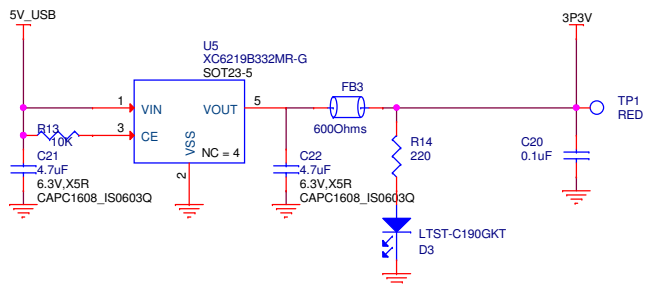
Band Pass Filter Options:
 Chip BPF: Install Position A on R11 and Install Position B on R12
 4 Element BPF: Install Position B on R11 and Install Position A on R12



XTAL_STRAP[1:0]	STRAPPING OPTIONS
00	40MHz
01	24MHz
10	20MHz
11	Read from serial flash or EEPROM

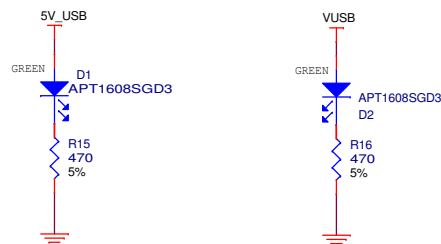
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3.3V 240mA LDO regulator



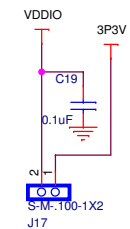
D3: Green, Indicating that 3P3V is on

LED INDICATORS

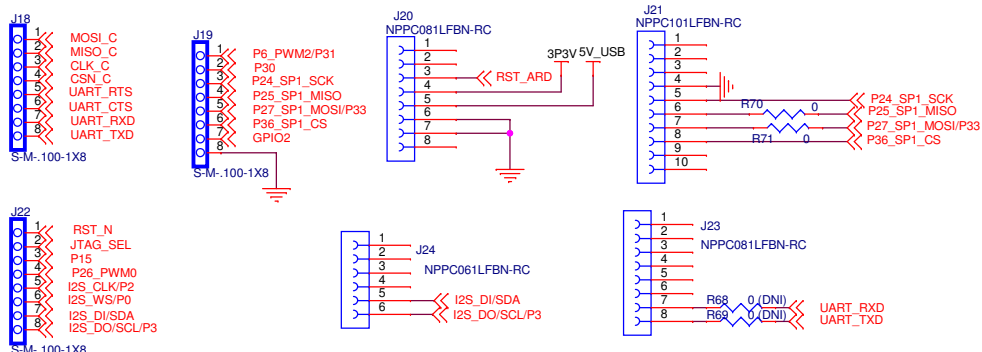


D1: Green, Indicating that Power switch SW4 is switched on.
D2: Green, Indicating that USB cable is plugged in.

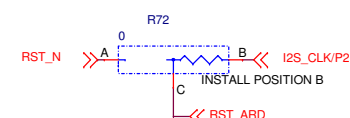
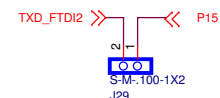
3.3V Jumper



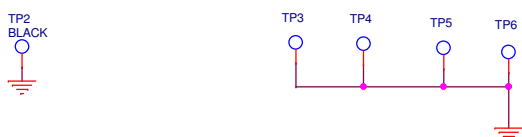
TEST HEADERS



Debug GPIO



GND Test Points



ZJ1 SNT-100-BK-G	ZJ2 SNT-100-BK-G
ZJ3 SNT-100-BK-G	ZJ4 SNT-100-BK-G
ZJ5 SNT-100-BK-G	ZJ6 SNT-100-BK-G
ZJ7 SNT-100-BK-G	ZJ8 SNT-100-BK-G
ZJ9 SNT-100-BK-G	ZJ10 SNT-100-BK-G

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