

Revision History:

Rev 01 (08/01/16)

1) Initial Release

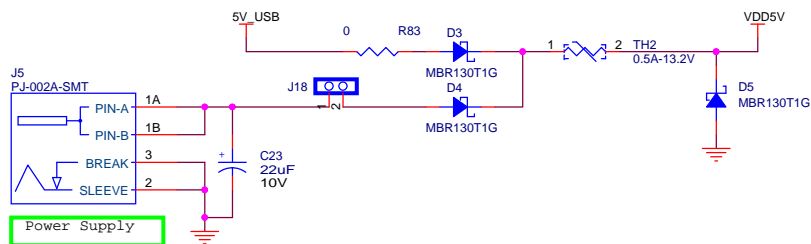
Rev 02 (10/13/16)

1) Updated C19 and C20 to 10pF.

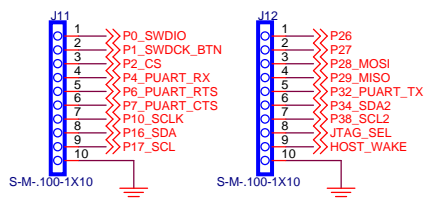
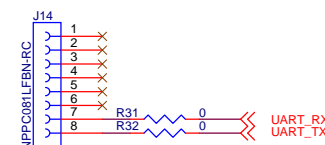
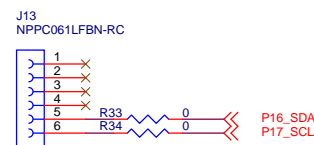
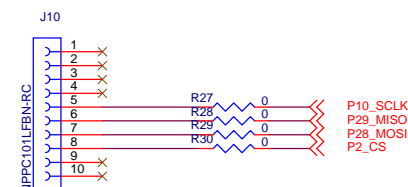
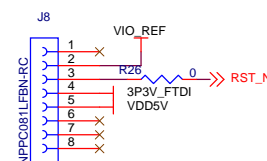
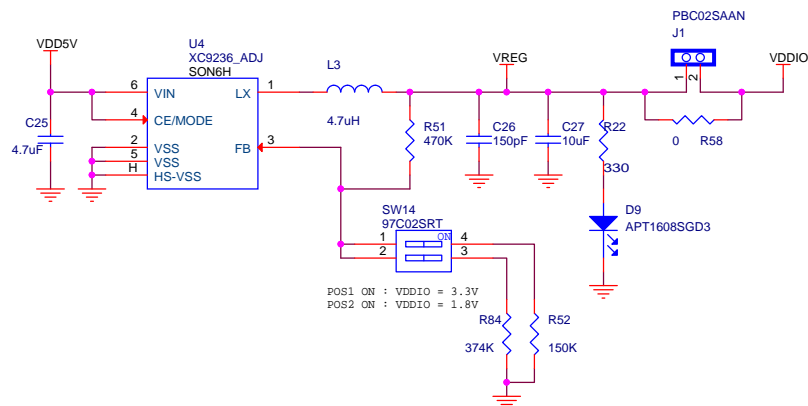
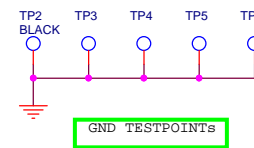
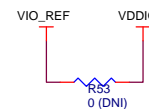
PAGE1: COVER PAGE
PAGE2: BASEBAND
PAGE3: RF
PAGE4: POWER & TEST HEADERS
PAGE5: FTDI INTERFACE

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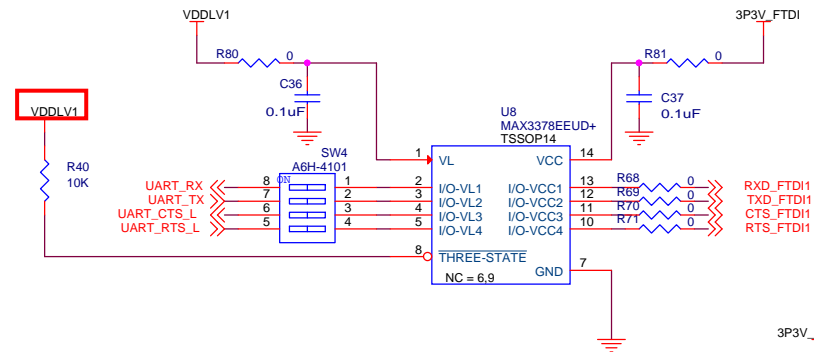


NOTE: Power On/Off switch

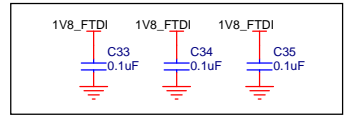
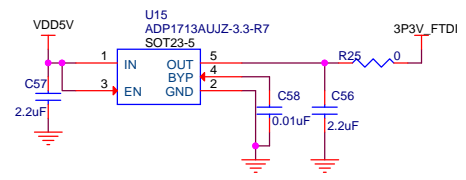
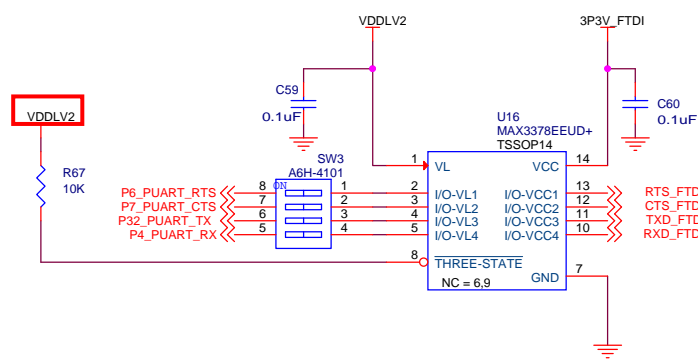
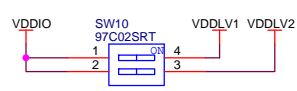


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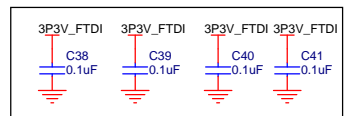
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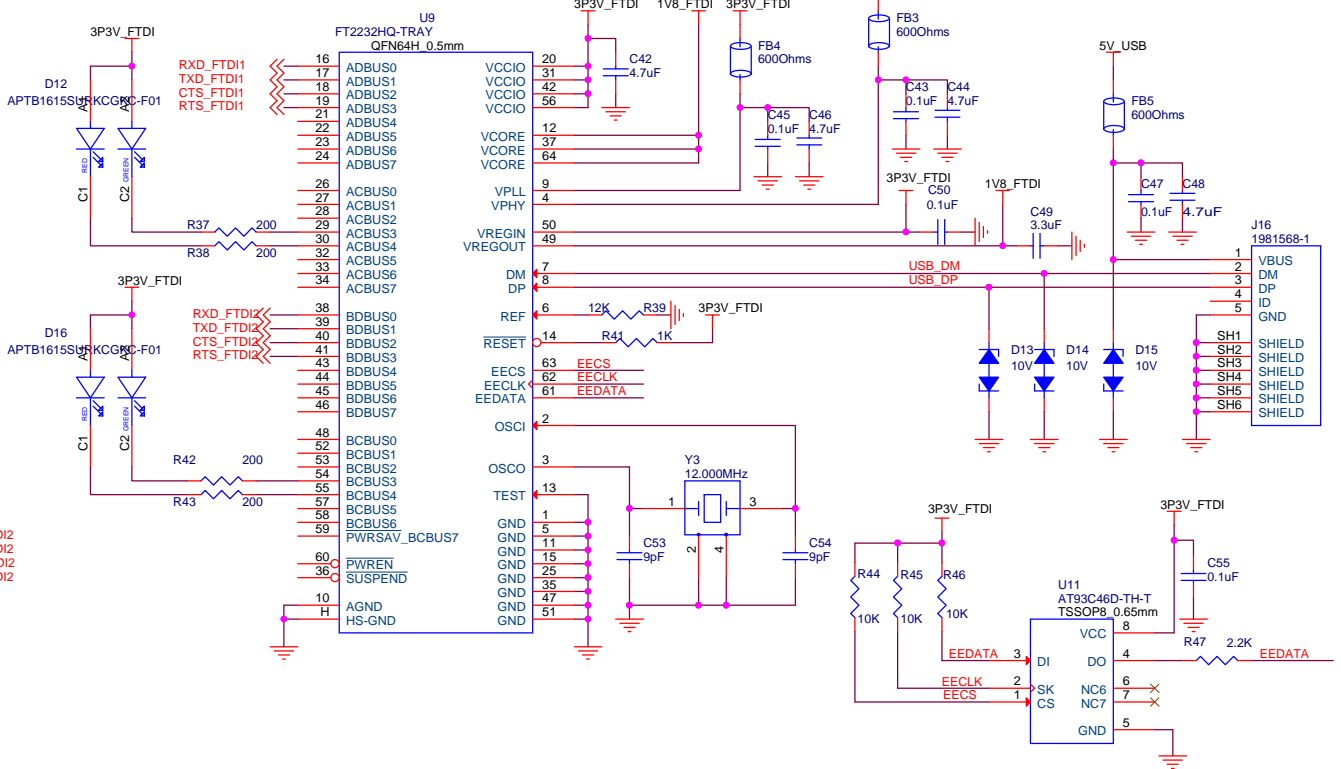
Level translator



Place close to VCORE pins



Place close to VCCIO pins



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