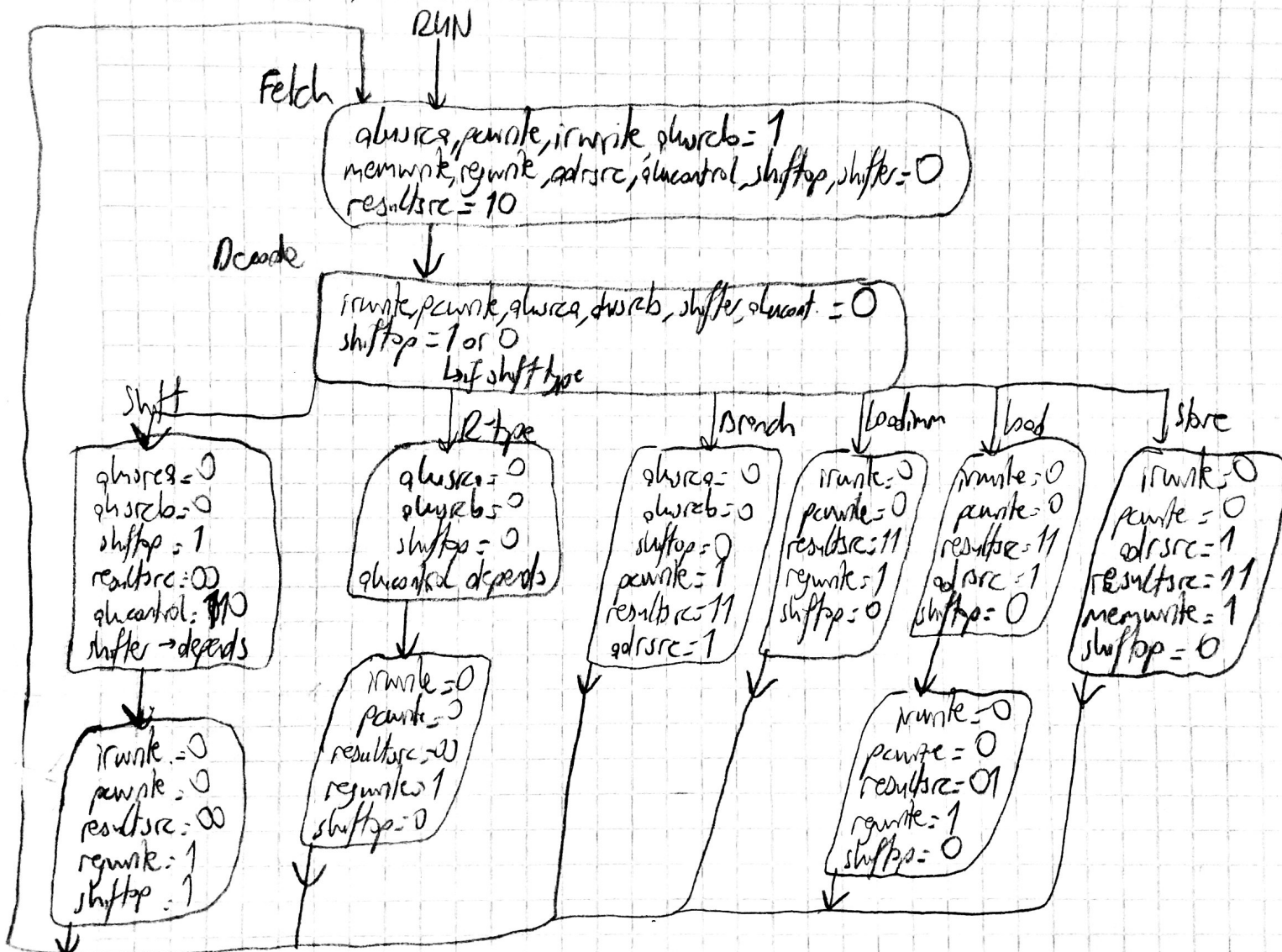


1.2.1

1-) Control signals: $pcwrite, adrsrc, memwrite, irwrite, rgwrite, shifter, shiftop$
 $qlusrc, qlusrcb, qlucntrol, resultsrc$

2-) Asm Chart:



Max 4 states (Shift & R-type & Load)