

Lab 7: Memory and Video

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November 14, 2022

1 Part I

1. Export the subcircuit schematic as an image and include it in your report.

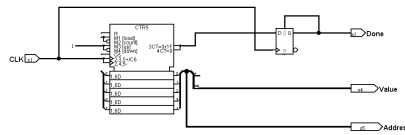


Figure 1: A schematic of MemoryWriter.

- Export the timing diagram as an image and include it in your report.

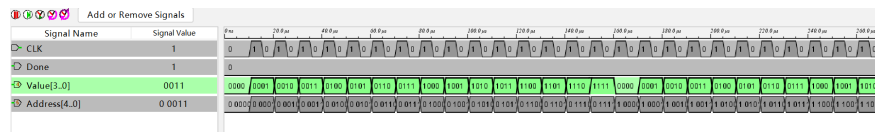


Figure 2: A timing simulation of the MemoryWriter.

2 Part II

1. Export the subcircuit schematic as an image and include it in your report.

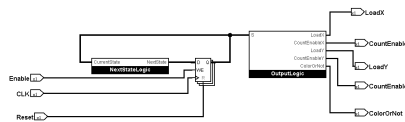


Figure 3: A schematic of `CtrlUnit`.

2. Export the subcircuit schematic as an image and include it in your report.

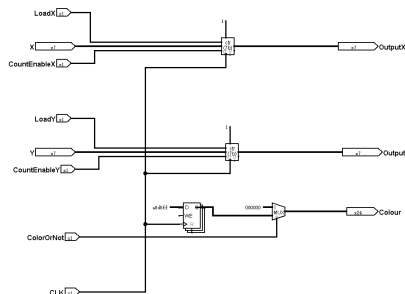


Figure 4: A schematic of DataPath.

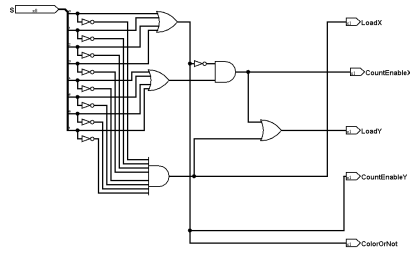


Figure 5: A schematic of OutputLogic.

3. Export the subcircuit schematic as an image and include it in your report.
4. Export the timing simulations as an image and include it in your report.

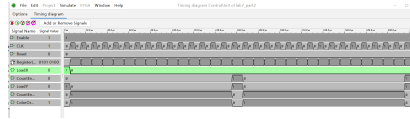


Figure 6: A schematic of timing_{simulations}*ControlUnit*.



Figure 7: A schematic of timing_{simulations}*Main*.

5. Export the Test vectors as an image and include it in your report.

Passed: 8 Failed: 0								
Status	Enable	CLK	Reset	LoadX	CountEnableX	LoadY	CountEnableY	ColorOrNot
pass	0	0	0	1	0	1	0	0
pass	0	0	1	1	0	1	0	0
pass	0	1	0	1	0	1	0	0
pass	0	1	1	1	0	1	0	0
pass	1	0	0	1	0	1	0	0
pass	1	0	1	1	0	1	0	0
pass	1	1	0	1	0	1	0	0
pass	1	1	1	1	0	1	0	0

Figure 8: A schematic of Test_{vectors}*ofControlUnit*.