Assignment 4

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Design code, testbench and simulation the following module

Problem 1 : 4-bit Ripple Carry Counter

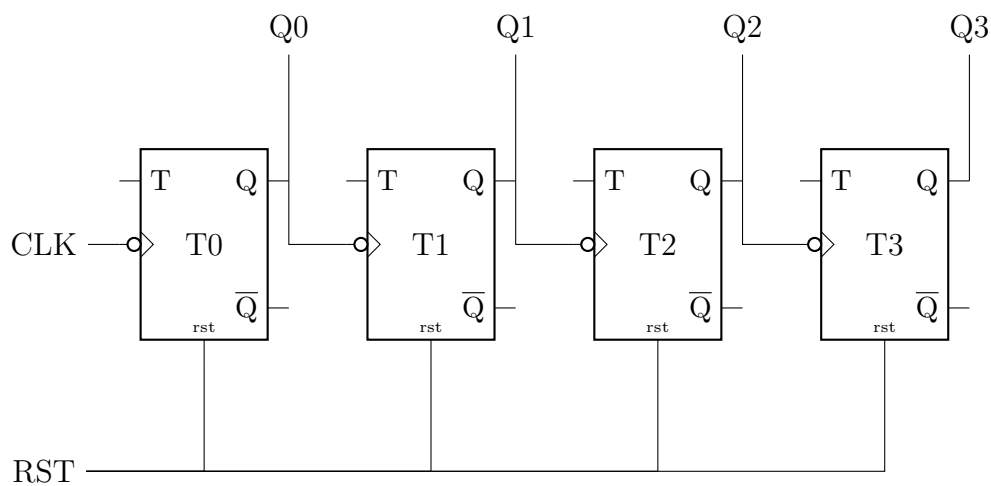*NOTE : Using T - flipflop (Structural model) that you designed in Assignment 3.*

Figure 1: 4-bit Ripple Carry Counter

Problem 2 : 4-bit Gray Counter

NOTE : The counter has synchrounous reset

Problem 3 : BCD to 7 Segment Display Decoder