

Assignment 7

2023

Problem 1 :

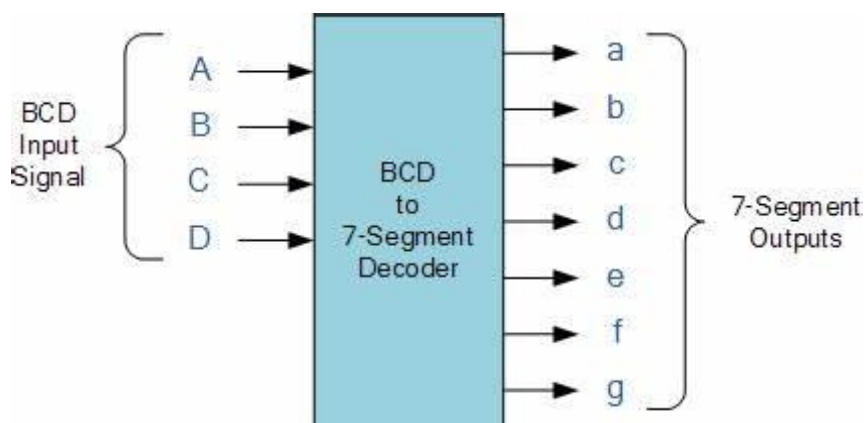
Use Behavioral model to design circuits. Simulate your designs on ModelSim. Create code coverage report, and waveform. Screenshot your results

1. Encoder 8-to-3



2. Majority 4 bit circuit

3. BCD to 7-segment Decoder



4. Flip Flops: D-FF, T-FF, SR-FF, JK-FF

1. Use Synchronous reset.
2. Use Asynchronous reset.

How does these synthesis (Lecture 4)

Problem 2 : Answer the questions

Describe the initial statement and always statement in Behavioral Verilog.