

# Ngo Van Canh

FPT SOFTWARE · C++ DEVELOPER

Duc Noi, Viet Hung, Dong Anh, Ha Noi, Viet Nam

☎ (+84) 383090063 | ✉ nv.canh@outlook.com | 🏠 August 17th, 2001 | 📷 canh25xp

“Choose a job you love, and you will never have to work a day in your life.”

## Overview

Electronics and Telecommunication Engineering student with 5 years of programming experience in both software and hardware development projects. Proficient in C, C++ and Python, with expertise in industry-standard tools like CMake, Git, and Docker. Member of AICS Lab, contributing to several AI projects including “Document skew correction”, “English grammar correction” and “Optical Character Recognition”. Authored an IEEE paper on “A novel deep learning based method for Vietnamese ID card skew correction”.

## Education

### Hanoi University of Science and Technology (HUST)

Hanoi, Vietnam

B. S. IN ELECTRONICS AND TELECOMMUNICATION ENGINEERING (UNDERGRADUATE)

Mar. 2019 - Aug. 2024

School of Electrical and Electronic Engineering

## Skills

Software development	C, C++, Assembly, Python
Hardware development	Embedded c, Verilog
Languages	English, Vietnamese
Others	Docker, Latex

## Experience

### Samsung R&D Center Vietnam

Hanoi, Vietnam

C/C++ DEVELOPER INTERN

Jul. 2023 - Aug. 2023

- Trained about various algorithms in C++, ranging from basic to advanced.
- Collaboratively developed a mini console based game in C++.

## Extracurricular Activity

### AICS LAB (Artificial Intelligence Convolutional System Laboratory)

Hanoi, Vietnam

CORE MEMBER

Aug. 2023 - Present

- Gained knowledge about Machine Learning, Deep Learning, Computer Vision, AI for smartphone, AI for FPGA
- Participated on several AI related projects namely ID card skew correction, Document-Table skew correction, Optical Character Recognition
- Proposed a method for skew estimation and correction specifically for Vietnamese ID cards.

### Hanoi University of Science and Technology

Hanoi, Vietnam

TEACHING ASSISTANT (DIGITAL DESIGN USING VHDL)

Oct. 2023 - Jan. 2024

- Gained in-depth knowledge about Hardware Description Language (HDL), Field Programmable Gate Array (FPGA)
- Improve English communications skills

## Certificates

- 2023 Modern Project Management in ICT, Prof. Dr. Harald Wehnes
- 2023 Samsung Software Internship Program, Samsung R&D Center Vietnam (SRV)

## Publications

### CONFERENCE PROCEEDINGS

Vo Le Cuong, Nguyen Phuong Huy, Ngo Van Canh, Do Duy Thai, Nguyen Sy Duy, Vo Sy Hung. “A novel deep learning based method for Vietnamese ID card skew correction”. 2024.

## Home Security Alarm

ELECTRICAL AND ELECTRONIC ENGINEERING INTRODUCTION

Apr. 2020 - Jun. 2020

- Introduced to arduino
- Learn how managing a project, working as a team and making a presentation.

## Student Management System

C/C++ PROGRAMMING LANGUAGE

Apr. 2021 - May. 2021

- Build a simple Student Management System
- Learn about C and C++ programming : pointers, class, variable types, ...

## Bank Management System

DATA STRUCTURES AND ALGORITHMS

May. 2022 - Jun. 2022

- Build a simple Terminal-based Bank Management System
- Learn about Data Structures and Algorithms

## RISC-SPM

DIGITAL DESIGN USING VHDL

Jan. 2023 - Mar. 2023

- Design a RISC Stored-Program Machine Using Verilog HDL.
- Gained knowledge about CPU, hardware description language

## Audio Spectrum Visualizer

MICRO PROCESSOR

Jan. 2023 - Mar. 2023

- Design a Audio Spectrum Visualizer on a STM32 cortex m3 chip
- Gained knowledge about embeded programming, CPU.

## ID card detection using deep learning for eKYC products

DESIGN PROJECT 2

Jan. 2023 - Mar. 2023

- Design a Audio Spectrum Visualizer on a STM32 cortex m3 chip
- Gained knowledge about embeded programming, CPU.