Внутриплатные интерфейсы 57M32F401R

Системные выводы интерфейсы на внешние коннекторы

Специальные GPIO на внешние коннекторы

CU Pin Name		1			152		T		1	T	T		Terre	1.5	Arec	Распредел			T	1-
in Name 43 PA10	Type I/O	XH A01	AF0	AF1 TIM1 CH3	AF2	AF3	AF4	AF5	AF6	AF7 USART1 RX	AF8	AF9	AF10 USB_OTG_FS_ID	AF11	AF12	MCU-SD	PWR IMU	RF-GNSS	FPGA GPIO7	Comment Резервный интерфе
GND	-	A02		111112_0113						OSARTIZ_IDE			030_010_13_10						01107	т сэсропом интерфе
42 PA9	1/0	A03		TIM1_CH2			I2C3_SMBA			USART1_TX			USB_OTG_FS_VBUS						GPIO2	Резервный интерфе
41 PA8	1/0	A04	RCC_MCO_1	TIM1_CH1			I2C3_SCL			USART1_CK			USB_OTG_FS_SOF						GPIO3	Тактирование FPGA
38 PC7	1/0	A05			TIM3_CH2			I2S2_MCK	I2S3_MCK		USART6_RX USART6_TX				SDIO_D7			GNSS_TX	GPIO1	for GNSS
37 PC6 36 PB15	1/0	A06 A07	RTC_REFIN	TIM1 CH3N	TIM3_CH1			I2S2_MCK I2S2_SD/SPI2_MOSI			USAKI6_IX				SDIO_D6		MOSI	GNSS_RX MOSI	GPIO133 GPIO132	for GNSS main SPI
35 PB14	1/0	A08	NTC_RETIN	TIM1_CH2N				SPI2 MISO	I2S2_ext_SD								MISO	MISO	GPI0132	main SPI
+5.0V	-	A09																		
GND	-	A10																		
34 PB13	1/0	A11		TIM1_CH1N				I2S2_CK/SPI2_SCK									SCK	SCK	GPIO128	main SPI
33 PB12	1/0	A12		TIM1_BKIN			I2C2_SMBA	I2S2_WS/SPI2_NSS											GPI0121	main SPI
29 PB10 28 PB2	1/0	A13 A14		TIM2_CH3			I2C2_SCL	I2S2_CK/SPI2_SCK						-		LED	PE_nINT	GNSS_PPS	GPIO120	MCU_LED
+4.2Vbat	-	A15												1			PE_IIINI			
GND	-	A16																		
27 PB1	1/0	A17		TIM1_CH3N	TIM3_CH4												Ctrl2			
26 PB0	1/0	A18		TIM1_CH2N	TIM3_CH3												Ctrl1		GPIO11	
25 PC5	1/0	A19																SR_nOE	GPIO114	ADC
24 PC4	1/0	A20																SR_Latch	GPIO10	ADC
23 PA7 22 PA6	1/0	A21 A22		TIM1_CH1N TIM1_BKIN	TIM3_CH2 TIM3_CH1			SPI1_MOSI SPI1_MISO											GPIO115	Резервный интерс
+3.3V	1/0	A22 A23		LINIT_RKIN	THM3_CH1	-	I	3FII_IMISU						+	-	-		-	GPI0119	Резервный интер
GND		A24	<u> </u>			+	1			1				1		1		1	1	
21 PA5	1/0	B01		TIM2_CH1/TIM2_ETR			†	SPI1_SCK						1					GPIO60	Резервный интер
GND	-	B02																		
17 PA3	1/0	B03		TIM2_CH4	TIM5_CH4	TIM9_CH2				USART2_RX								RF2_TX		Резервный интер
20 PA4	1/0	B04						SPI1_NSS	I2S3_WS/SPI3_NSS	USART2_CK									GPIO59	Резервный интер
16 PA2	1/0	B05		TIM2_CH3	TIM5_CH3	TIM9_CH1				USART2_TX								RF2_RX		Резервный интер
15 PA1	1/0	B06		TIM2_CH2	TIM5_CH2 TIM5_CH1					USART2_RTS USART2_CTS							1Wire			
14 PA0-WKUP 11 PC3	1/0	B07 B08		TIM2_CH1/TIM2_ETR	IINI2_CH1			I2S2_SD/SPI2_MOSI		USAR12_CIS							Ctrl5	RF2_AUX	-	ADC
+5.0V		B09						1232_30/3112_W031							+	†		INI Z_AUA		AUC
GND		B10																		
9 PC1	1/0	B11															SR_Latch	1	GPIO31	ADC
10 PC2	1/0	B12						SPI2_MISO	I2S2_ext_SD									RF_IRQ	GPIO58	ADC
8 PC0	1/0	B13															Mux_Ou	t		ADC
2 PC13-ANTI_TAME	1/0	B14															SR_nOE		GPIO28	
+4.2Vbat GND	-	B15 B16												-						
61 PB8	1/0	B17			TIM4_CH3	TIM10_CH1	12C1 SCI								SDIO_D4				EN	
62 PB9	1/0	B18			TIM4_CH4	TIM11_CH1		I2S2_WS/SPI2_NSS							SDIO_D5				GPIO55	
58 PB6	1/0	B19			TIM4_CH1		I2C1 SCL			USART1_TX							SCL		GPIO49	main I2C
59 PB7	1/0	B20			TIM4_CH2		I2C1_SDA			USART1_RX							SDA		GPIO50	main I2C
57 PB5	1/0	B21			TIM3_CH2		I2C1_SMBA	SPI1_MOSI	I2S3_SD/SPI3_MOSI								Ctrl4		GPIO44	
56 PB4	1/0	B22	SYS_JTRST		TIM3_CH1			SPI1_MISO	SPI3_MISO	I2S3_ext_SD		I2C3_SDA					Ctrl3		GPIO46	
+3.3V GND	-	B23 B24																		
1 VBAT	Power	BZ4																	-	
3 PC14-OSC32 IN	1/0		RCC OSC32 IN																	
4 PC15-OSC32_OUT	1/0		RCC_OSC32_OUT																	
5 PHO - OSC_IN	1/0		RCC_OSC_IN																	
6 PH1 - OSC_OUT	1/0		RCC_OSC_OUT																	
7 NRST	Reset	1	1	1			-		1	1		-		-	1	!			1	1
12 VSSA/VREF- 13 VREF+	Power Power	-	1		-	-	 		+	-				+	-	-		-	1	1
18 VSS	Power	-			 	-	I							+	-	-		-		
19 VDD	Power						t							1					1	
30 VCAP1	Power													1						
31 VSS	Power															[
32 VDD	Power																			
39 PC8	1/0				TIM3_CH3						USART6_CK				SDIO_D0					
40 PC9	1/0		RCC_MCO_2		TIM3_CH4		I2C3_SDA	I2S_CKIN							SDIO_D1					
44 PA11 45 PA12	1/0	+	+	TIM1_CH4 TIM1_ETR		+	1		1	USART1_CTS USART1_RTS	USAR 16_TX		USB_OTG_FS_DM USB_OTG_FS_DP		+	 		-	+	1
46 PA13	1/0	-	SYS ITMS-SWIDE	I IIVIT_EIK			1		1	ODMUIT_KI2	OSAN (B_KX	 	036_010_F5_DF		+	 			+	1
46 PA13	Power	+	J.J_JI WIS-SWUIL			_	†		†	1		†		+	+	†		+	+	1
48 VDD	Power		1						1					1					1	1
49 PA14	1/0		SYS_JTCK-SWCLK											1						
50 PA15	1/0		SYS_JTDI	TIM2_CH1/TIM2_ETR				SPI1_NSS	I2S3_WS/SPI3_NSS							SD_CD				
51 PC10	1/0			1					I2S3_CK/SPI3_SCK						SDIO_D2					
52 PC11	1/0							I2S3_ext_SD	SPI3_MISO						SDIO_D3					
53 PC12	1/0		1				1		I2S3_SD/SPI3_MOSI			ļ		-	SDIO_CK				1	
54 PD2	1/0	-	CVC ITDO CV:	TIMAS CUIS	TIM3_ETR	1	1	CDIA CCIV	iaca ev tenia ce::			1262 60:		1	SDIO_CMD			-	1	1
55 PB3 60 BOOT0	I/O	-	242_100-SW0	TIM2_CH2	-	+	l	SPI1_SCK	I2S3_CK/SPI3_SCK	-	-	I2C2_SDA		+	+	 			+	1
60 BOOTO 63 VSS	Boot Power		1		1	-	 		1	-				+		-		+	+	1