

The diagram shows a circuit for driving DTR and RTS lines. It consists of two NPN transistors, T1 and T2, both labeled S8050. The base of T1 is connected to the DTR signal line through a 10K resistor (R6). The base of T2 is connected to the RTS signal line through a 10K resistor (R7). The emitters of both transistors are connected to ground. The collector of T1 is connected to the RESET signal line, and the collector of T2 is connected to the IO signal line. The signal lines are represented by green lines, and the power/ground lines are represented by red lines.

The diagram illustrates the electrical connections for the FPC 0.5MM 18P Pull type H2.0mm. It shows the following components and connections:

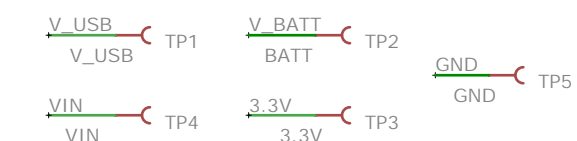
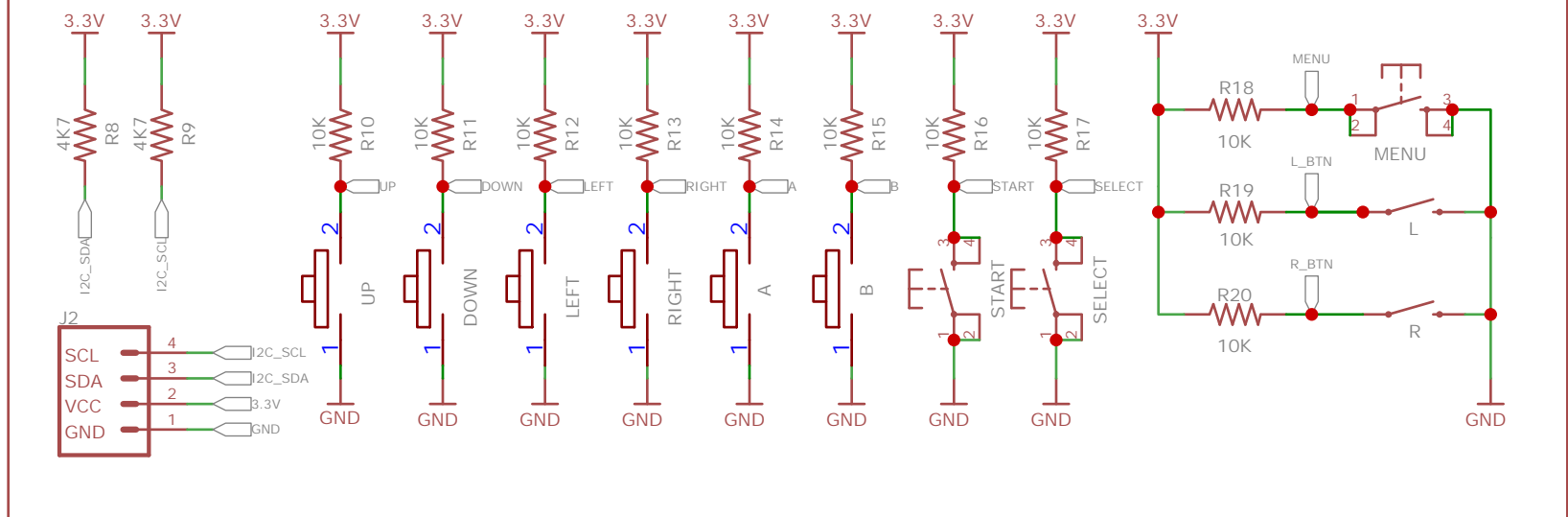
- Microcontroller (JP2):** Pins 1 through 20 are shown. Pins 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, and 20 are labeled. Pins 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, and 20 are connected to the FPC pins P\$1 through P\$18 respectively. Pins 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, and 20 are also connected to the FPC pins P\$1 through P\$18 respectively.
- FPC Connector:** Pins P\$1 through P\$18 are labeled. Pins P\$1 through P\$18 are connected to the microcontroller pins 1 through 18 respectively. Pins P\$1 through P\$18 are also connected to the microcontroller pins 1 through 18 respectively.
- Transistor (T3 S8050):** The transistor is connected to the FPC pins P\$13, P\$14, P\$15, P\$16, P\$17, and P\$18. The base of the transistor is connected to P\$13, the emitter to P\$14, and the collector to P\$15. The base is also connected to P\$16, the emitter to P\$17, and the collector to P\$18.
- Power and Ground:** A 3.3V_TFT supply is connected to the FPC pins P\$1, P\$2, P\$3, P\$4, P\$5, P\$6, P\$7, P\$8, P\$9, P\$10, P\$11, P\$12, P\$13, P\$14, P\$15, P\$16, P\$17, P\$18, and P\$19. Ground (GND) is connected to the FPC pins P\$1, P\$2, P\$3, P\$4, P\$5, P\$6, P\$7, P\$8, P\$9, P\$10, P\$11, P\$12, P\$13, P\$14, P\$15, P\$16, P\$17, P\$18, and P\$19.
- Resistors:** A 100K resistor (R21) is connected between the 3.3V_TFT supply and the base of the transistor. A 10K resistor (R22) is connected between the base of the transistor and the emitter. A 10K resistor (R23) is connected between the emitter of the transistor and ground.

The diagram illustrates the electrical connection between a MicroSD-Push-Out connector (J1) and an SD card. The connector pins and their connections are as follows:

- CD**: Connected to a 3.3V supply through a 10K resistor (R3).
- DAT1**: Connected to a 3.3V supply through a 10K resistor (R4).
- DAT0_DO**: Connected to the SD card's **SD_DATA0** pin.
- VSS**: Connected to the SD card's **VSS** pin.
- CLK**: Connected to the SD card's **SD_CLK** pin.
- VDD**: Connected to the SD card's **VDD** pin.
- CMD_DIN**: Connected to the SD card's **SD_CMD** pin.
- CD_DAT3_CS**: Connected to the SD card's **CD** pin through a 10K resistor (R5).
- DAT2**: Connected to the SD card's **DAT2** pin.

Additional components include:

- JP1**: A bootstrap jumper connected between the 3.3V supply and the **SD_DATA0** pin.
- C4**: A 10uF capacitor connected between the 3.3V supply and ground.
- D2, D3, D4**: ESD protection diodes connected between the 3.3V supply and ground.



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| ESPLAY MICRO | |
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