CS410 Project 3 Design Report

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December 2022

Introduction

This report discuss the simulation of Turing Machine written in C++17 with g++ compiler. Turing Machines written in a specific format in text files are read and processed in design logic.

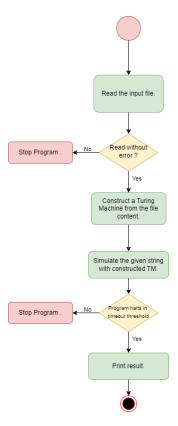
Objectives

The implementation follows below objectives.

- 1. Read the input file.
- 2. Construct a Turing Machine from the file content.
- 3. With the given input string, simulate the Turing Machine
- 4. Print the result if the program halts in timeout threshold, otherwise quit the program.

Software Architecture Overview

Activity Diagram



As can be seen from the activity diagram, the input file is read at the start of the program. If it is read with an error, the program prints the error to the console and exits. Otherwise, Turing Machine is constructed from the file content. After construction, TM is simulated with the given string. In the end, if the program halts in the timeout threshold, the result is printed. Otherwise, the program is terminated.

Class Diagram

```
+FILE_STATES: vector<string>
+INPUT_STRING: string
+TM: TuringMachine

+FileReader(string file_path): TuringMachine
+printTM(TuringMachine TM):void
+simulate_tm(TuringMachine TM, string input_string): vector<string>,string
```

TuringMachine INPUT_ALPHABET: set<string> TAPE_ALPHABET: set<string> TAPE: list<string> BLANK: string STATES: : set<string> START: string ACCEPT: string REJECT: string TRANSITIONS: map<string, map<string, map<string, string>>>

This program consists of 1 class and 1 struct. The struct named Turing-Machine encapsulates the definition members of the Turing Machines. Turing Machine definition text files are read thanks to FileReader method. Each state in the text file representing which definition member of the grammar will be captured thanks to a vector of strings containing file states called **FILE_STATES**. While reading the file's content, **TuringMachine** structure is used to encapsulate the definition members of grammar. This structure gives the blueprint of the desired grammar. The code defines grammar members as follows: IN-PUT_ALPHABET, TAPE_ALPHABET, TAPE, BLANK, STATES, ACCEPT, REJECT, and TRANSITIONS. Also, INPUT_ALPHABET. TAPE_ALPHABET, STATES variables are encapsulated in the set data structure. TAPE variable is encapsulated in the list. BLANK, START, ACCEPT, REJECT variables are simply a string. Finally, TRANSITIONS variable is encapsulated in the 3-dimensional map data structure. After creating a TM from the text file, **simulate_tm** method is used for simulation with the given input. After the simulation, if the program halts in the predetermined timeout threshold, results are printed. Otherwise, the program stops.

Simulation Process

To simulate the Turing Machine with a given input string, **simulate_tm** function is executed with constructed TM and the input string. First, the tape of

the TM is initialized by copying the content of the input string to the tape. Secondly, tape_head and current_state variables are created and set to the START state of TM. Also, a vector of strings typed variable namely route is created and START state is added to this vector. Thirdly, while loop is created within the condition of the current state is any state rather than ACCEPT or REJECT states. Inside the while loop, the current symbol is set to tape head and get the content of the Turing machine with respect to the current symbol. If the direction is right, the tape head is set to the right of the current tape content. If the tape is reached to end, the blank symbol is added to the end. If the direction is left, the tape head is set to the left of the current tape content. If the tape is reached to start, the tape head stayed at the same location. After deciding on the tape head, the current state is updated to a new state, and this new state is added to the route vector. At the end of the while loop, the route and final state are returned.

Results

Here, I tested my algorithm with 2 text files that I created, input.txt and loop.txt. The program takes the path of the text file as an argument and prints the original TM and the simulation results in the desired format.

```
Simulation
   Turing Machine
  INPUT
                          [INFO]Reading the file : input.txt
                                -----Turing Machine Content--
  TAPE
                          INPUT ALPHABET: 0
  0 X
                          TAPE ALPHABET: 0 X b
  BLANK
                         BLANK SYMBOL: b
                          STATES: q1 q2 q3 q4 q5 qA qR
  STATES
                          START: q1
  q1 q2 q3 q4 q5 qA qR
                          ACCEPT: qA
  START
                          REJECT: qR
  ACCEPT
                          TRANSITIONS:
                          q1 0 b R q2
  qΑ
  REJECT
                          q1 X X R qR
  qR
                          q1 b b R qR
  TRANSITION
                          q2 0 X R q3
  q1 0 b R q2
                         q2 X X R q2
q2 b b R qA
q3 0 0 R q4
  q1 b b R qR
  q1 X X R qR
  q2 0 X R q3
                          q3 X X R q3
  q2 X X R q2
                          q3 b b L q5
  q2 b b R qA
  q3 X X R q3
                          q4 0 X R q3
                          q4 X X R q4
q4 b b R qR
  q3 0 0 R q4
  q3 b b L q5
  q4 X X R q4
                          q5 0 0 L q5
  q4 0 X R q3
                          q5 X X L q5
  q4 b b R qR
                          q5 b b R q2
  q5 0 0 L q5
                                         ---Simulation Results------
  q5 X X L q5
                          ROUT: q1 q2 q3 q4 qR
  q5 b b R q2
                          RESULT: REJECT
  STRING
                            -----End of Simulation-----
  000
INPUT
TAPE
0
BLANK
b
STATES
q1 q2 qA qR
START
q1
                           [INFO]Reading the file : loop.txt
ACCEPT
                            ----------Turing Machine Content---
                           INPUT ALPHABET: 0
qΑ
                           TAPE ALPHABET: 0 b
REJECT
                           BLANK SYMBOL: b
                           STATES: q1 q2 qA qR
qR
                           START: q1
                           ACCEPT: qA
TRANSITION
                           REJECT: qR
q1 0 0 R q2
                           TRANSITIONS:
                           q1 0 0 R q2
q2 0 0 L q1
                           q2 0 0 L q1
STRING
                           Loop Detected!
00
                           Exiting...
```

Table 1: Table of results