

# Module Guide for FSL

Bo Cao

December 6, 2019

[Please disregard the following problems: reference not working, and cross-reference not working in regards to requirements from SRS. I have noticed them, and I am tackling them now. However, these problems do not affect the whole picture of this document, and I think that you can know the right content did these problems not exist. —Author]

[I’ve fixed your bibliography. You just needed to add the correct references to your bib file. With respect to the external cross-references, I’m not sure where it comes up in your document. After a quick look, I didn’t find any external references, or references that didn’t work. —SS]

[The reason you did not find external references is that I removed all of them after I found that they don’t work. Sorry that I did say it clear in my documents. I thought you mentioned in the classes that we have to use external references in this document for requirements, facilitating maintenance. —Author]

## 1 Revision History

| Date         | Version | Notes         |
|--------------|---------|---------------|
| Nov 25, 2019 | 0.99    | Initial Draft |

## 2 Reference Material

This section records information for easy reference.

### 2.1 Abbreviations and Acronyms

| symbol | description                         |
|--------|-------------------------------------|
| AC     | Anticipated Change                  |
| DAG    | Directed Acyclic Graph              |
| M      | Module                              |
| MG     | Module Guide                        |
| OS     | Operating System                    |
| R      | Requirement                         |
| SC     | Scientific Computing                |
| SRS    | Software Requirements Specification |
| FSL    | Fourier Series Library              |
| UC     | Unlikely Change                     |

# Contents

|          |   |           |
|----------|---|-----------|
| <b>1</b> | <b>Revision History</b>                           | <b>i</b>  |
| <b>2</b> | <b>Reference Material</b>                         | <b>ii</b> |
| 2.1      | Abbreviations and Acronyms . . . . .              | ii        |
| <b>3</b> | <b>Introduction</b>                               | <b>1</b>  |
| <b>4</b> | <b>Anticipated and Unlikely Changes</b>           | <b>2</b>  |
| 4.1      | Anticipated Changes . . . . .                     | 2         |
| 4.2      | Unlikely Changes . . . . .                        | 2         |
| <b>5</b> | <b>Module Hierarchy</b>                           | <b>2</b>  |
| <b>6</b> | <b>Connection Between Requirements and Design</b> | <b>3</b>  |
| <b>7</b> | <b>Module Decomposition</b>                       | <b>3</b>  |
| 7.1      | Infrastructure module (M1) . . . . .              | 4         |
| 7.2      | Software Decision Modules . . . . .               | 4         |
| 7.2.1    | Linear solver module M2 . . . . .                 | 4         |
| 7.2.2    | Integral module M3 . . . . .                      | 4         |
| 7.3      | Behavior-hiding modules . . . . .                 | 4         |
| 7.3.1    | Data definition module M4 . . . . .               | 4         |
| 7.3.2    | Conversion module M5 . . . . .                    | 5         |
| 7.3.3    | Transformations M6 . . . . .                      | 5         |
| 7.3.4    | Basic operation M7 . . . . .                      | 5         |
| 7.3.5    | Advanced operation M8 . . . . .                   | 5         |
| <b>8</b> | <b>Traceability Matrix</b>                        | <b>6</b>  |
| <b>9</b> | <b>Use Hierarchy Between Modules</b>              | <b>6</b>  |

## List of Tables

|   |   |   |
|---|---|---|
| 1 | Module Hierarchy . . . . .                              | 3 |
| 2 | Trace Between Requirements and Modules . . . . .        | 6 |
| 3 | Trace Between Anticipated Changes and Modules . . . . . | 6 |

## List of Figures

|   |                                       |   |
|---|---------------------------------------|---|
| 1 | Use hierarchy among modules . . . . . | 7 |
|---|---------------------------------------|---|

### 3 Introduction

Decomposing a system into modules is a commonly accepted approach to developing software. A module is a work assignment for a programmer or programming team (Parnas et al., 1984). We advocate a decomposition based on the principle of information hiding (Parnas, 1972). This principle supports design for change, because the “secrets” that each module hides represent likely future changes. Design for change is valuable in SC, where modifications are frequent, especially during initial development as the solution space is explored.

Our design follows the rules laid out by Parnas et al. (1984), as follows:

- System details that are likely to change independently should be the secrets of separate modules.
- Each data structure is implemented in only one module.
- Any other program that requires information stored in a module’s data structures must obtain it by calling access programs belonging to that module.

After completing the first stage of the design, the Software Requirements Specification (SRS), the Module Guide (MG) is developed (Parnas et al., 1984). The MG specifies the modular structure of the system and is intended to allow both designers and maintainers to easily identify the parts of the software. The potential readers of this document are as follows:

- New project members: This document can be a guide for a new project member to easily understand the overall structure and quickly find the relevant modules they are searching for.
- Maintainers: The hierarchical structure of the module guide improves the maintainers’ understanding when they need to make changes to the system. It is important for a maintainer to update the relevant sections of the document after changes have been made.
- Designers: Once the module guide has been written, it can be used to check for consistency, feasibility and flexibility. Designers can verify the system in various ways, such as consistency among modules, feasibility of the decomposition, and flexibility of the design.

The rest of the document is organized as follows. Section 4 lists the anticipated and unlikely changes of the software requirements. Section 5 summarizes the module decomposition that was constructed according to the likely changes. Section 6 specifies the connections between the software requirements and the modules. Section 7 gives a detailed description of the modules. Section 8 includes two traceability matrices. One checks the completeness of the design against the requirements provided in the SRS. The other shows the relation between anticipated changes and the modules. Section 9 describes the use relation between modules.

## 4 Anticipated and Unlikely Changes

This section lists possible changes to the system. According to the likeliness of the change, the possible changes are classified into two categories. Anticipated changes are listed in Section 4.1, and unlikely changes are listed in Section 4.2.

### 4.1 Anticipated Changes

Anticipated changes are the source of the information that is to be hidden inside the modules. Ideally, changing one of the anticipated changes will only require changing the one module that hides the associated decision. The approach adapted here is called design for change.

**AC1:** The specific hardware and OS on which the software is running.

**AC2:** The format of the data that the conversion functions accept.

**AC3:** The type used in the API's languages that represents a function transformed into a CFS.

**AC4:** The linear solver used in the division operation.

**AC5:** The integral function used in transformation to CFS.

### 4.2 Unlikely Changes

The module design should be as general as possible. However, a general system is more complex. Sometimes this complexity is not necessary. Fixing some design decisions at the system architecture stage can simplify the software design. If these decision should later need to be changed, then many parts of the design will potentially need to be modified. Hence, it is not intended that these decisions will be changed.

**UC1:** The data structure to store  $A_i$ 's and  $B_i$ 's in the CFS's.

## 5 Module Hierarchy

This section provides an overview of the module design. Modules are summarized in a hierarchy decomposed by secrets in Table 1. The modules listed below, which are leaves in the hierarchy tree, are the modules that will actually be implemented.

**M1:** The module that provides the infrastructure of the library. Part of the implementing languages' run-time libraries.

**M2:** The linear solver module, implemented by others, and used by our library's division operation.

**M3:** The integral module, implemented by others, and used by the function in our library that transforms a mathematical function to a CFS.

**M4:** The data definition module.

**M5:** The conversion module to convert other data formats from/to CFS's.

**M6:** The transformation module to transform mathematical functions from/to CFS's

**M7:** The basic operation module including the addition, subtraction, multiplication, division and amplitude operation.

**M8:** The advanced operation module including the function of CFS, and tolerated equality operations.

| Level 1                  | Level 2   |
|--------------------------|---|
| Hardware-Hiding Module   | M1 (hardware-related part, external)              |
|                          | M4 Data definition module                         |
|                          | M5 Conversion module                              |
|                          | M6 Transformation module                          |
| Behaviour-Hiding Module  | M7 Basic operations module                        |
|                          | M8 Advanced operations module                     |
|                          | M2 Linear solver (external or partially external) |
| Software Decision Module | M3 Integral (external or partially external)      |

Table 1: Module Hierarchy

## 6 Connection Between Requirements and Design

The design of the system is intended to satisfy the requirements developed in the SRS. In this stage, the system is decomposed into modules. The connection between requirements and modules is listed in Table 2.

## 7 Module Decomposition

Modules are decomposed according to the principle of “information hiding” proposed by Parnas et al. (1984). The *Secrets* field in a module decomposition is a brief statement of the design decision hidden by the module. The *Services* field specifies *what* the module will do without documenting *how* to do it. For each module, a suggestion for the implementing

software is given under the *Implemented By* title. If the entry is *OS*, this means that the module is provided by the operating system or by standard programming language libraries. *FSL* means the module will be implemented by the FSL software.

Only the leaf modules in the hierarchy have to be implemented. If a dash (–) is shown, this means that the module is not a leaf and will not have to be implemented.

## 7.1 Infrastructure module (M1)

**Secrets:** How the basic data structure [spell check and proof read —SS] [Corrected. Sorry that my spell check plug-in did not detect this, for strange reasons. —Author] and functions are implemented on a specific hardware/OS set.

**Services:** Serves as the basic functions and data structure (like sequence, etc.) used by the rest of the system. This module provides the interface between the hardware and the application software. So, the system can use it to build data structures, execute related operations, accept inputs or display outputs.

**Implemented By:** OS and run-time library of our implementing language.

## 7.2 Software Decision Modules

### 7.2.1 Linear solver module M2

**Secrets:** Implement the linear equation solving algorithm chosen by the designer.

**Services:** Solves the linear equation generated by FSL.

**Implemented By:** Third party.

### 7.2.2 Integral module M3

**Secrets:** Implement the integration algorithm decided by the module designer.

**Services:** Calculates the integration in the transformation from a mathematical function to its CFS.

**Implemented By:** Third party

## 7.3 Behavior-hiding modules

### 7.3.1 Data definition module M4

**Secrets:** Define the internal data structure of a CFS

**Services:** Provide CFS's getters and setters.

**Implemented By:** FSL



### 7.3.2 Conversion module M5

**Secrets:** Implement the copy of the data based on the CFS's getters and setters.

**Services:** Provide conversion from/to data in other formats.

**Implemented By:** FSL

### 7.3.3 Transformations M6

**Secrets:** Implement the transformation algorithm based on already implemented integral.

**Services:** Provide transformation from/to a mathematical function.

**Implemented By:** FSL

### 7.3.4 Basic operation M7

**Secrets:** Addition, subtraction, multiplication amplitude: Word-by-word as described in theories in SRS.

Division: Given that the division is solving a linear equation whose variable is a vector constructed from the CFS, to get equation  $Ax = b$ , the library will calculate the  $A$  and  $b$  from the operands, pass the equation to linear solver for solution, and calculate the coefficients in CFST-type result from the solution.

**Services:** Provide addition, subtraction, multiplication, division and amplitude operations.

**Implemented By:** FSL

### 7.3.5 Advanced operation M8

**Secrets:** Word-by-word as described in theories in SRS. [Is this a copy and paste error? I would think the word Advanced would be used here. What are the operations that are having their implementation hidden? —SS] [Rephrased here for better understanding. —Author]

[By advanced, I tend to mean those operations whose definitions rely on other operations, and for rather complex (and rare) functionalities. Their defining expressions, if not counting the basic operations within themselves, are quite simple structurally. Actually, if you refer to my SRS, especially the theories and their proof in the appendix, you will find that the longest one is the basic multiplication, not the ones in the advanced operations module. —Author]

**Services:** Provide function of CFS and tolerated equality operations.

**Implemented By:** FSL

## 8 Traceability Matrix

This section shows two traceability matrices: between the modules and the requirements and between the modules and the anticipated changes.

| Req. | Modules                        |
|------|--------------------------------|
| R1   | M4, M5, M6                     |
| R2   | M7, M8                         |
| R3   | M1, M4, M5, M6, M7, M8, M2, M3 |
| R4   | M2, M3, M6, M7, M8             |
| R5   | M1, M2, M3, M4,                |

Table 2: Trace Between Requirements and Modules

| AC  | Modules |
|-----|---------|
| AC1 | M1, M4  |
| AC2 | M5      |
| AC3 | M6      |
| AC4 | M7, M2  |
| AC5 | M6, M3  |

Table 3: Trace Between Anticipated Changes and Modules

## 9 Use Hierarchy Between Modules

In this section, the uses hierarchy between modules is provided. Parnas (1978) said of two programs A and B that A *uses* B if correct execution of B may be necessary for A to complete the task described in its specification. That is, A *uses* B if there exist situations in which the correct functioning of A depends upon the availability of a correct implementation of B. Figure 1 illustrates the use relation between the modules. It can be seen that the graph is a directed acyclic graph (DAG). Each level of the hierarchy offers a testable and usable subset of the system, and modules in the higher level of the hierarchy are essentially simpler because they use modules from the lower levels.

**Please note that any module without the comment “(external)” is directly accessible by the user of this library.**

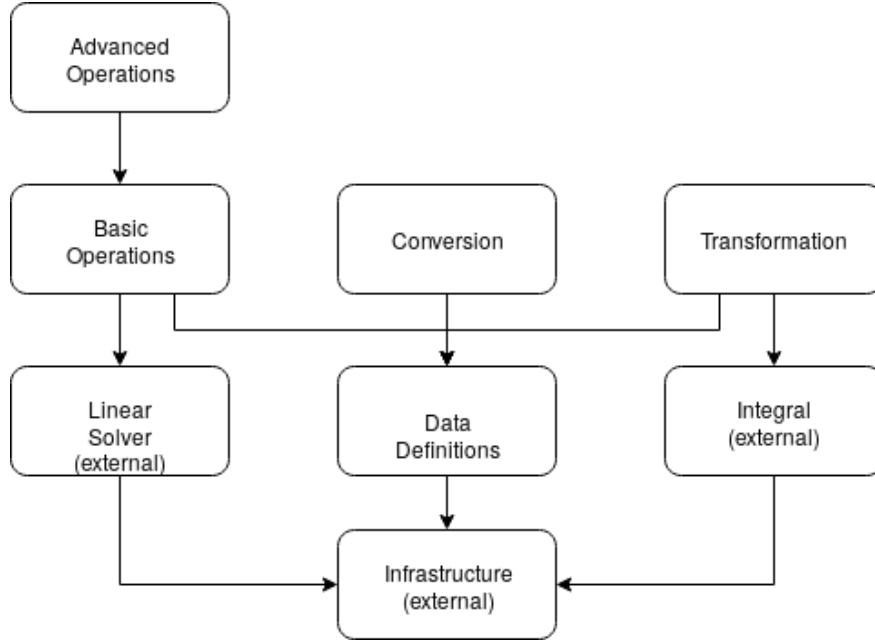


Figure 1: Use hierarchy among modules

[With a uses hierarchy for a library, I find it useful to include a symbol to represent the “driver” program. Instead of a rectangle, you can put the driver program in an ellipse, or circle, at the top of your diagram. It would have some useful information, since I cannot currently tell whether the external program can directly access the basic operations, or whether only advanced operations are accessible on the front end. —SS] [Theortically, I intend to expose any module that does not say 'external', thus I did not see this point at that time. I will make such notice just before this figure. —Author]

## References

- David L. Parnas. On the criteria to be used in decomposing systems into modules. *Comm. ACM*, 15(2):1053–1058, December 1972.
- David L. Parnas. Designing software for ease of extension and contraction. In *ICSE '78: Proceedings of the 3rd international conference on Software engineering*, pages 264–277, Piscataway, NJ, USA, 1978. IEEE Press. ISBN none.
- D.L. Parnas, P.C. Clement, and D. M. Weiss. The modular structure of complex systems. In *International Conference on Software Engineering*, pages 408–419, 1984.