# A Fourier Series Library: System Verification and Validation Plan for FSL

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# 1 Revision History

Date	Version	Notes
Oct. 28, 2019 Date 2	1.0 1.1	First draft. Notes

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# 2 Symbols, Abbreviations and Acronyms

Some symbols, abbreviations and acronyms are defined in the Common Analysis (CA) document <sup>1</sup>. For simplicity and maintainability, they are not redefined here. Readers shall refer to the CA documents when a certain item is not defined here.

symbol	description					
Т	Test					

<sup>&</sup>lt;sup>1</sup>This document is available at https://github.com/caobo1994/FourierSeries/blob/master/docs/SRS/CA.pdf.

This document provides an overview of the Verification and Validation (VnV) plan for the Fourier Series Library (FSL). It lays out the purpose, methods, and test cases for the VnV procedure.

#### 3 General Information

#### 3.1 Summary

The library to be tested is called the Fourier Series Library (FSL). This library performs a set of computations, transformations, and/or input/output at the request of the library user.

#### 3.2 Objectives

The intended objective of the VnV procedure is to verify that this library has generally met the requirements described in the CA document. These requirements include the functional requirements (FRs) and the non-functional requirements (NFRs).

Note that if a small part of the NFRs has not been met, the library is still acceptable when the not-met NFRs' impact has been analyzed and deemed non-essential.

#### 3.3 Relevant Documentation

As we said before, this document relies on the CA document. This document is also the base of the Unit Test Plan document.

#### 4 Plan

#### 4.1 Verification and Validation Team

The major member of the team is the author himself. Other contributors might assist in the VnV procedure, but their contributions are not guaranteed.

#### 4.2 SRS Verification Plan

[List any approaches you intend to use for SRS verification. This may just be ad hoc feedback from reviewers, like your classmates, or you may have something more rigorous/systematic in mind..—SS]

#### 4.3 Design Verification Plan

[Plans for design verification—SS]

#### 4.4 Implementation Verification Plan

[You should at least point to the tests listed in this document and the unit testing plan. —SS]

[In this section you would also give any details of any plans for static verification of the implementation. Potential techniques include code walk-throughs, code inspection, static analyzers, etc. —SS]

#### 4.5 Software Validation Plan

[If there is any external data that can be used for validation, you should point to it here. If there are no plans for validation, you should state that here. —SS]

## 5 System Test Description

## 5.1 Tests for Functional Requirements

[Subsets of the tests may be in related, so this section is divided into different areas. If there are no identifiable subsets for the tests, this level of document structure can be removed. —SS]

[Include a blurb here to explain why the subsections below cover the requirements. References to the SRS would be good. —SS]

#### 5.1.1 Area of Testing1

[It would be nice to have a blurb here to explain why the subsections below cover the requirements. References to the SRS would be good. If a section covers tests for input constraints, you should reference the data constraints table in the SRS. —SS

#### Title for Test

1. test-id1

Control: Manual versus Automatic

Initial State:

Input:

Output: [The expected result for the given inputs—SS]

Test Case Derivation: [Justify the expected value given in the Output field —SS]

How test will be performed:

2. test-id2

Control: Manual versus Automatic

Initial State:

Input:

Output: [The expected result for the given inputs —SS]

Test Case Derivation: [Justify the expected value given in the Output field —SS]

How test will be performed:

#### 5.1.2 Area of Testing2

. . .

#### 5.2 Tests for Nonfunctional Requirements

[The nonfunctional requirements for accuracy will likely just reference the appropriate functional tests from above. The test cases should mention reporting the relative error for these tests. —SS]

[Tests related to usability could include conducting a usability test and survey. —SS]

#### 5.2.1 Area of Testing1

#### Title for Test

1. test-id1

Type:

Initial State:

Input/Condition:

Output/Result:

How test will be performed:

2. test-id2

Type: Functional, Dynamic, Manual, Static etc.

Initial State:

Input:

Output:

How test will be performed:

#### 5.2.2 Area of Testing2

. . .

# 5.3 Traceability Between Test Cases and Requirements

[Provide a table that shows which test cases are supporting which requirements. —SS]

# References

# 6 Appendix

This is where you can place additional information.

# 6.1 Symbolic Parameters

The definition of the test cases will call for SYMBOLIC\_CONSTANTS. Their values are defined in this section for easy maintenance.

## 6.2 Usability Survey Questions?

[This is a section that would be appropriate for some projects. —SS]