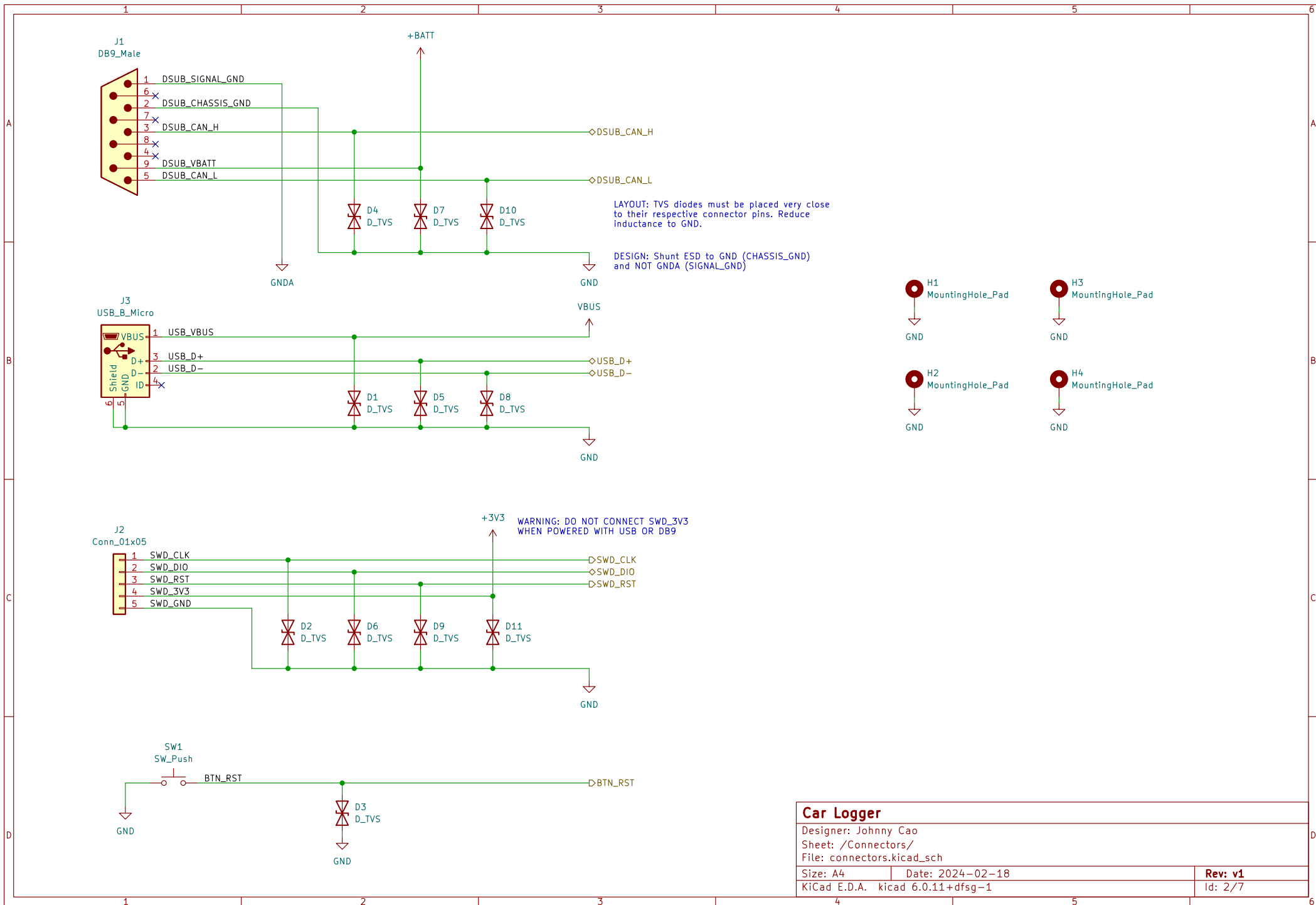
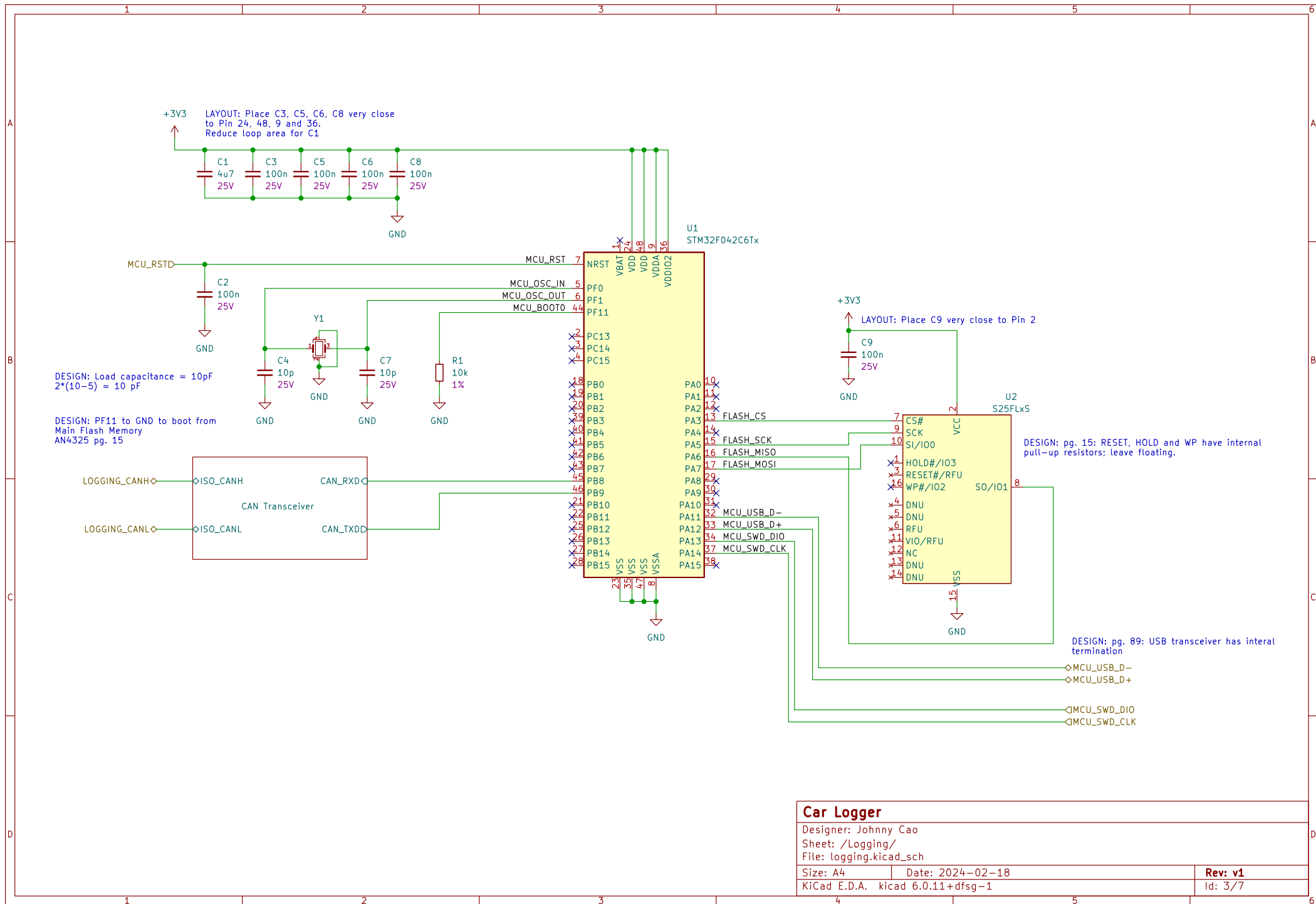


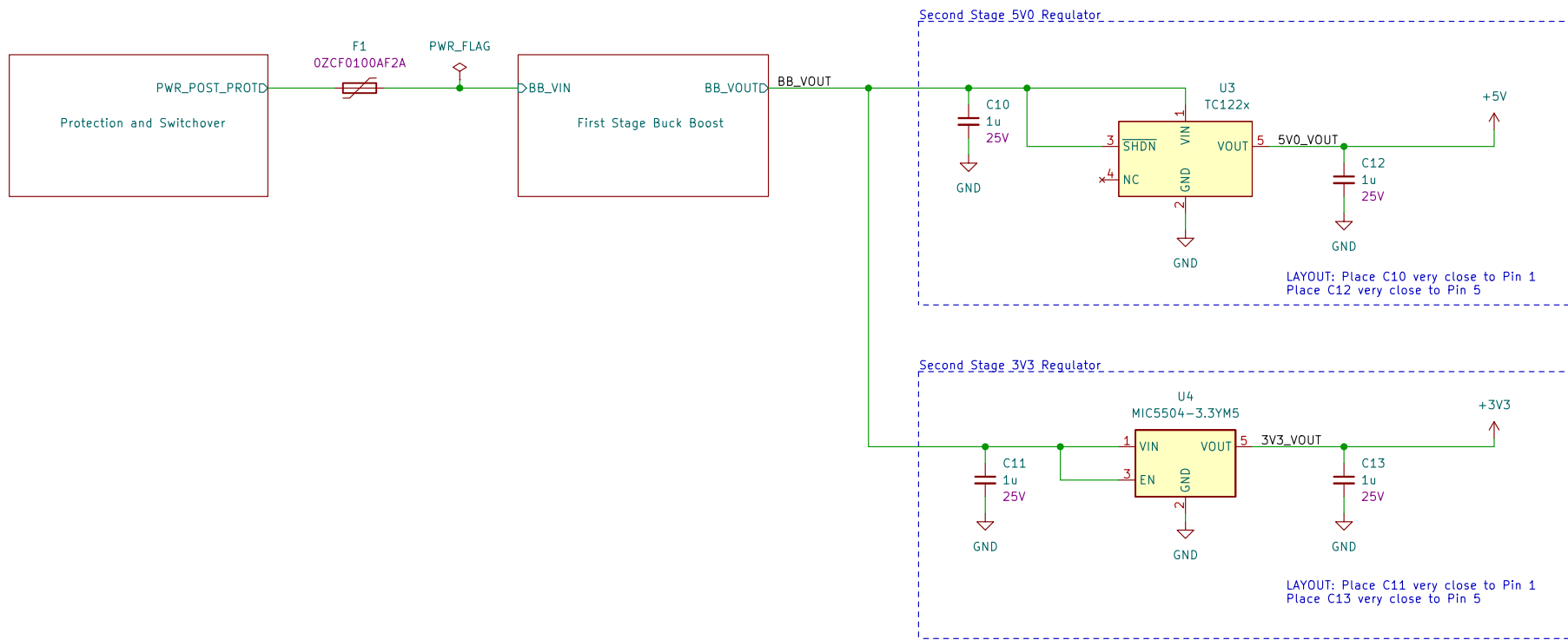
Car Logger		
Designer: Johnny Cao		
Sheet: /		
File: car-logger.kicad_sch		
Size: A4	Date: 2024-02-18	Rev: v1
KiCad E.D.A. kicad 6.0.11+dfsg-1		Id: 1/7



Car Logger		
Designer: Johnny Cao		
Sheet: /Connectors/		
File: connectors.kicad_sch		
Size: A4	Date: 2024-02-18	Rev: v1
KiCad E.D.A. kicad 6.0.11+dfsg-1		Id: 2/7



Car Logger		
Designer: Johnny Cao		
Sheet: /Logging/		
File: logging.kicad_sch		
Size: A4	Date: 2024-02-18	Rev: v1
KiCad E.D.A. kicad 6.0.11+dfsg-1	Id: 3/7	



Car Logger

Designer: Johnny Cao

Sheet: /Power/

File: power.kicad_sch

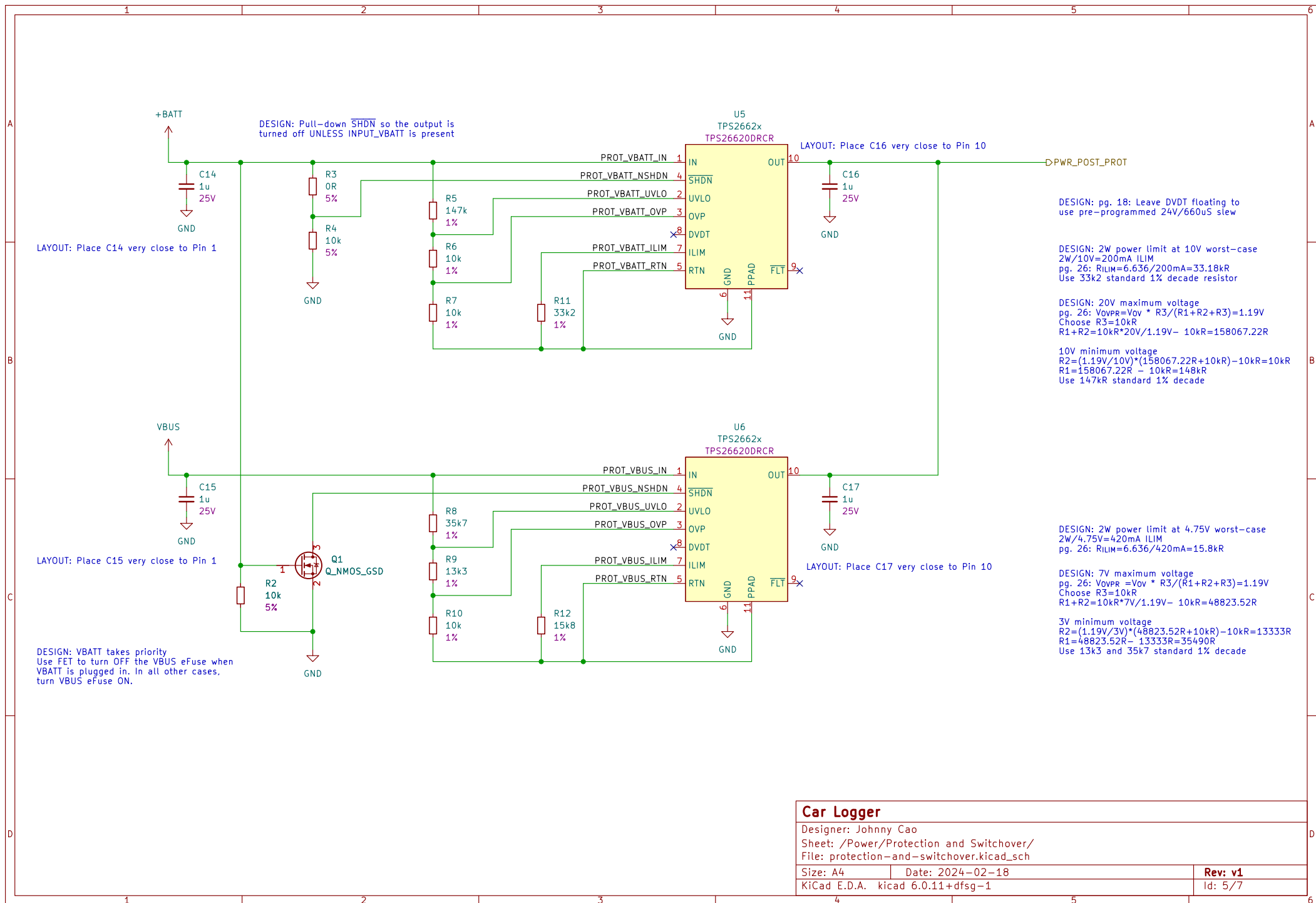
Size: A4

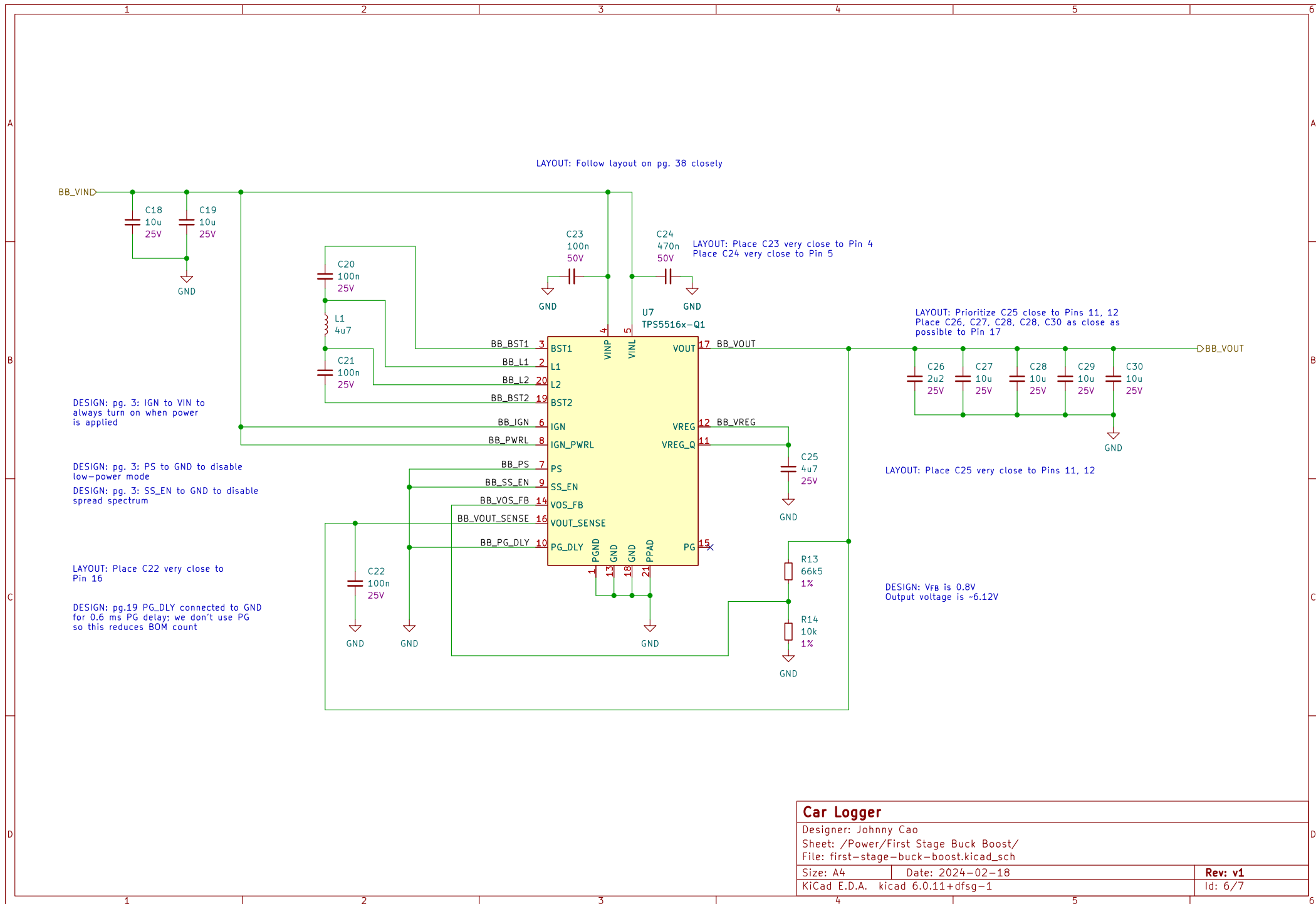
Date: 2024-02-18

Rev: v1

KiCad E.D.A. kicad 6.0.11+dfsg-1

Id: 4/7





DESIGN: VIO is logic-level supply; use 3V3.
VDD is the converter voltage,
must be 4V5-5V5; use 5V0

LAYOUT: Place C31, C33, C35, C37
very close to Pin 9

LAYOUT: Place C32, C34, C36, C38
very close to Pin 1

DESIGN: pg. 3; connect EN to VIO
to enable DC-DC

DESIGN: pg. 3; Connect STB to GNDIO in
normal mode

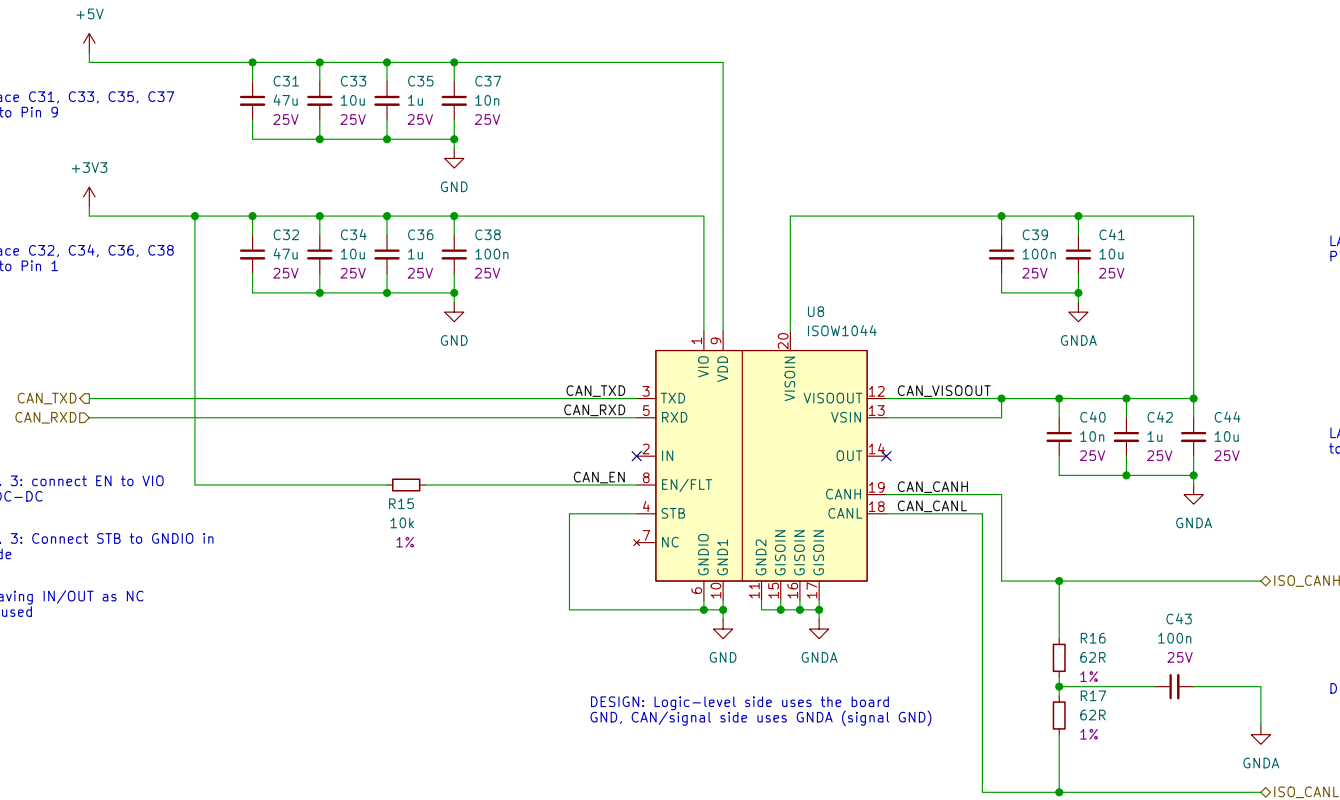
DESIGN: Leaving IN/OUT as NC
because unused

DESIGN: Logic-level side uses the board
GND, CAN/signal side uses GNDA (signal GND)

LAYOUT: Place C39, C41 very close to
Pin 20

LAYOUT: Place C40, C42, C44 very close
to Pin 12

DESIGN: Ideal termination is 60R



Car Logger

Designer: Johnny Cao
Sheet: /Logging/CAN Transceiver/
File: can-transceiver.kicad_sch

Size: A4
KiCad E.D.A. kicad 6.0.11+dfsg-1

Date: 2024-02-18

Rev: v1

Id: 7/7