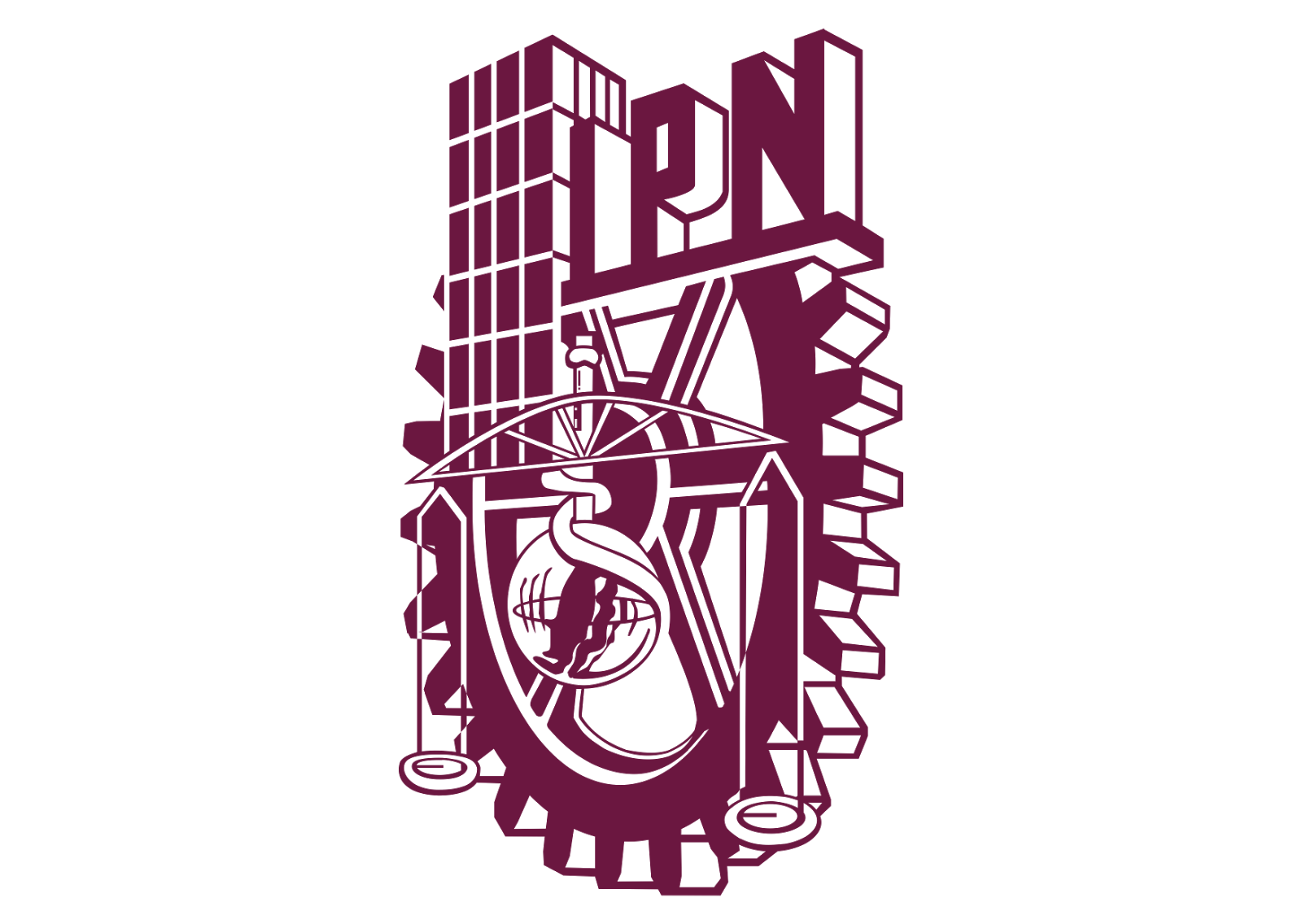
**Instituto Politécnico Nacional**

**Escuela Superior de Cómputo**

Arquitectura de computadoras

**Práctica 1: sumador/restador de N bits con**

**acarreo en cascada.**

Rocío Vivia Delgadillo

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Grupo 3CV3

Profesora: Nayeli Vega García

**Sumador**

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| a | b | Cin | s | Cout |
| 0 | 0 | 0 | 0 | 0 |
| 0 | 0 | 1 | 1 | 0 |
| 0 | 1 | 0 | 1 | 0 |
| 0 | 1 | 1 | 0 | 1 |
| 1 | 0 | 0 | 1 | 0 |
| 1 | 0 | 1 | 0 | 1 |
| 1 | 1 | 0 | 0 | 1 |
| 1 | 1 | 1 | 1 | 1 |