

# EPICS Test Report

## SN0028 lab{1}

### 2025-10-13T13:36:59.075012

#### Device configuration

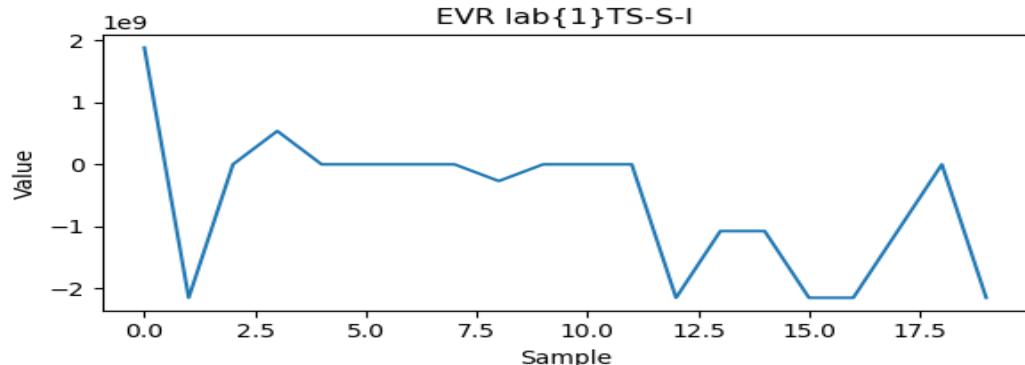
PV	Value
lab{1}NumChannels-Mode	2 Channel
lab{1}Resolution-Mode	HS
lab{1}Bandwidth-Mode	Slow
lab{1}Polarity-Mode	Unipolar

#### Section 1: DAC loopback test

Channel	Readback	Pass/Fail
Chan1	0.9986029267311096	PASS
Chan2	1.003689169883728	PASS

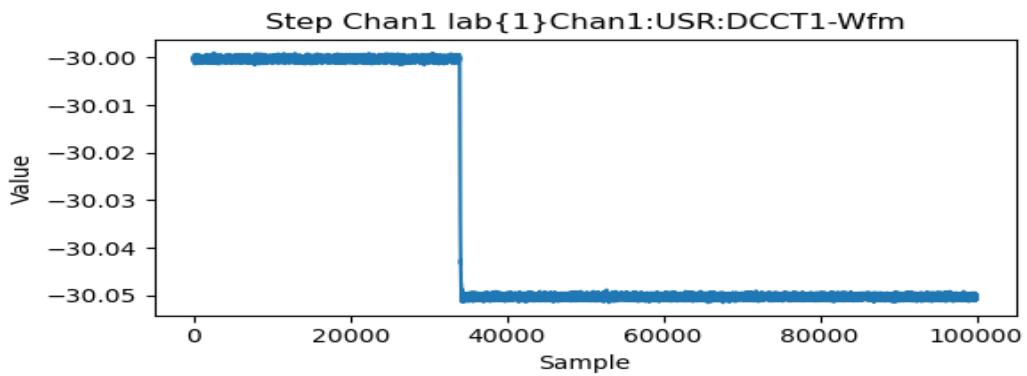
#### Section 2: EVR monotonicity test

EVR PV: lab{1}TS-S-I monotonic test -> FAIL

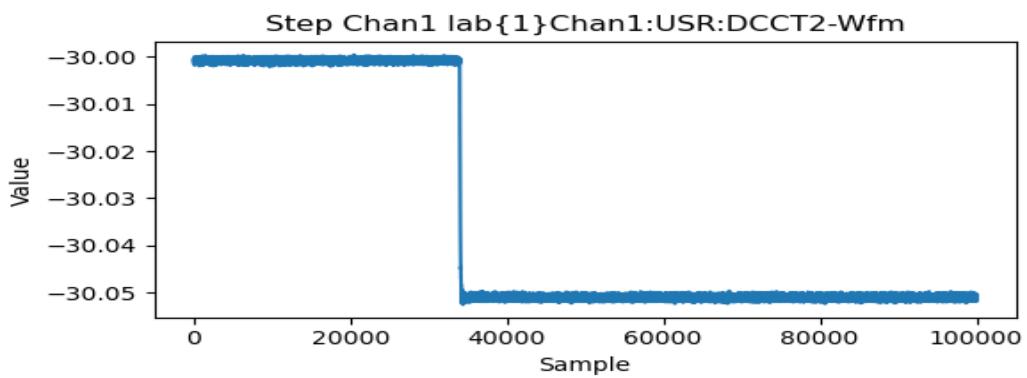


#### Section 3: Test Data

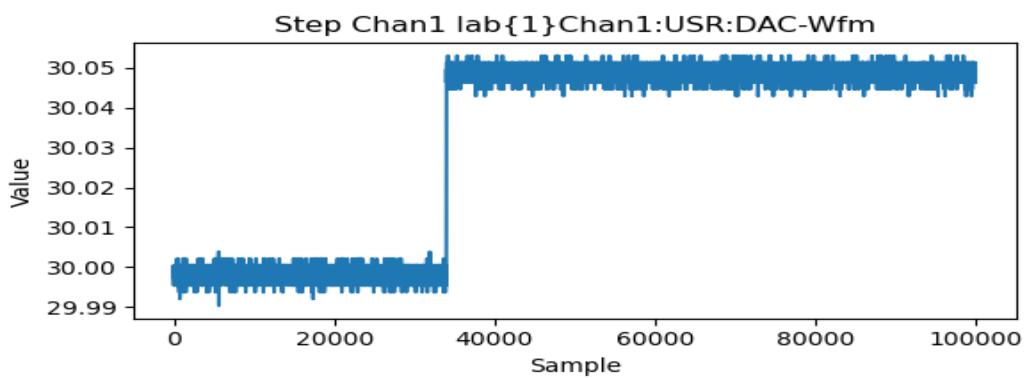
Step Chan1 PV: lab{1}Chan1:USR:DCCT1-Wfm



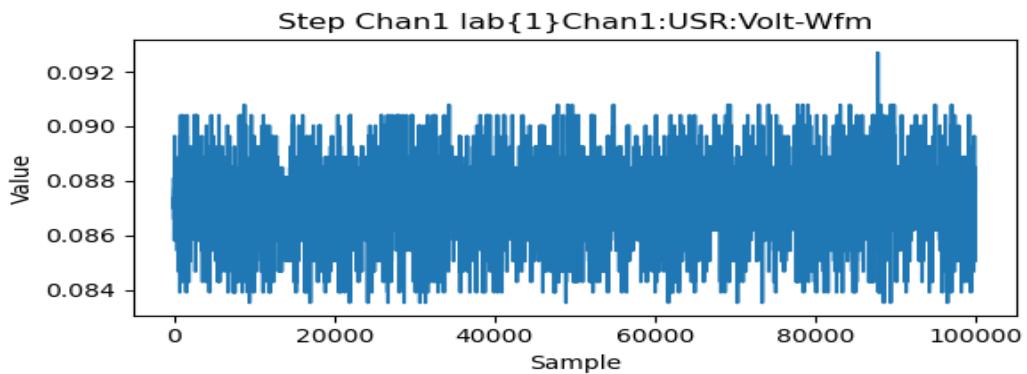
Step Chan1 PV: lab{1}Chan1:USR:DCCT2-Wfm



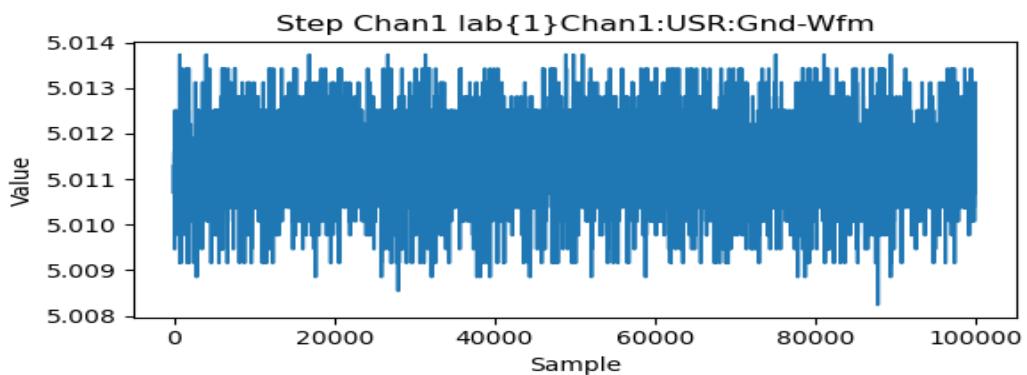
Step Chan1 PV: lab{1}Chan1:USR:DAC-Wfm



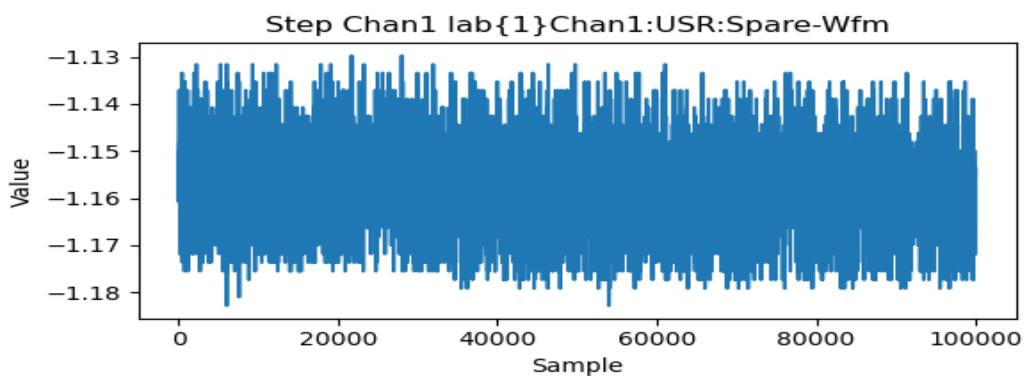
Step Chan1 PV: lab{1}Chan1:USR:Volt-Wfm



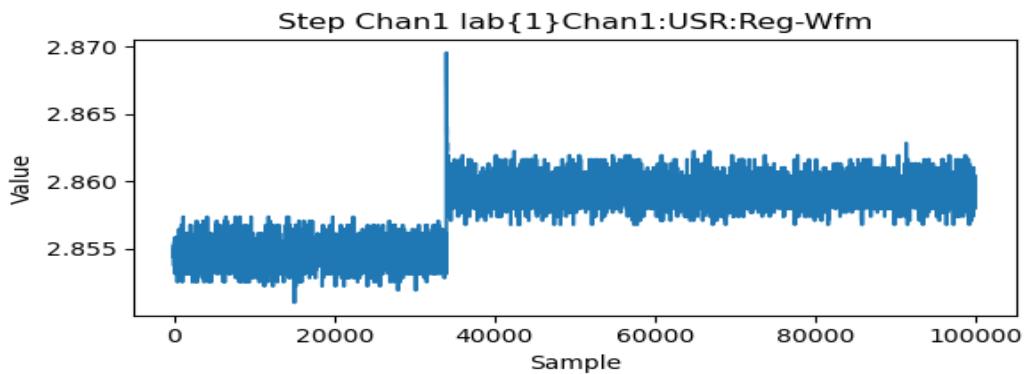
Step Chan1 PV: lab{1}Chan1:USR:Gnd-Wfm



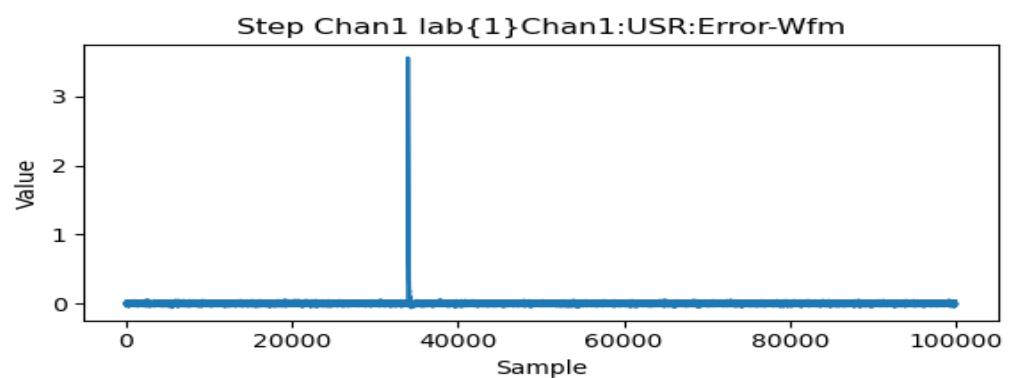
Step Chan1 PV: lab{1}Chan1:USR:Spare-Wfm



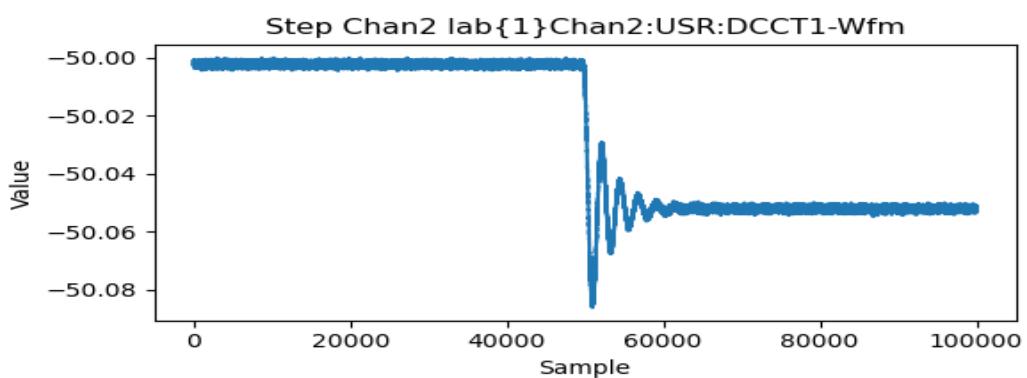
Step Chan1 PV: lab{1}Chan1:USR:Reg-Wfm



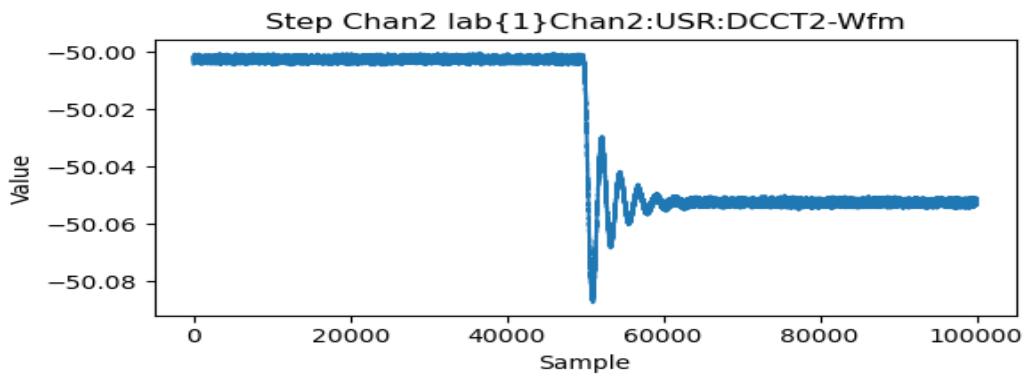
Step Chan1 PV: lab{1}Chan1:USR:Error-Wfm



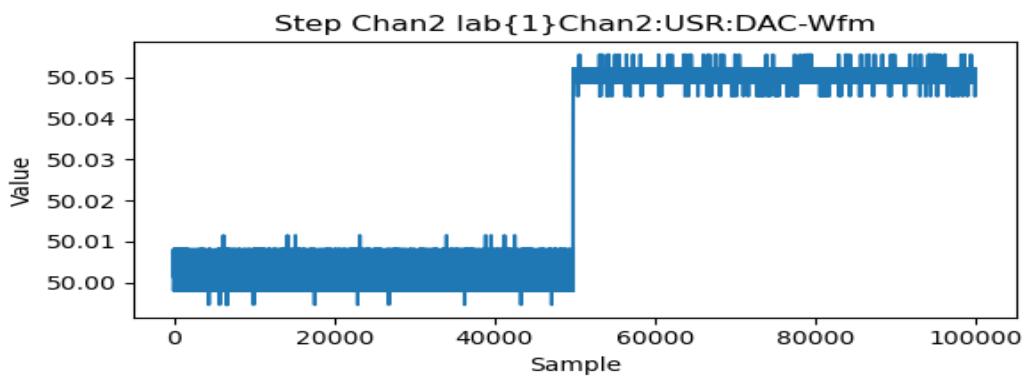
Step Chan2 PV: lab{1}Chan2:USR:DCCT1-Wfm



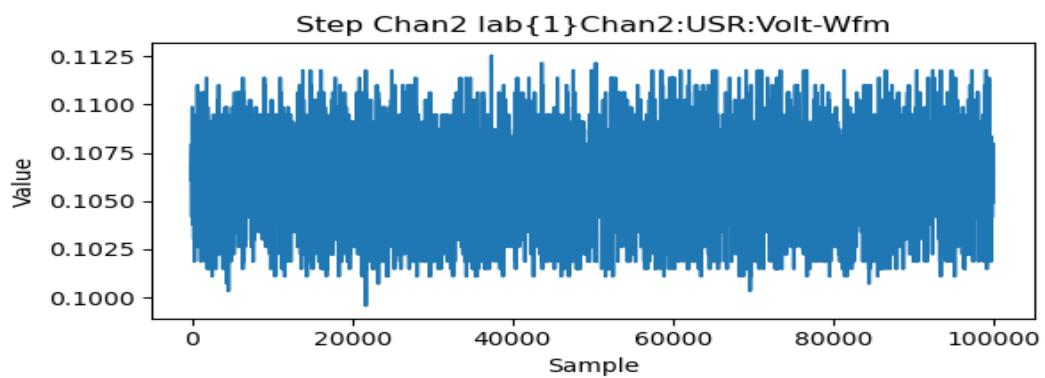
Step Chan2 PV: lab{1}Chan2:USR:DCCT2-Wfm



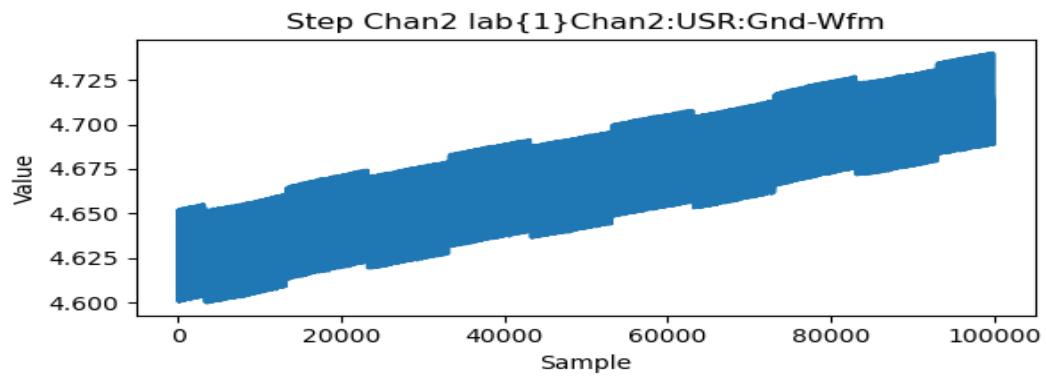
Step Chan2 PV: lab{1}Chan2:USR:DAC-Wfm



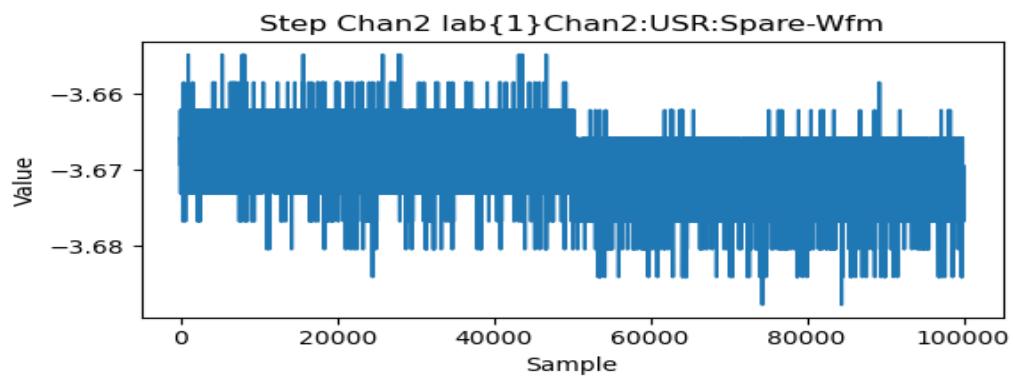
Step Chan2 PV: lab{1}Chan2:USR:Volt-Wfm



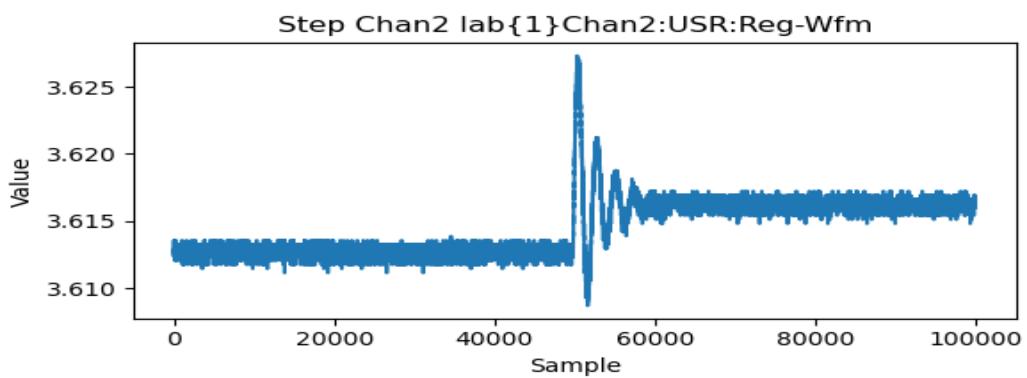
Step Chan2 PV: lab{1}Chan2:USR:Gnd-Wfm



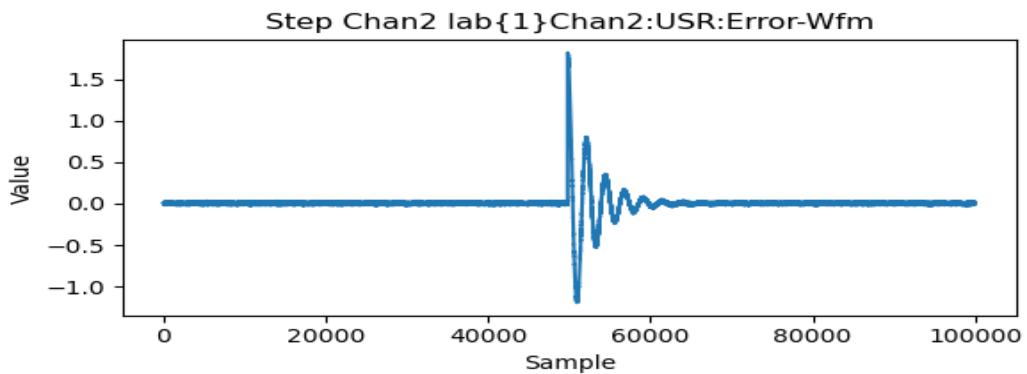
Step Chan2 PV: lab{1}Chan2:USR:Spare-Wfm



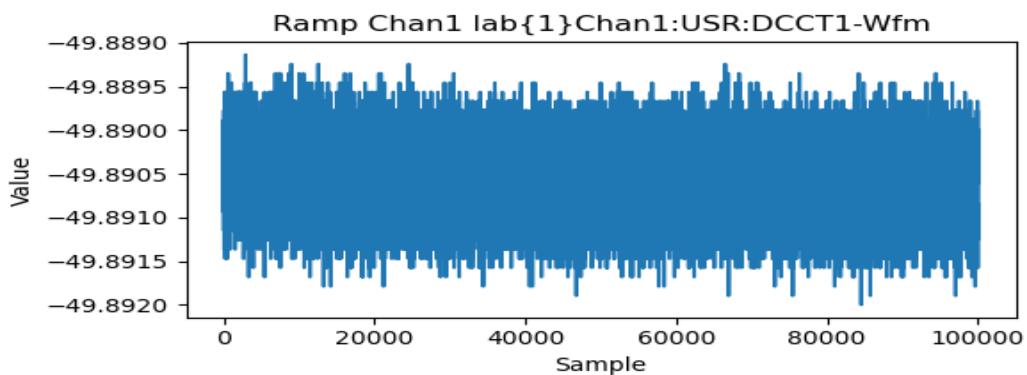
Step Chan2 PV: lab{1}Chan2:USR:Reg-Wfm



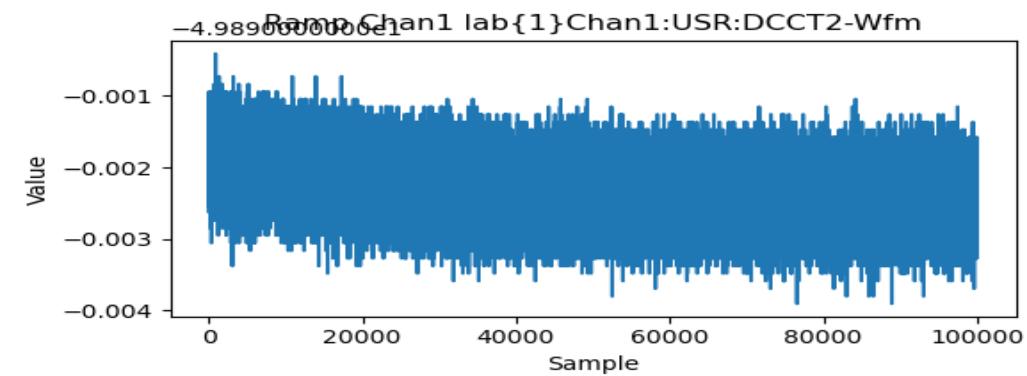
Step Chan2 PV: lab{1}Chan2:USR:Error-Wfm



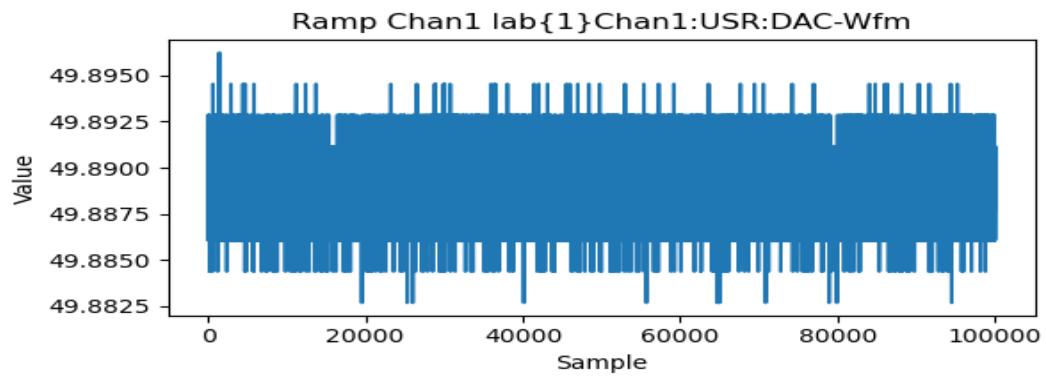
Ramp Chan1 PV: lab{1}Chan1:USR:DCCT1-Wfm



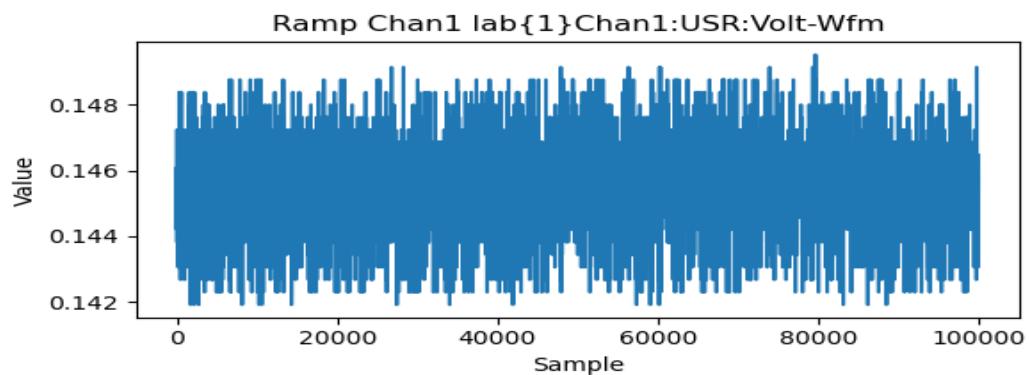
Ramp Chan1 PV: lab{1}Chan1:USR:DCCT2-Wfm



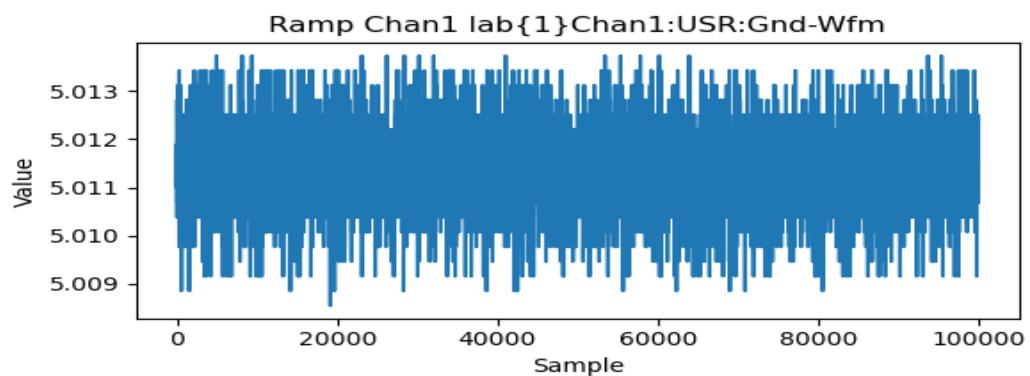
Ramp Chan1 PV: lab{1}Chan1:USR:DAC-Wfm



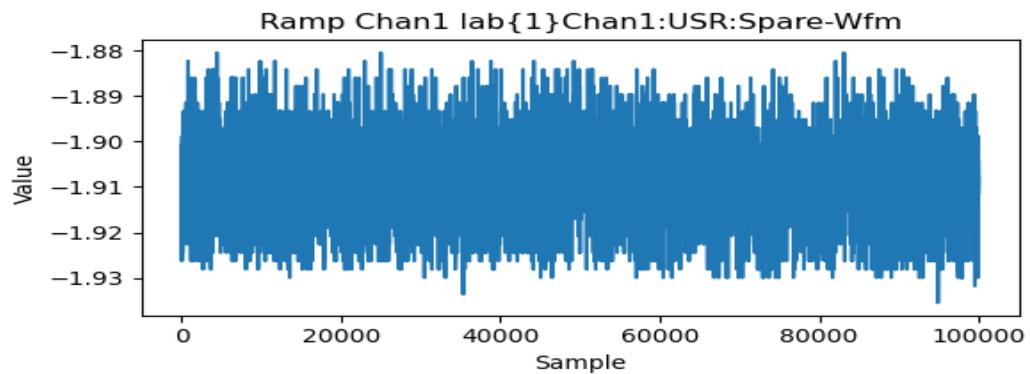
Ramp Chan1 PV: lab{1}Chan1:USR:Volt-Wfm



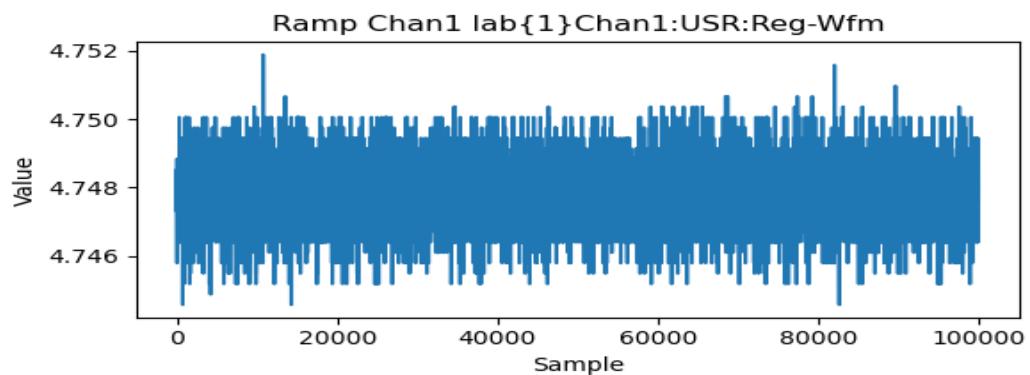
Ramp Chan1 PV: lab{1}Chan1:USR:Gnd-Wfm



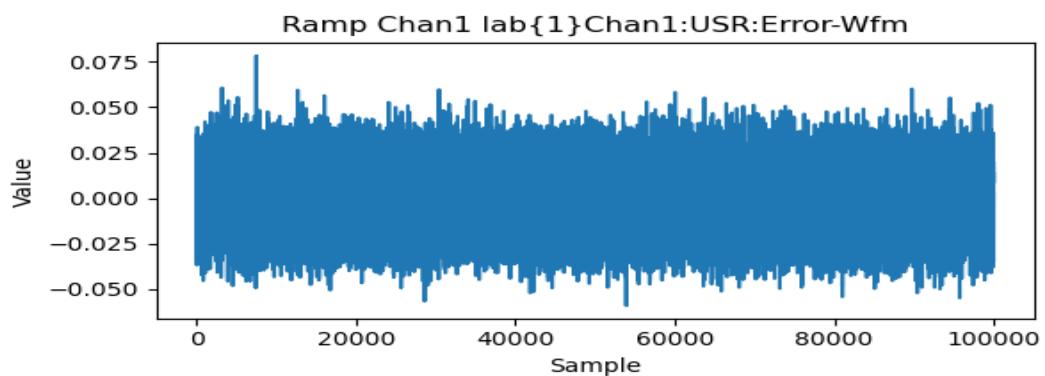
Ramp Chan1 PV: lab{1}Chan1:USR:Spare-Wfm



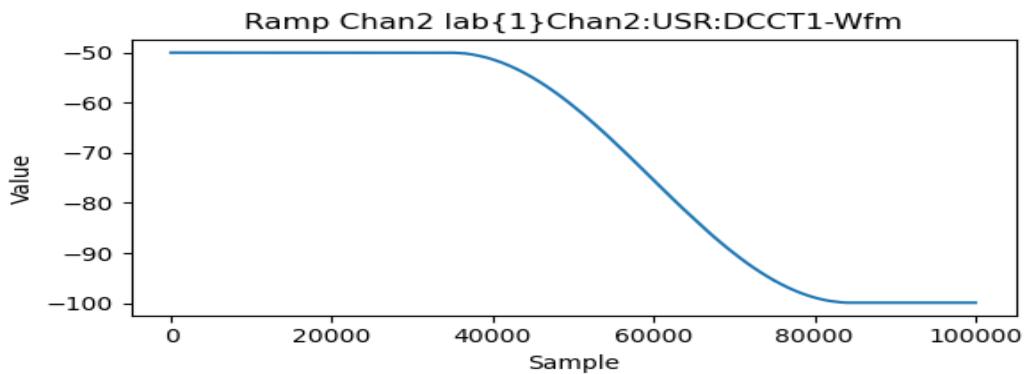
Ramp Chan1 PV: lab{1}Chan1:USR:Reg-Wfm



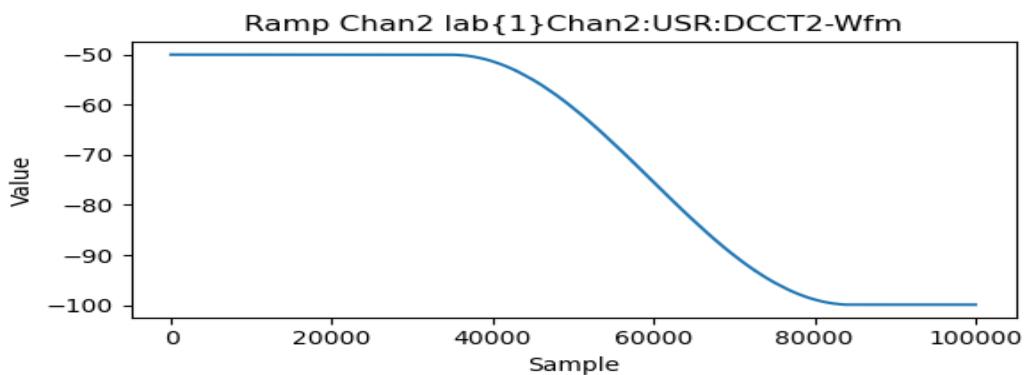
Ramp Chan1 PV: lab{1}Chan1:USR:Error-Wfm



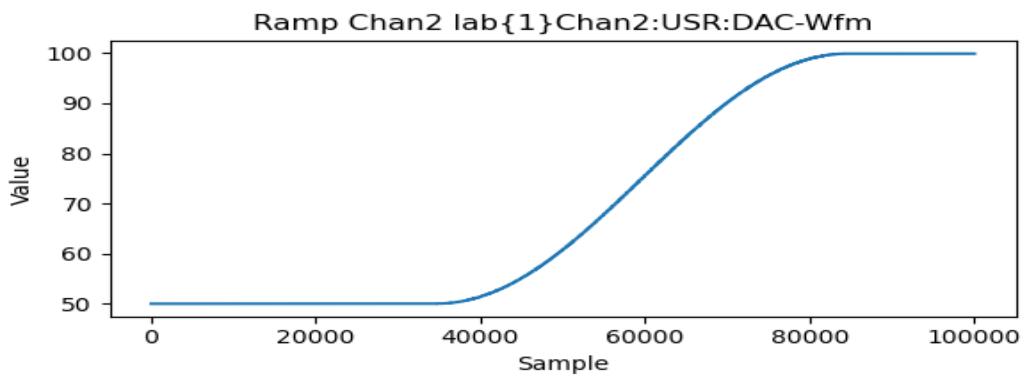
Ramp Chan2 PV: lab{1}Chan2:USR:DCCT1-Wfm



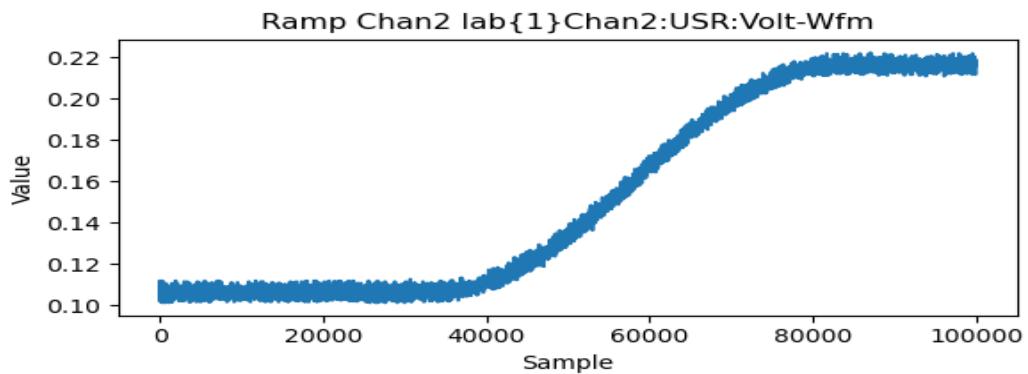
Ramp Chan2 PV: lab{1}Chan2:USR:DCCT2-Wfm



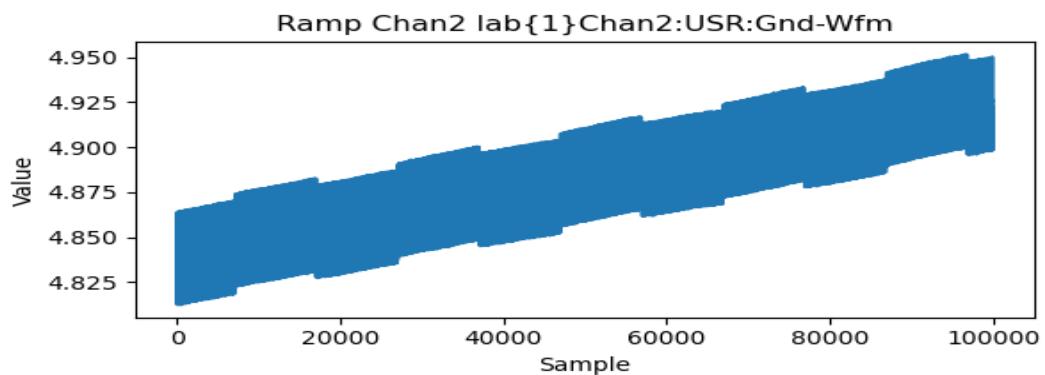
Ramp Chan2 PV: lab{1}Chan2:USR:DAC-Wfm



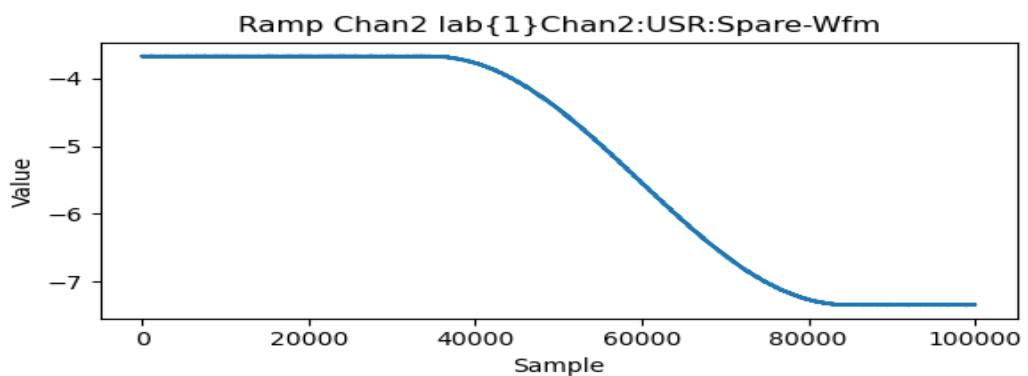
Ramp Chan2 PV: lab{1}Chan2:USR:Volt-Wfm



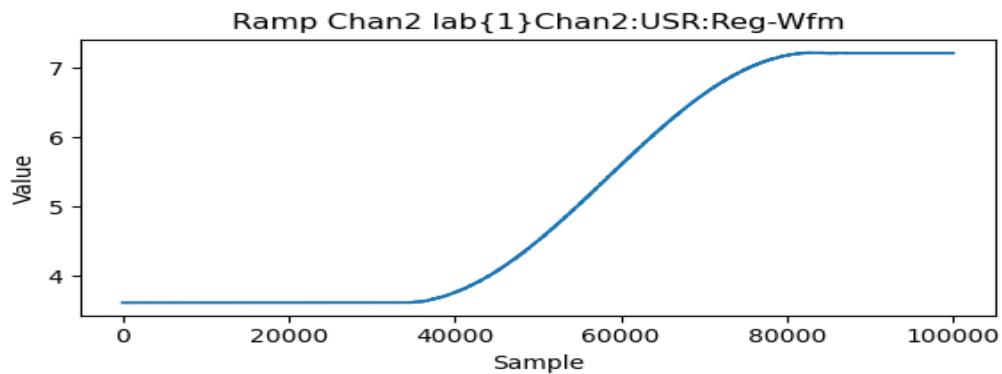
Ramp Chan2 PV: lab{1}Chan2:USR:Gnd-Wfm



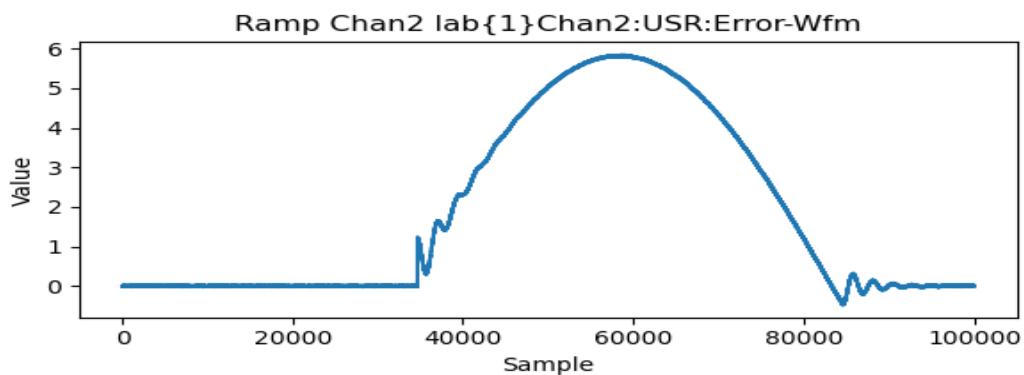
Ramp Chan2 PV: lab{1}Chan2:USR:Spare-Wfm



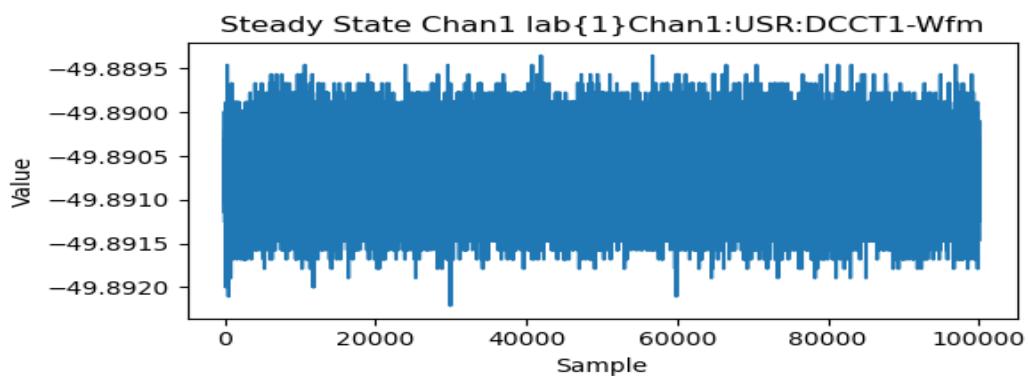
Ramp Chan2 PV: lab{1}Chan2:USR:Reg-Wfm



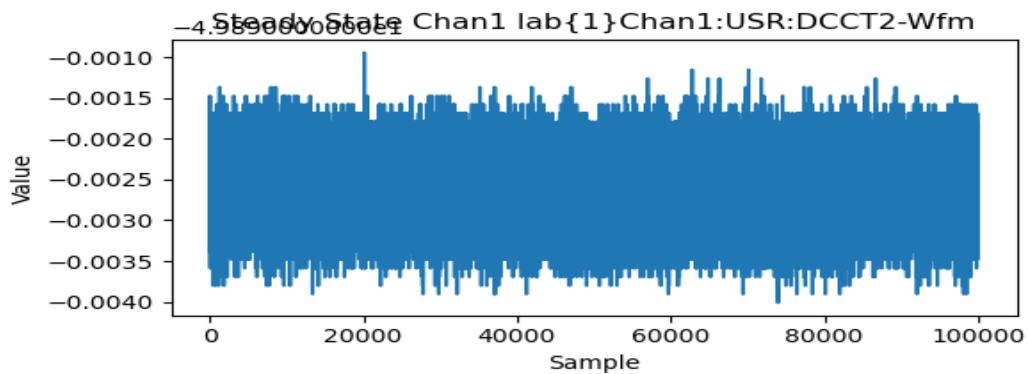
Ramp Chan2 PV: lab{1}Chan2:USR:Error-Wfm



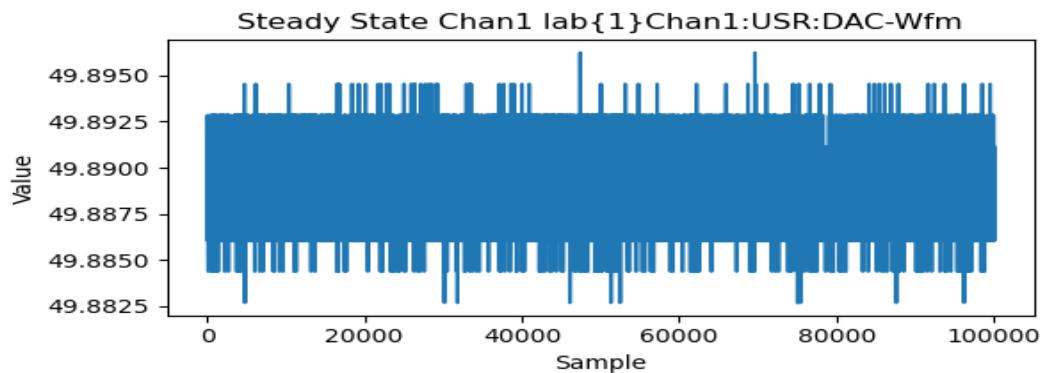
Steady State Chan1 PV: lab{1}Chan1:USR:DCCT1-Wfm



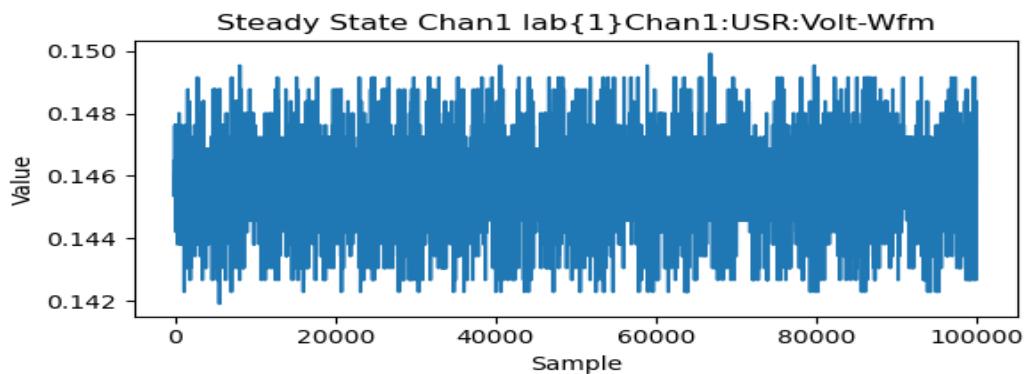
Steady State Chan1 PV: lab{1}Chan1:USR:DCCT2-Wfm



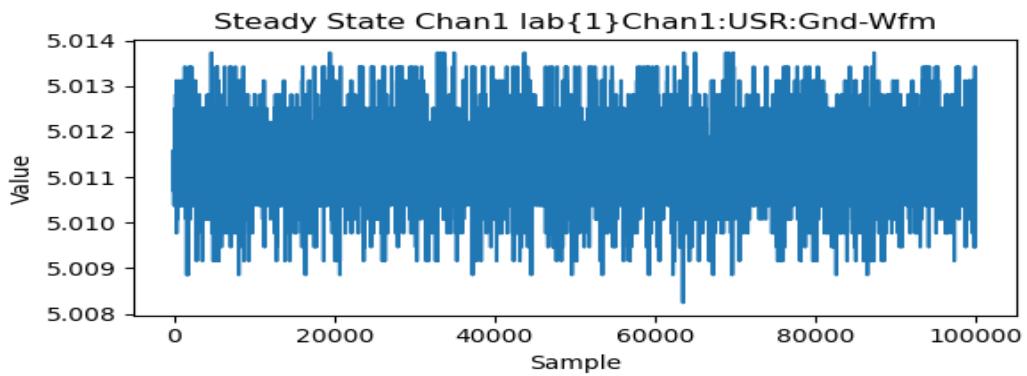
Steady State Chan1 PV: lab{1}Chan1:USR:DAC-Wfm



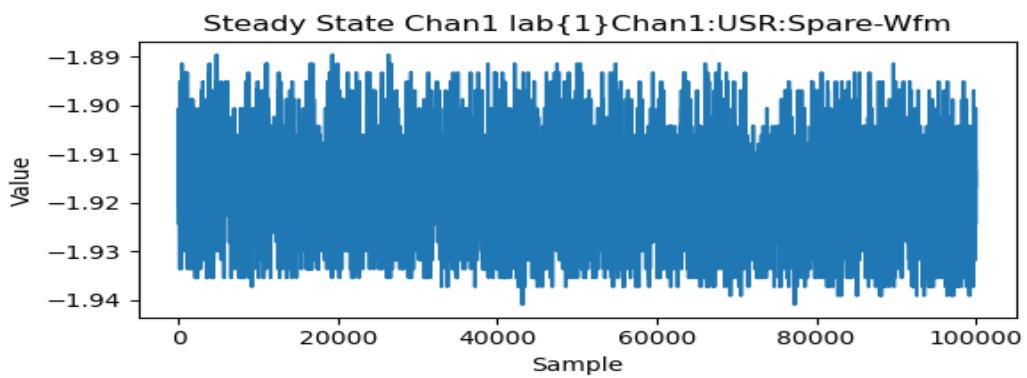
Steady State Chan1 PV: lab{1}Chan1:USR:Volt-Wfm



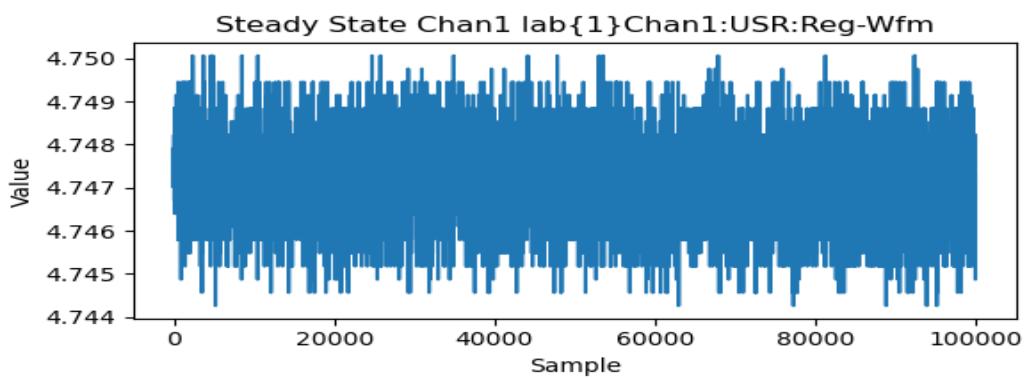
Steady State Chan1 PV: lab{1}Chan1:USR:Gnd-Wfm



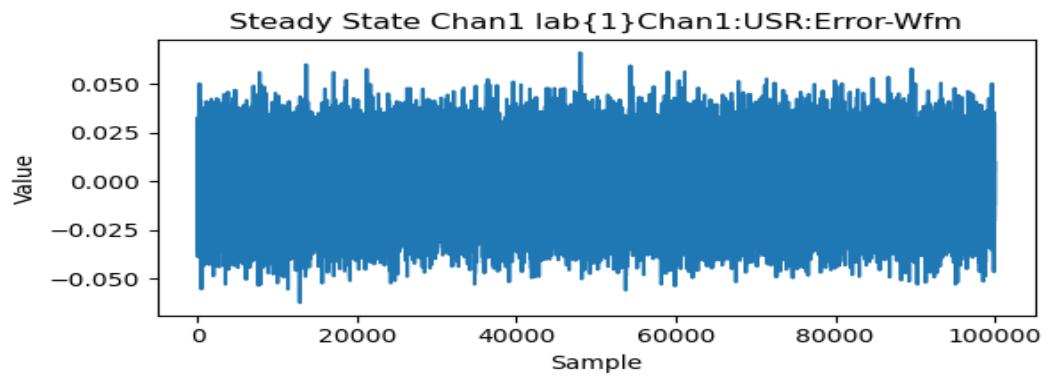
Steady State Chan1 PV: lab{1}Chan1:USR:Spare-Wfm



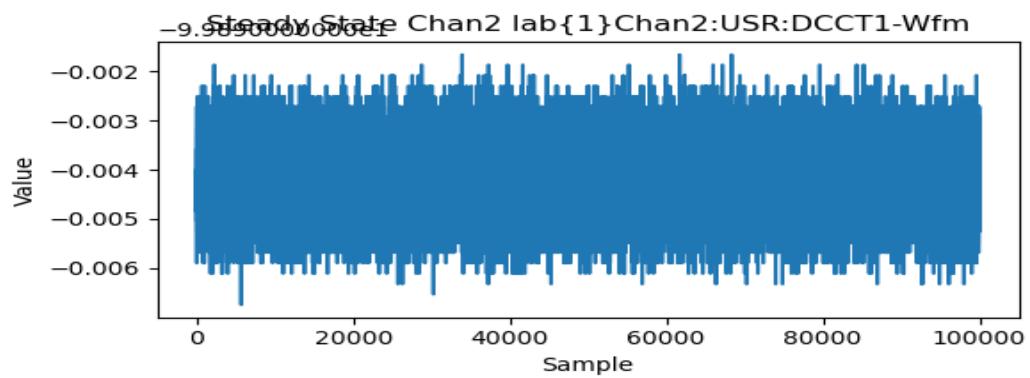
Steady State Chan1 PV: lab{1}Chan1:USR:Reg-Wfm



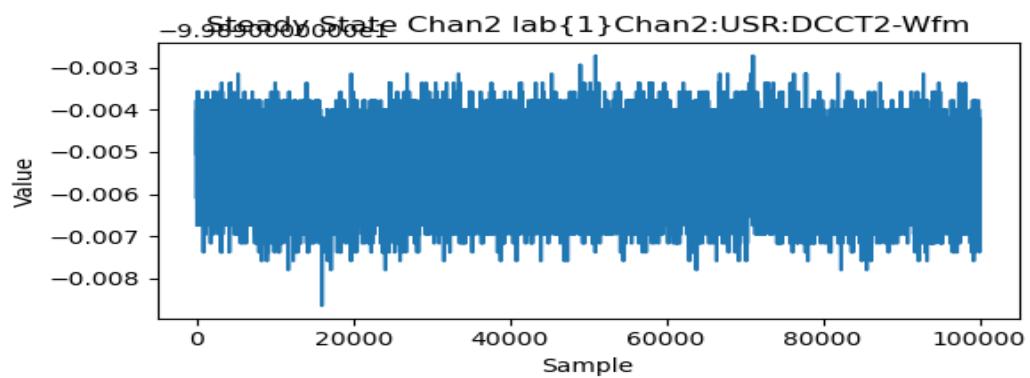
Steady State Chan1 PV: lab{1}Chan1:USR>Error-Wfm



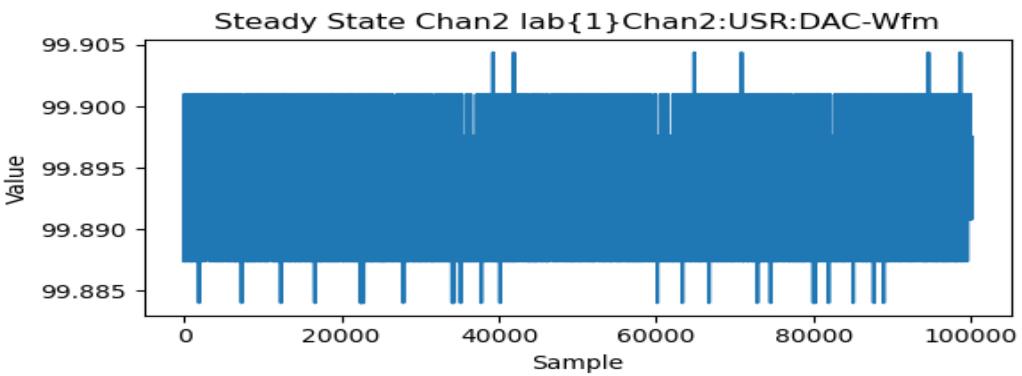
Steady State Chan2 PV: lab{1}Chan2:USR:DCCT1-Wfm



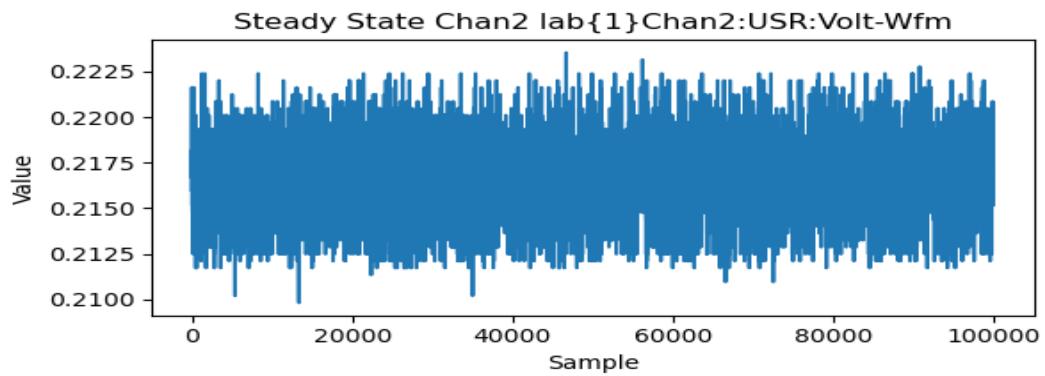
Steady State Chan2 PV: lab{1}Chan2:USR:DCCT2-Wfm



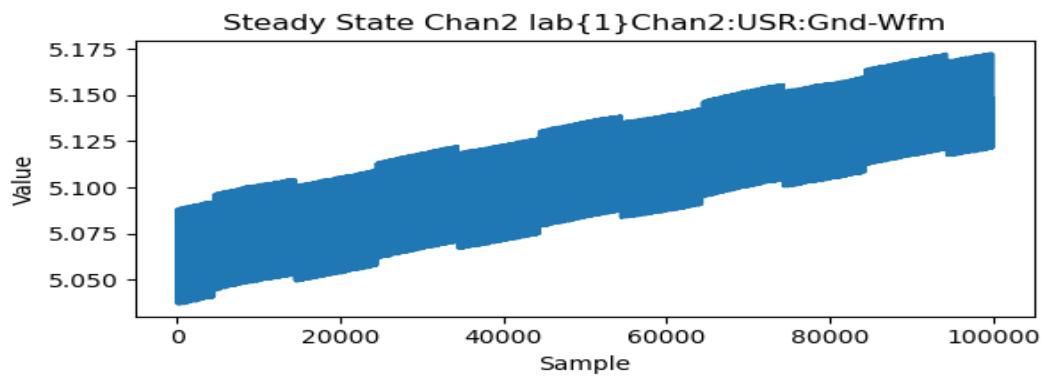
Steady State Chan2 PV: lab{1}Chan2:USR:DAC-Wfm



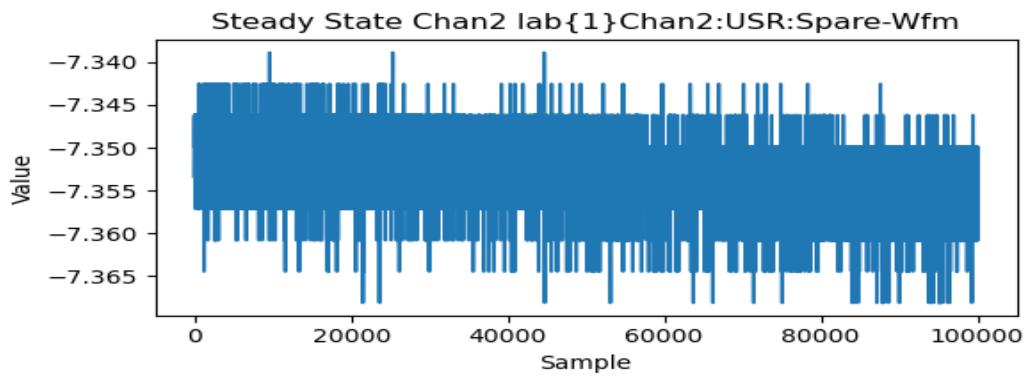
Steady State Chan2 PV: lab{1}Chan2:USR:Volt-Wfm



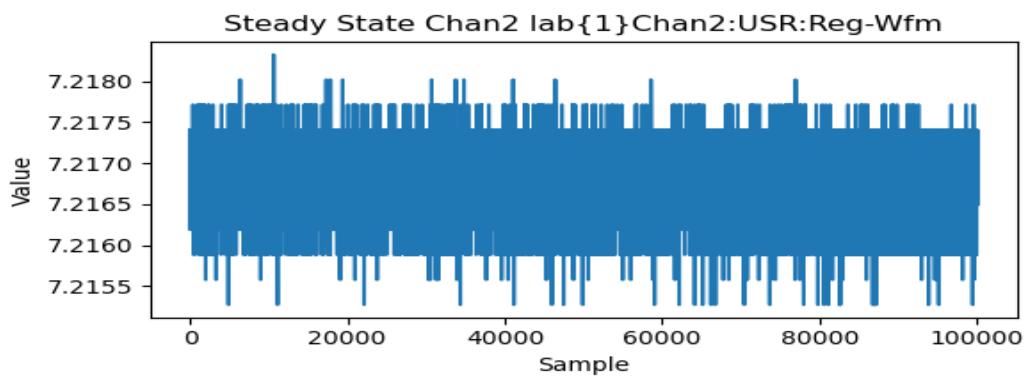
Steady State Chan2 PV: lab{1}Chan2:USR:Gnd-Wfm



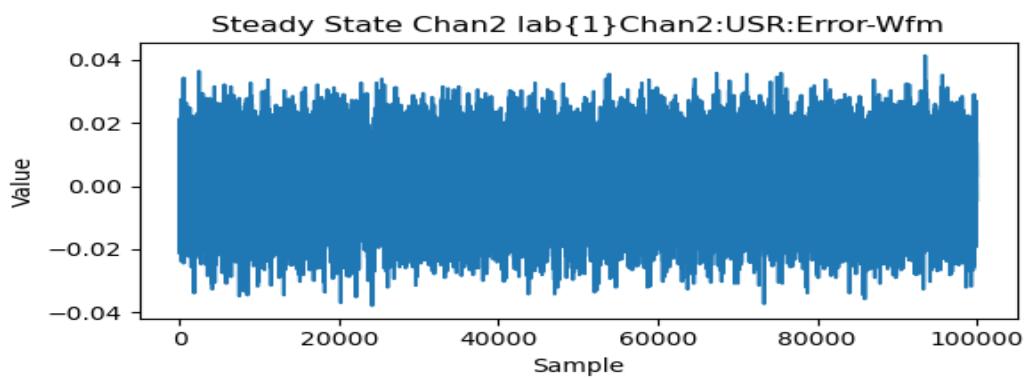
Steady State Chan2 PV: lab{1}Chan2:USR:Spare-Wfm



Steady State Chan2 PV: lab{1}Chan2:USR:Reg-Wfm



Steady State Chan2 PV: lab{1}Chan2:USR:Error-Wfm



**FOFB test: SKIPPED (Bandwidth-Mode != Fast)**