

PX30 I/O LIST

版本	更新时间	修改内容										修改人
V1.0	2018.04.27	正式版										XHF
PX30 Pin No.	PX30 Pin Name	Pin Type	I/O Def	I/O Pull	Default Drive (mA)	Func 1	Func 2	Func 3	Func 4	Func5	Default function	Description
PART I												
W19	NPOR	I	I	up	4	NPOR					RESET	System reset input
W20	TVSS	I	I	down	4	TEST					VSS	Enter into test mode,connect to VSS default
U20	OSC_24M_IN	I	I	N/A	4	OSC_24M_IN					24MXIN_OSC	Oscillator 24MHz clock input
U21	OSC_24M_OUT	O	O	N/A	4	OSC_24M_OUT					24MXOUT_OSC	Oscillator 24MHz clock output
Y21	GPIO0_A0/REF_CLKO	I/O	I	down	4	GPIO0A0	clk_out_wifi				REF_CLKO	Reference clock output
Y20	GPIO0_A1	I/O	I	down	4	GPIO0A1					HOST_WAKE_BT	AP wake up BT module
V21	GPIO0_A2	I/O	I	down	4	GPIO0A2					WIFI_REG_ON	WIFI module wake up AP
AA20	GPIO0_A3/SDMMC0_DET	I/O	I	up	4	GPIO0A3	sdmmc0_detn				SDMMC0_DET	SDMMC0 power control output
V20	GPIO0_A4/PMIC_SLEEP/TSADC_SHUT_M1	I/O	I	down	4	GPIO0A4	pmic_sleep	tsadc_shutm1			PMIC_SLEEP	PMIC sleep control output
T21	GPIO0_A5	I/O	I	up	4	GPIO0A5					TP_INT	Touch pannel interrupt
W21	GPIO0_A6/TSADC_SHUT_M0/TSADC_SHUTOR	I/O	I	Z	4	GPIO0A6	tsadc_shutm0	tsadc_shutorg			TSADC_SHUT	Over-temperature protection reset power
Y19	GPIO0_A7	I/O	I	up	4	GPIO0A7					PMIC_INT	PMIC interrupt
N18	PLL_AVDD_1V0	P	N/A	N/A							VDD_1V0	PLL analog power supply
M18	PLL_AVDD_1V8	P	N/A	N/A							VCC_1V8	PLL analog power supply
P17	PMU_VDD_1V0	P	N/A	N/A							VDD_1V0	PMU Post-Driver power supply
T13	OTP_VCC_1V8	P	N/A	N/A							VCC_1V8	PMU digital I/O power supply
U19	PMUIO1	P	N/A	N/A							VCC3V0_PMU	PMUIO1 Post-Driver and digital I/O power supply
N17	AVSS	P	N/A	N/A							VSS	pll analog power ground
PART J												
R21	GPIO0_B0/I2C0_SCL	I/O	I	up	4	GPIO0B0	i2c0_scl				I2C0_SCL_PMIC	I2C serial port 0,for PMIC,need external pull-up
P21	GPIO0_B1/I2C0_SDA	I/O	I	up	4	GPIO0B1	i2c0_sda				I2C0_SDA_PMIC	I2C serial port 0,for PMIC,need external pull-up
N20	GPIO0_B2/UART0_TX	I/O	I	down	4	GPIO0B2	uart0_tx				WIFI_WAKE_HOST	WIFI module wake up AP
P18	GPIO0_B3/UART0_RX	I/O	I	down	4	GPIO0B3	uart0_rx				BT_WAKE_HOST	BT module wake up AP
R18	GPIO0_B4/UART0_CTS	I/O	I	up	4	GPIO0B4	uart0_cts				TP_RST	Touch pannel reset
N19	GPIO0_B5/UART0_RTS/TEST_CLK1	I/O	I	up	4	GPIO0B5	uart0_rts	test_clk1			CHG_DET	LCD panel power enable
M19	GPIO0_B7/PWM0/OTG_DRV	I/O	I	down	4	GPIO0B7	pwm_0	otg_drv			SENSOR_INT	G-sensor interrupt
N21	GPIO0_C0/UART3_TX_M0/PWM1/PMU_DEBUG	I/O	I	down	4	GPIO0C0	pwm_1	uart3_txm0			LCDC_BL_PWM	LCD panel backlight brightness control

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P19	GPIO0_C1/UART3_RX_M0/PWM3/PMU_DEBUG4	I/O	I	up	4	GPIO0C1	pwm_3	uart3_rxm0			IR_IN	IR_IN
T20	GPIO0_C2/I2C1_SCL/U	I/O	I	down	4	GPIO0C2	i2c1_scl	uart3_ctsm			I2C1_SCL	I2C serial port 1,for TP/Sensor,need
R20	GPIO0_C3/I2C1_SDA/UART3_RTS_M0/PMU_DEBUG5	I/O	I	down	4	GPIO0C3	i2c1_sda	uart3_rtsm0			I2C1_SDA	I2C serial port 1,for TP/Sensor,need external pull-up
P20	GPIO0_C4/CLKIO_32K	I/O	I	Z	4	GPIO0C4	clk_inout_32k				CLKOUT_32K	32KHz real time clock input or output
R19	FLASH_VOLSEL	I/O	I	up	4	GPIO0B6	flash_vol_sel				FLASH_VOLSEL	Nand Flash default power supply voltage select for boot
T19	PMUIO2	P	N/A	N/A							VCC3V0_PMU	PMUIO2 digital I/O power supply
PART B												
AA5	GPIO2_A0/CIF_D2_M0/RMII_TXEN	I/O	I	down	4	GPIO2A0	cif_d2m0	rmii_txen			RMII_TXEN	Camera data port
AA8	GPIO2_A1/CIF_D3_M0/RMII_TXD1	I/O	I	down	4	GPIO2A1	cif_d3m0	rmii_txd1			RMII_TXD1	Camera data port
AA7	GPIO2_A2/CIF_D4_M0/RMII_TXD0	I/O	I	down	4	GPIO2A2	cif_d4m0	rmii_txd0			RMII_TXD0	Camera data port
Y6	GPIO2_A3/CIF_D5_M0/RMII_RXD0	I/O	I	down	4	GPIO2A3	cif_d5m0	rmii_rxd0			RMII_RXD0	Camera data port
Y8	GPIO2_A4/CIF_D6_M0/RMII_RXD1	I/O	I	down	4	GPIO2A4	cif_d6m0	rmii_rxd1			RMII_RXD1	Camera data port
Y7	GPIO2_A5/CIF_D7_M0/RMII_RXER	I/O	I	down	4	GPIO2A5	cif_d7m0	rmii_rxer			RMII_RXER	Camera data port
W5	GPIO2_A6/CIF_D8_M0/RMII_RXDV	I/O	I	down	4	GPIO2A6	cif_d8m0	rmii_rxdv			RMII_RXDV	Camera data port
W7	GPIO2_A7/CIF_D9_M0/RMII_MDIO	I/O	I	down	4	GPIO2A7	cif_d9m0	rmii_mdio			RMII_MDIO	Camera data port
U7	GPIO2_B7/CIF_D10_M0/I2C2_SCL	I/O	I	up	4	GPIO2B7	cif_d10m0	i2c2_scl			I2C2_SCL	Camera data port, I2C serial port 1,for camera,need external pull-up
V6	GPIO2_C0/CIF_D11_M0/I2C2_SDA	I/O	I	up	4	GPIO2C0	cif_d11m0	i2c2_sda			I2C2_SDA	Camera data port, I2C serial port 1,for camera,need external pull-up
Y4	GPIO2_B0/CIF_VSYNC_M0	I/O	I	down	4	GPIO2B0	cif_vsyncm0				BT_REG_ON	Camera vsync input
AA4	GPIO2_B1/CIF_HREF_M0/RMII_MDC	I/O	I	down	4	GPIO2B1	cif_hrefm0	rmii_mdc			RMII_MDC	Camera href input
AA6	GPIO2_B2/CIF_CLKI_M0/RMII_CLK	I/O	I	down	4	GPIO2B2	cif_clkim0	rmii_clk			RMII_CLK	Camera clock input
Y5	GPIO2_B3/CIF_CLKO_M0/CLK_OUT_ETHERNET	I/O	I	down	4	GPIO2B3	cif_clkom0	clk_out_ethernet			MIPI_CLKO	Camera clock output
V12	GPIO2_B4/CIF_D0_M0/UART2_TX_M1	I/O	I	down	4	GPIO2B4	cif_d0m0	uart2_txm1			GPIO2_B4	Camera data port

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V7	GPIO2_B5/PWM2	I/O	I	down	4	GPIO2B5	pwm_2				RMII_RST	Camera power down control output for front
W6	GPIO2_B6/CIF_D1_M0/UART2_RX_M1	I/O	I	up	4	GPIO2B6	cif_d1m0	uart2_rxm1			CIF_PDN0	Camera data port
U6	VCCIO3	P	N/A	N/A							VCC_RMII	
PART K												
J20	GPIO1_C0/UART1_RX	I/O	I	up	8	GPIO1C0	uart1_rx				UART1_RXD	UART0 serial port, for BT module
K20	GPIO1_C1/UART1_TX	I/O	I	up	8	GPIO1C1	uart1_tx				UART1_TXD	UART0 serial port, for BT module
K21	GPIO1_C2/UART1_CTS	I/O	I	up	8	GPIO1C2	uart1_cts				UART1_CTS	UART0 serial port, for BT module
L21	GPIO1_C3/UART1_RTS	I/O	I	up	8	GPIO1C3	uart1_rts				UART1_RTS	UART0 serial port, for BT module
H19	GPIO1_C4/SDMMC1_C MD	I/O	I	up	8	GPIO1C4	sdmmc1_cmd				SDIO_CMD	SDIO0 command output,for WIFI module
M20	GPIO1_C5/SDMMC1_CLK	I/O	I	up	8	GPIO1C5	sdmmc1_clk				SDIO_CLK	SDIO0 clock output,for WIFI module
L20	GPIO1_C6/SDMMC1_D0	I/O	I	up	8	GPIO1C6	sdmmc1_d0				SDIO_D0	SDIO0 data port ,for WIFI module
L19	GPIO1_C7/SDMMC1_D1	I/O	I	up	8	GPIO1C7	sdmmc1_d1				SDIO_D1	SDIO0 data port ,for WIFI module
M21	GPIO1_D0/SDMMC1_D2	I/O	I	up	8	GPIO1D0	sdmmc1_d2				SDIO_D2	SDIO0 data port ,for WIFI module
J19	GPIO1_D1/SDMMC1_D3	I/O	I	up	8	GPIO1D1	sdmmc1_d3				SDIO_D3	SDIO0 data port ,for WIFI module
K19	VCCIO1	P	N/A	N/A							VCC3V3_WL	APIO3 Post-Driver power supply
PART M												
B16	LVDS_TX0N/MIPI_TX_D0N/LCDC_D11_M1	A	N/A	N/A							LVDS_TX0N/MIPI_TX_D0N	MIPI-DSI0 differential lane 0 positive
B17	LVDS_TX0P/MIPI_TX_D0P/LCDC_D8_M1	A	N/A	N/A							LVDS_TX0N/MIPI_TX_D0P	MIPI-DSI0 differential lane 0 negative
A16	LVDS_TX1P/MIPI_TX_D1P/LCDC_D10_M1	A	N/A	N/A							LVDS_TX1P/MIPI_TX_D1P	MIPI-DSI0 differential lane 1 positive
B15	LVDS_TX1N/MIPI_TX_D1N/LCDC_D1_M1	A	N/A	N/A							LVDS_TX1N/MIPI_TX_D1N	MIPI-DSI0 differential lane 1 negative
A14	LVDS_CLKP/MIPI_TX_CLKP/LCDC_D3_M1	A	N/A	N/A							LVDS_CLKP/MIPI_TX_CLKP	MIPI-DSI0 differential clock lane positive
B14	LVDS_CLKN/MIPI_TX_CLKN/LCDC_D4_M1	A	N/A	N/A							LVDS_CLKN/MIPI_TX_CLKN	MIPI-DSI0 differential clock lane negative
B13	LVDS_TX2P/MIPI_TX_D2P/LCDC_D5_M1	A	N/A	N/A							LVDS_TX2P/MIPI_TX_D2P	MIPI-DSI0 differential lane 2 positive
C13	LVDS_TX2N/MIPI_TX_D2N/LCDC_VSYNC_M1	A	N/A	N/A							LVDS_TX2N/MIPI_TX_D2N	MIPI-DSI0 differential lane 2 negative

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A12	LVDS_TX3P/MIPI_TX_D3P/LCDC_DEN_M1	A	N/A	N/A							LVDS_TX3P/MIPI_TX_D3P	MIPI-DSI0 differential lane 3 positive
B12	LVDS_TX3N/MIPI_TX_D3N/LCDC_HSYNC_M1	A	N/A	N/A							LVDS_TX3N/MIPI_TX_D3N	MIPI-DSI0 differential lane 3 negative
D19	GPIO3_A0/LCDC_CLK	I/O	I	down	4	GPIO3A0	lcdc_clk				lcdc_clk	LCDC pixel clk output
E13	GPIO3_A1/LCDC_HSYNC_M0/CIF_D0_M1/I2S2_2CH_MCLK/UART5_RX	I/O	I	down	4	GPIO3A1	lcdc_hsyncm0	i2s2_2ch_mclk	cif_d0m1	uart5_rx	GPIO3_A1/LCDC_HSYNC_M0/CIF_D0_M1/I2S2_2CH_MCLK/UART5_RX	
F13	GPIO3_A2/LCDC_VSYNC_M0/CIF_D1_M1/I2S2_2CH_SCLK/UART5_TX	I/O	I	down	4	GPIO3A2	lcdc_vsyncm0	i2s2_2ch_sclk	cif_d1m1	uart5_tx	GPIO3_A2/LCDC_VSYNC_M0/CIF_D1_M1/I2S2_2CH_SCLK/UART5_TX	I2S2 port,for BT module
E14	GPIO3_A3/LCDC_DEN_M0/CIF_D2_M1/I2S2_2CH_LRCK/UART5_CTS	I/O	I	down	4	GPIO3A3	lcdc_denm0	i2s2_2ch_lrck	cif_d2m1	uart5_cts	GPIO3_A3/LCDC_DEN_M0/CIF_D2_M1/I2S2_2CH_LRCK/UART5_CTS	I2S2 port,for BT module
C15	GPIO3_A4/LCDC_D0	I/O	I	down	4	GPIO3A4	lcdc_d0				GPIO3_A4/LCDC_D0	LCDC data port
E15	GPIO3_A5/LCDC_D1_M0/CIF_D3_M1/I2S2_2CH_SDI/UART5_RTX	I/O	I	down	4	GPIO3A5	lcdc_d1m0	i2s2_2ch_sdi	cif_d3m1	uart5_rts	GPIO3_A5/LCDC_D1_M0/CIF_D3_M1/I2S2_2CH_SDI/UART5_RTX	I2S2 port,for BT module
C14	GPIO3_A6/LCDC_D2	I/O	I	down	4	GPIO3A6	lcdc_d2				GPIO3_A6/LCDC_D2	LCDC data port
E16	GPIO3_A7/LCDC_D3_M0/CIF_D4_M1/I2S2_2CH_SDO	I/O	I	down	4	GPIO3A7	lcdc_d3m0	i2s2_2ch_sdo	cif_d4m1		GPIO3_A7/LCDC_D3_M0/CIF_D4_M1/I2S2_2CH_SDO	I2S2 port,for BT module
E17	GPIO3_B0/LCDC_D4_M0/CIF_D5_M1/I2S0_8CH_SDI3	I/O	I	down	4	GPIO3B0	lcdc_d4m0	i2s0_8ch_sdi3	cif_d5m1		GPIO3_B0/LCDC_D4_M0/CIF_D5_M1/I2S0_8CH_SDI3	Power enable for RK618
F17	GPIO3_B1/LCDC_D5_M0/CIF_D6_M1/I2S0_8CH_SDI2/SPI1_CSN	I/O	I	down	4	GPIO3B1	lcdc_d5m0	i2s0_8ch_sdi2	cif_d6m1	spi1_csn	GPIO3_B1/LCDC_D5_M0/CIF_D6_M1/I2S0_8CH_SDI2/SPI1_CSN	Reset for RK618
B18	GPIO3_B2/LCDC_D6	I/O	I	down	4	GPIO3B2	lcdc_d6	spi1_csn1			GPIO3_B2/LCDC_D6	LCDC data port
C17	GPIO3_B3/LCDC_D7/I2S0_8CH_SDI1	I/O	I	down	4	GPIO3B3	lcdc_d7	i2s0_8ch_sdi1			GPIO3_B3/LCDC_D7/I2S0_8CH_SDI1	LCDC data port
F18	GPIO3_B4/LCDC_D8_M0/CIF_D7_M1/I2S0_8CH_SCLKRX/SPI1_MOSI	I/O	I	down	4	GPIO3B4	lcdc_d8m0	i2s0_8ch_sclkrx	cif_d7m1	spi1_mosi	GPIO3_B4/LCDC_D8_M0/CIF_D7_M1/I2S0_8CH_SCLKRX/SPI1_MOSI	Compass interrupt

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C16	GPIO3_B5/LCDC_D9_M0/I2S0_8CH_LRCKRX	I/O	I	down	4	GPIO3B5	lcdc_d9m0	i2s0_8ch_lrckrx			GPIO3_B5/LCDC_D9_M0/I2S0_8CH_LRCKRX	LCDC data port
G18	GPIO3_B6/LCDC_D10_M0/CIF_D8_M1/I2S0_8CH_SDO3/SPI1_RXD	I/O	I	down	4	GPIO3B6	lcdc_d10m0	i2s0_8ch_sdo3	cif_d8m1	spi1_miso	GPIO3_B6/LCDC_D10_M0/CIF_D8_M1/I2S0_8CH_SDO3/SPI1_RXD	WIFI module power enable
G17	GPIO3_B7/LCDC_D11_M0/CIF_D9_M1/I2S0_8CH_SDO2/SPI1_CLK	I/O	I	down	4	GPIO3B7	lcdc_d11m0	i2s0_8ch_sdo2	cif_d9m1	spi1_clk	GPIO3_B7/LCDC_D11_M0/CIF_D9_M1/I2S0_8CH_SDO2/SPI1_CLK	LCD panel reset
A20	GPIO3_C0/LCDC_D12/I2S0_8CH_SDO1	I/O	I	down	4	GPIO3C0	lcdc_d12	i2s0_8ch_sdo1			GPIO3_C0/LCDC_D12/I2S0_8CH_SDO1	LCDC data port
B20	GPIO3_C1/LCDC_D13/I2S0_8CH_MCLK	I/O	I	down	4	GPIO3C1	lcdc_d13	i2s0_8ch_mclk			GPIO3_C1/LCDC_D13/I2S0_8CH_MCLK	LCDC data port
C19	GPIO3_C2/LCDC_D14/PWM4/I2S0_8CH_LRCKTX	I/O	I	down	4	GPIO3C2	lcdc_d14	i2s0_8ch_lrcktx	pwm_4	tdm_fsync	GPIO3_C2/LCDC_D14/PWM4/I2S0_8CH_LRCKTX	LCDC data port
B19	GPIO3_C3/LCDC_D15/PWM5/I2S0_8CH_LRCKTX	I/O	I	down	4	GPIO3C3	lcdc_d15	i2s0_8ch_sclktx	pwm_5	tdm_sclk	GPIO3_C3/LCDC_D15/PWM5/I2S0_8CH_LRCKTX	LCDC data port
C18	GPIO3_C4/LCDC_D16/PWM6/I2S0_8CH_SDO0	I/O	I	down	4	GPIO3C4	lcdc_d16	i2s0_8ch_sdo0	pwm_6	tdm_sdo	GPIO3_C4/LCDC_D16/PWM6/I2S0_8CH_SDO0	LCDC data port
A18	GPIO3_C5/LCDC_D17/PWM7/I2S0_8CH_SDI0	I/O	I	down	4	GPIO3C5	lcdc_d17	i2s0_8ch_sdi0	pwm_7	tdm_sdi	GPIO3_C5/LCDC_D17/PWM7/I2S0_8CH_SDI0	LCDC data port
D13	GPIO3_C6/LCDC_D18/CIF_D10_M1/PDM_CLK0_M0	I/O	I	down	4	GPIO3C6	lcdc_d18	pdm_clk0m0	cif_d10m1		GPIO3_C6/LCDC_D18/CIF_D10_M1/PDM_CLK0_M0	LCDC data port
D14	GPIO3_C7/LCDC_D19/CIF_D11_M1/PDM_CLK1	I/O	I	down	4	GPIO3C7	lcdc_d19	pdm_clk1	cif_d11m1		GPIO3_C7/LCDC_D19/CIF_D11_M1/PDM_CLK1	LCDC data port
D15	GPIO3_D0/LCDC_D20/CIF_CLKOUT_M1/PDM_SDI1	I/O	I	down	4	GPIO3D0	lcdc_d20	pdm_sdi1	cif_clkoutm1		GPIO3_D0/LCDC_D20/CIF_CLKOUT_M1/PDM_SDI1	LCDC data port
D16	GPIO3_D1/LCDC_D21/CIF_VSYNC_M1/PDM_SDI2/ISP_PRELIGHT_TRIG	I/O	I	down	4	GPIO3D1	lcdc_d21	pdm_sdi2	cif_vsyncm1	isp_prelight_trig	GPIO3_D1/LCDC_D21/CIF_VSYNC_M1/PDM_SDI2/ISP_PRELIGHT_TRIG	LCDC data port

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D17	GPIO3_D2/LCDC_D22/CIF_HREF_M1/PDM_SDI3/ISP_FLASH_TRIGOUT	I/O	I	down	4	GPIO3D2	lcdc_d22	pdm_sdi3	cif_hrefm1	isp_flash_trigout	GPIO3_D2/LCDC_D22/CIF_HREF_M1/PDM_SDI3/ISP_FLASH_TRIGOUT	LCDC data port
D18	GPIO3_D3/LCDC_D23/CIF_CLKIN_M1/PDM_SDI0_M0/ISP_FLASH_TRIGIN	I/O	I	down	4	GPIO3D3	lcdc_d23	pdm_sdi0m0	cif_clk1nm1	isp_flash_trigin	GPIO3_D3/LCDC_D23/CIF_CLKIN_M1/PDM_SDI0_M0/ISP_FLASH_TRIGIN	LCDC data port
G12	LVDS_RBIAS	A	N/A	N/A							LVDS_RBIAS	LVDS reference current generate,connect a 2K%1 resistor to VSS
F11	MIPI_DSI_VCCA_1V0	P	N/A	N/A							VDD_1V0	LVDS/MIPI phy analog power supply
F12	MIPI_DSI_VCCA_1V8	P	N/A	N/A							VCC_1V8	LVDS/MIPI phy analog power supply
E12	MIPI_DSI_VCCA_3V3	P	N/A	N/A							VCC_3V0	LVDS/MIPI phy analog power supply
D12	VCCIO4	P	N/A	N/A								VCCIO4 domain digital I/O power supply
PART G											VCC_3V3 mode	
AA15	GPIO2_C1/I2S1_2CH_LRCK_TXRX	I/O	I	down	4	GPIO2C1	i2s1_2ch_lrck				I2S1_LRCK_TXRX	I2S 1 port, for RK817 audio codec
AA14	GPIO2_C2/I2S1_2CH_SCLK	I/O	I	down	4	GPIO2C2	i2s1_2ch_sclk				I2S1_SCLK	I2S 1 port, for RK817 audio codec
Y14	GPIO2_C3/I2S1_2CH_MCLK	I/O	I	down	4	GPIO2C3	i2s1_2ch_mclk				I2S1_MCLK	I2S 1 port, for RK817 audio codec
Y15	GPIO2_C4/I2S1_2CH_SDO	I/O	I	down	4	GPIO2C4	i2s1_2ch_sdo				I2S1_SDO	I2S 1 port, for RK817 audio codec
AA13	GPIO2_C5/I2S1_SDI/PDM_SDI0_M1	I/O	I	down	4	GPIO2C5	i2s1_2ch_sdi	pdm_sdi0m1			I2S1_SDI	I2S 1 port, for RK817 audio codec
W15	GPIO2_C6/PDM_CLK0_M1	I/O	I	down	4	GPIO2C6	pdm_clk0m1				PDM_CLK0	PDM CLK OUT
W16	VCCIO5	P	N/A	N/A							VCC_3V0	VCCIO5 digital I/O power supply
PART H											VCCIO_SD=AUTO	
AA17	GPIO1_D2/SDMMC0_D0/UART2_TX_M0	I/O	I	up	8	GPIO1D2	sdmmc0_d0	uart2_txm0			SDMMC0_D0/UART2_TX_M0	SDMMC0 data port
AA18	GPIO1_D3/SDMMC0_D1/UART2_RX_M0	I/O	I	up	8	GPIO1D3	sdmmc0_d1	uart2_rxm0			SDMMC0_D1/UART2_RX_M0	SDMMC0 data port
AA19	GPIO1_D4/SDMMC0_D2/UART4_RX/JTAG_TCK	I/O	I	up	8	GPIO1D4	sdmmc0_d2	uart4_rx	jtag_tck		SDMMC0_D2/JTAG_TCK	SDMMC0 data port JTAG TCK for AP
AA16	GPIO1_D5/SDMMC0_D3/UART4_TX/JTAG_TMS	I/O	I	up	8	GPIO1D5	sdmmc0_d3	uart4_tx	jtag_tms		SDMMC0_D3/JTAG_TMS	SDMMC0 data port JTAG TMS for AP
Y17	GPIO1_D6/SDMMC0_CLK0/UART4_CTS/TEST_CLK0	I/O	I	down	8	GPIO1D6	sdmmc0_clko	uart4_cts	test_clko		SDMMC0_CLK	SDMMC0 clock output

PX30 I/O LIST

版本	更新时间	修改内容										修改人
V1.0	2018.04.27	正式版										XHF
PX30 Pin No.	PX30 Pin Name	Pin Type	I/O Def	I/O Pull	Default Drive (mA)	Func 1	Func 2	Func 3	Func 4	Func5	Default function	Description
Y16	GPIO1_D7/SDMMC0_CMD/UART4_RTS	I/O	I	up	8	GPIO1D7	sdmmc0_cmd	uart4_rts			SDMMC0_CMD	SDMMC0 command output
Y18	VCCIO2	P	N/A	N/A							VCCIO_SD	SDMMC0 digital I/O power supply
PART F SAR ADC												
V14	ADC_IN0	A	N/A	N/A							ADC0_HW_ID	Hardware version recognition
W14	ADC_IN1	A	N/A	N/A							ADC1_HP_HOOK	Headphone key detect
V15	ADC_IN2	A	N/A	N/A							ADC2_KEY_IN	AD keyboard input
U13	ADC_AVDD_1V8	AP	N/A	N/A							VCC_1V8	SAR-ADC analog power supply
PART D MIPI RX PHY												
W10	MIPI_CSI_D0P	A	N/A	N/A							MIPI_CSI_DP0	MIPI-CSIO differential lane 0 positive
V11	MIPI_CSI_D0N	P	N/A	N/A							MIPI_CSI_DN0	MIPI-CSIO differential lane 0 negative
Y9	MIPI_CSI_D1P	A	N/A	N/A							MIPI_CSI_DP1	MIPI-CSIO differential lane 1 positive
W9	MIPI_CSI_D1N	A	N/A	N/A							MIPI_CSI_DN1	MIPI-CSIO differential lane 1 negative
V10	MIPI_CSI_CLKP	A	N/A	N/A							MIPI_CSI_CLKP	MIPI-CSIO differential clock lane positive
U10	MIPI_CSI_CLKN	A	N/A	N/A							MIPI_CSI_CLKN	MIPI-CSIO differential clock lane negative
U9	MIPI_CSI_D2P	A	N/A	N/A							MIPI_CSI_DP2	MIPI-CSIO differential lane 2 positive
V8	MIPI_CSI_D2N	A	N/A	N/A							MIPI_CSI_DN2	MIPI-CSIO differential lane 2 negative
V9	MIPI_CSI_D3P	A	N/A	N/A							MIPI_CSI_DP3	MIPI-CSIO differential lane 3 positive
W8	MIPI_CSI_D3N	A	N/A	N/A							MIPI_CSI_DN3	MIPI-CSIO differential lane 3 negative
U11	MIPI_CSI_RBIAS	A	N/A	N/A							MIPI_CSI_RBIAS	MIPI-CSIO reference current generate,connect a 2K%1 resistor to VSS
U8	MIPI_CSI_VCCA_1V0	P	N/A	N/A							VDD_1V0	MIPI-CSIO analog power supply
PART E USB 2.0 PHY												
Y10	USB_OTG_DP	A	N/A	N/A							OTG_DP	USB OTG Data Plus port
AA10	USB_OTG_DM	A	N/A	N/A							OTG_DM	USB OTG Data Minus port
Y13	USB_VBUS	A	N/A	N/A							USB_DET	TYPEC0 connected/vbus power detect for USB2.0
Y11	USB_ID	A	N/A	up							USB_ID	USB ID detect input,200kohm internal pull-up to USB_AVDD_1V8
AA12	USB_HOST_DP	A	N/A	N/A							USB_HOST_DP	USB HOST Data Plus port
Y12	USB_HOST_DM	A	N/A	N/A							USB_HOST_DM	USB HOST Data Minus port
AA11	USB_RBIAS	A	N/A	N/A							USB_RBIAS	USB PHY0(include HOST0&TYPEC0) reference current generate,connect a 133ohm resistor to VSS.
U12	USB_AVDD_1V0	P	N/A	N/A							USB_AVDD_1V0	USB analog power supply
V13	USB_AVDD_1V8	P	N/A	N/A							USB_AVDD_1V8	USB analog power supply
W13	USB_AVDD_3V3	P	N/A	N/A							USB_AVDD_3V3	USB analog power supply
PART L EMMC PHY												
E20	GPIO1_A0/FLASH_D0/EMMC_D0/SFC_SIO0	I	N/A	up	8	GPIO1A0	flash_d0	emmc_d0	sfc_sio0		FLASH_D0	EMMC data port

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版本	更新时间	修改内容										修改人
V1.0	2018.04.27	正式版										XHF
PX30 Pin No.	PX30 Pin Name	Pin Type	I/O Def	I/O Pull	Default Drive (mA)	Func 1	Func 2	Func 3	Func 4	Func5	Default function	Description
D21	GPIO1_A1/FLASH_D1/E MMC D1/SFC SIO1	I	N/A	up	8	GPIO1A1	flash_d1	emmc_d1	sfc_sio1		FLASH_D1	eMMC data port
C21	GPIO1_A2/FLASH_D2/E MMC D2/SFC SIO2	I	N/A	up	8	GPIO1A2	flash_d2	emmc_d2	sfc_sio2		FLASH_D2	eMMC data port
E19	GPIO1_A3/FLASH_D3/E MMC D3/SFC SIO3	I	N/A	up	8	GPIO1A3	flash_d3	emmc_d3	sfc_sio3		FLASH_D3	eMMC data port
E21	GPIO1_A4/FLASH_D4/E MMC D4/SFC CSN0	I	N/A	up	8	GPIO1A4	flash_d4	emmc_d4	sfc_csn0		FLASH_D4	eMMC data port
D20	GPIO1_A5/FLASH_D5/E MMC D5	I	N/A	up	8	GPIO1A5	flash_d5	emmc_d5			FLASH_D5	eMMC data port
C20	GPIO1_A6/FLASH_D6/E MMC D6	I	N/A	up	8	GPIO1A6	flash_d6	emmc_d6			FLASH_D6	eMMC data port
B21	GPIO1_A7/FLASH_D7/E MMC D7	I	N/A	up	8	GPIO1A7	flash_d7	emmc_d7			FLASH_D7	eMMC data port
F19	GPIO1_B0/FLASH_CS0	I		up	8	GPIO1B0	flash_cs0	emmc_pwr en			FLASH_CS0	Nand flash select0 port
H18	GPIO1_B1/FLASH_RDY/ EMMC CLKOUT/SFC C	I		up	8	GPIO1B1	flash_rdy	emmc_clk out	sfc_clk		FLASH_RDY/EMMC_CLK/SFC_CLK	eMMC clock out
G19	GPIO1_B2/FLASH_DQS/ EMMC CMD	I	N/A	up	8	GPIO1B2	flash_dqs	emmc_cm d			FLASH_DQS/EMMC_CMD	eMMC command port
E18	GPIO1_B3/FLASH_ALE/ EMMC RSTN	I	N/A	down	8	GPIO1B3	flash_ale	emmc_rstn			FLASH_ALE/EMMC_RST	eMMC strobe port
G21	GPIO1_B4/FLASH_CLE/ UART3 CTS M1/SPI0	I		down	8	GPIO1B4	flash_cle	uart3_ctsm 1	spi0_mosi	i2c3_sda	FLASH_CLE	Nand flash command latch enable
H20	GPIO1_B5/FLASH_WRN /UART3 RTS M1/SPI0	I		up	8	GPIO1B5	flash_wrn	uart3_rtsm 1	spi0_miso	i2c3_scl	FLASH_WRN	Nand flash write enable
G20	GPIO1_B6/FLASH_CS1/ UART3 TX M1/SPI0 C	I	O	up	8	GPIO1B6	flash_cs1	uart3_txm1	spi0_csn		FLASH_CS1	Nand flash select1 port
H21	GPIO1_B7/FLASH_RDN/ UART3 RX M1/SPI0 C	I		up	8	GPIO1B7	flash_rdn	uart3_rxm1	spi0_clk		FLASH_RDN	Nand flash read enable
J18	VCCIO6	P	N/A	N/A							VCCIO_FLASH	eMMC I/O power supply
PART A DDR												
R4	DDR DQ0	A	N/A	N/A							DDR DQ0	DRAM data port
R1	DDR DQ1	A	N/A	N/A							DDR DQ1	DRAM data port
U3	DDR DQ2	A	N/A	N/A							DDR DQ2	DRAM data port
Y2	DDR DQ3	A	N/A	N/A							DDR DQ3	DRAM data port
N2	DDR DQ4	A	N/A	N/A							DDR DQ4	DRAM data port
R3	DDR DQ5	A	N/A	N/A							DDR DQ5	DRAM data port
R2	DDR DQ6	A	N/A	N/A							DDR DQ6	DRAM data port
Y1	DDR DQ7	A	N/A	N/A							DDR DQ7	DRAM data port
AA2	DDR DQ8	A	N/A	N/A							DDR DQ8	DRAM data port
P3	DDR DQ9	A	N/A	N/A							DDR DQ9	DRAM data port
Y3	DDR DQ10	A	N/A	N/A							DDR DQ10	DRAM data port
M2	DDR DQ11	A	N/A	N/A							DDR DQ11	DRAM data port
V4	DDR DQ12	A	N/A	N/A							DDR DQ12	DRAM data port
M3	DDR DQ13	A	N/A	N/A							DDR DQ13	DRAM data port
V3	DDR DQ14	A	N/A	N/A							DDR DQ14	DRAM data port
N4	DDR DQ15	A	N/A	N/A							DDR DQ15	DRAM data port
F4	DDR DQ16	A	N/A	N/A							DDR DQ16	DRAM data port
G3	DDR DQ17	A	N/A	N/A							DDR DQ17	DRAM data port
F3	DDR DQ18	A	N/A	N/A							DDR DQ18	DRAM data port

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版本	更新时间	修改内容										修改人
V1.0	2018.04.27	正式版										XHF
PX30 Pin No.	PX30 Pin Name	Pin Type	I/O Def	I/O Pull	Default Drive (mA)	Func 1	Func 2	Func 3	Func 4	Func5	Default function	Description
K2	DDR DQ19	A	N/A	N/A							DDR DQ19	DRAM data port
L2	DDR DQ20	A	N/A	N/A							DDR DQ20	DRAM data port
M1	DDR DQ21	A	N/A	N/A							DDR DQ21	DRAM data port
E2	DDR DQ22	A	N/A	N/A							DDR DQ22	DRAM data port
L1	DDR DQ23	A	N/A	N/A							DDR DQ23	DRAM data port
K3	DDR DQ24	A	N/A	N/A							DDR DQ24	DRAM data port
J4	DDR DQ25	A	N/A	N/A							DDR DQ25	DRAM data port
C2	DDR DQ26	A	N/A	N/A							DDR DQ26	DRAM data port
D2	DDR DQ27	A	N/A	N/A							DDR DQ27	DRAM data port
K4	DDR DQ28	A	N/A	N/A							DDR DQ28	DRAM data port
J3	DDR DQ29	A	N/A	N/A							DDR DQ29	DRAM data port
D1	DDR DQ30	A	N/A	N/A							DDR DQ30	DRAM data port
E1	DDR DQ31	A	N/A	N/A							DDR DQ31	DRAM data port
W2	DDR DQS0 P	A	N/A	N/A							DDR DM0	DRAM data mask 0
W1	DDR DQS0 N	A	N/A	N/A							DDR DM1	DRAM data mask 1
U2	DDR DQS1 P	A	N/A	N/A							DDR DM2	DRAM data mask 2
U1	DDR DQS1 N	A	N/A	N/A							DDR DM3	DRAM data mask 3
J2	DDR DQS2 P	A	N/A	N/A							DDR DQS0P	DRAM data strobe 0
J1	DDR DQS2 N	A	N/A	N/A							DDR DQS0N	DRAM data strobe 0
G2	DDR DQS3 P	A	N/A	N/A							DDR DQS1P	DRAM data strobe 1
G1	DDR DQS3 N	A	N/A	N/A							DDR DQS1N	DRAM data strobe 1
T2	DDR DM0	A	N/A	N/A							DDR DQS2P	DRAM data strobe 2
U4	DDR DM1	A	N/A	N/A							DDR DQS2N	DRAM data strobe 2
G4	DDR DM2	A	N/A	N/A							DDR DQS3P	DRAM data strobe 3
M4	DDR DM3	A	N/A	N/A							DDR DQS3N	DRAM data strobe 3
C7	DDR3 A0/DDR4 A10	A	N/A	N/A							DDR A0	DRAM address port
D10	DDR3 A1/DDR4 A10	A	N/A	N/A							DDR A1	DRAM address port
D8	DDR3 A2/DDR4 A4	A	N/A	N/A							DDR A2	DRAM address port
B5	DDR3 A3/DDR4 A6	A	N/A	N/A							DDR A3	DRAM address port
A8	DDR3 A4/DDR4 A5	A	N/A	N/A							DDR A4	DRAM address port
A4	DDR3 A5/DDR4 A8	A	N/A	N/A							DDR A5	DRAM address port
B8	DDR3 A6/DDR4 A7	A	N/A	N/A							DDR A6	DRAM address port
B4	DDR3 A7/DDR4 A11	A	N/A	N/A							DDR A7	DRAM address port
C10	DDR3 A8/DDR4 A13	A	N/A	N/A							DDR A8	DRAM address port
C6	DDR3 A9/DDR4 A0	A	N/A	N/A							DDR A9	DRAM address port
A10	DDR3 A10/DDR4 CS0N	A	N/A	N/A							DDR A10	DRAM address port
B7	DDR3 A11/DDR4 A3	A	N/A	N/A							DDR A11	DRAM address port
A9	DDR3 A12/DDR4 BA1	A	N/A	N/A							DDR A12	DRAM address port
D6	DDR3 A13/DDR4 A2	A	N/A	N/A							DDR A13	DRAM address port
A7	DDR3 A14/DDR4 A1	A	N/A	N/A							DDR A14	DRAM address port
C9	DDR3 A15/DDR4 ODT0	A	N/A	N/A							DDR A15	DRAM address port
A3	DDR3 BA0/DDR4 BG0	A	N/A	N/A							DDR CK0P	DRAM differential clock output
B10	DDR3 BA1/DDR4 CAS	A	N/A	N/A							DDR CK0N	DRAM differential clock output
A5	DDR3 BA2/DDR4 BA0	A	N/A	N/A							DDR CKE0	DRAM clock enable 0
B3	DDR3 CSN0/DDR4 AC	A	N/A	N/A							DDR CSN0	DRAM chip select 0
D11	DDR3 CSN1/DDR4 CS	A	N/A	N/A							DDR CSN1	DRAM chip select 1
B2	DDR3 CLKP/DDR4 CLK	A	N/A	N/A							DDR BA0	DRAM bank select 0
B10	DDR3 CLKN/DDR4 CL	A	N/A	N/A							DDR BA1	DRAM bank select 1
D5	DDR3 ODT0/DDR4 WE	A	N/A	N/A							DDR BA2	DRAM bank select 2
C11	DDR3 ODT1/DDR4 OD	A	N/A	N/A							DDR ODT0	DRAM on die termination control 0
A21	DDR3 RESETN/DDR4	A	N/A	N/A							DDR ODT1	DRAM on die termination control 1
D9	DDR3 CASN/DDR4 A12	A	N/A	N/A							DDR CASN	DRAM column address strobe output
C4	DDR3 RASN/DDR4 CK	A	N/A	N/A							DDR RASN	DRAM row address strobe output
B11	DDR3 CKE/DDR4 RAS	A	N/A	N/A							DDR WEN	DRAM write enable strobe output
C8	DDR3 WEN/DDR4 BG1	A	N/A	N/A							DDR RESETN	DRAM reset output
F8	DDRIO VDD 1	P	N/A	N/A							DDR VDD	DRAM Digital power supply
F9	DDRIO VDD 2	P	N/A	N/A							DDR VDD	DRAM Digital power supply
F10	DDRIO VDD 3	P	N/A	N/A							DDR VDD	DRAM Digital power supply
G9	DDRIO VDD 4	P	N/A	N/A							DDR VDD	DRAM Digital power supply

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版本	更新时间	修改内容 正式版										修改人
V1.0	2018.04.27											XHF
PX30 Pin No.	PX30 Pin Name	Pin Type	I/O Def	I/O Pull	Default Drive (mA)	Func 1	Func 2	Func 3	Func 4	Func5	Default function	Description
G10	DDRIO VDD 5	P	N/A	N/A							DDR VDD	DRAM Digital power supply
J6	DDRIO VDD 6	P	N/A	N/A							DDR VDD	DRAM Digital power supply
J7	DDRIO VDD 7	P	N/A	N/A							DDR VDD	DRAM Digital power supply
K6	DDRIO VDD 8	P	N/A	N/A							DDR VDD	DRAM Digital power supply
K7	DDRIO VDD 9	P	N/A	N/A							DDR VDD	DRAM Digital power supply
L6	DDRIO VDD 10	P	N/A	N/A							DDR VDD	DRAM Digital power supply
L7	DDRIO VDD 11	P	N/A	N/A							DDR VDD	DRAM Digital power supply
M6	DDRIO VDD 12	P	N/A	N/A							DDR VDD	DRAM Digital power supply
M7	DDRIO VDD 13	P	N/A	N/A							DDR VDD	DRAM Digital power supply
PART N POWER												
K9	LOGIC VDD 1	P	N/A	N/A							LOGIC VDD 1	Logic /GPU power supply
L9	LOGIC VDD 2	P	N/A	N/A							LOGIC VDD 2	Logic /GPU power supply
K10	LOGIC VDD 3	P	N/A	N/A							LOGIC VDD 3	Logic /GPU power supply
L10	LOGIC VDD 4	P	N/A	N/A							LOGIC VDD 4	Logic /GPU power supply
N11	LOGIC VDD 5	P	N/A	N/A							LOGIC VDD 5	Logic /GPU power supply
N13	LOGIC VDD 6	P	N/A	N/A							LOGIC VDD 6	Logic /GPU power supply
N12	LOGIC VDD 7	P	N/A	N/A							LOGIC VDD 7	Logic /GPU power supply
M11	LOGIC VDD 8	P	N/A	N/A							LOGIC VDD 8	Logic /GPU power supply
L11	LOGIC VDD 9	P	N/A	N/A							LOGIC VDD 9	Logic /GPU power supply
H14	CPU VDD 1	P	N/A	N/A							CPU VDD 1	Logic /GPU power supply
H15	CPU VDD 2	P	N/A	N/A							CPU VDD 2	CPU power supply
H16	CPU VDD 3	P	N/A	N/A							CPU VDD 3	CPU power supply
J14	CPU VDD 4	P	N/A	N/A							CPU VDD 4	CPU power supply
J15	CPU VDD 5	P	N/A	N/A							CPU VDD 5	CPU power supply
J16	CPU VDD 6	P	N/A	N/A							CPU VDD 6	CPU power supply
K14	CPU VDD 7	P	N/A	N/A							CPU VDD 7	CPU power supply
K15	CPU VDD 8	P	N/A	N/A							CPU VDD 8	CPU power supply
K16	CPU VDD 9	P	N/A	N/A							CPU VDD 9	CPU power supply
PART C GROUND												
A1	VSS 1	G	N/A	N/A							VSS	Digital power ground
A21	VSS 2	G	N/A	N/A							VSS	Digital power ground
AA21	VSS 3	G	N/A	N/A							VSS	Digital power ground
AA1	VSS 4	G	N/A	N/A							VSS	Digital power ground
A11	VSS 5	G	N/A	N/A							VSS	Digital power ground
B6	VSS 6	G	N/A	N/A							VSS	Digital power ground
B9	VSS 7	G	N/A	N/A							VSS	Digital power ground
C3	VSS 8	G	N/A	N/A							VSS	Digital power ground
C5	VSS 9	G	N/A	N/A							VSS	Digital power ground
C12	VSS 10	G	N/A	N/A							VSS	Digital power ground
D3	VSS 11	G	N/A	N/A							VSS	Digital power ground
D4	VSS 12	G	N/A	N/A							VSS	Digital power ground
D7	VSS 13	G	N/A	N/A							VSS	Digital power ground
E3	VSS 14	G	N/A	N/A							VSS	Digital power ground
E4	VSS 15	G	N/A	N/A							VSS	Digital power ground
E5	VSS 16	G	N/A	N/A							VSS	Digital power ground
E6	VSS 17	G	N/A	N/A							VSS	Digital power ground
E7	VSS 18	G	N/A	N/A							VSS	Digital power ground
E8	VSS 19	G	N/A	N/A							VSS	Digital power ground
E9	VSS 20	G	N/A	N/A							VSS	Digital power ground
E10	VSS 21	G	N/A	N/A							VSS	Digital power ground
F2	VSS 22	G	N/A	N/A							VSS	Digital power ground
F5	VSS 23	G	N/A	N/A							VSS	Digital power ground
E11	VSS 24	G	N/A	N/A							VSS	Digital power ground
F14	VSS 25	G	N/A	N/A							VSS	Digital power ground
F15	VSS 26	G	N/A	N/A							VSS	Digital power ground
F16	VSS 27	G	N/A	N/A							VSS	Digital power ground



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版本	更新时间	修改内容										修改人
V1.0	2018.04.27	正式版										XHF
PX30 Pin No.	PX30 Pin Name	Pin Type	I/O Def	I/O Pull	Default Drive (mA)	Func 1	Func 2	Func 3	Func 4	Func5	Default function	Description
G5	VSS 28	G	N/A	N/A							VSS	Digital power ground
G7	VSS 29	G	N/A	N/A							VSS	Digital power ground
G8	VSS 30	G	N/A	N/A							VSS	Digital power ground
G11	VSS 31	G	N/A	N/A							VSS	Digital power ground
W3	VSS 32	G	N/A	N/A							VSS	Digital power ground
G13	VSS 33	G	N/A	N/A							VSS	Digital power ground
G14	VSS 34	G	N/A	N/A							VSS	Digital power ground
W11	VSS 35	G	N/A	N/A							VSS	Digital power ground
T12	VSS 36	G	N/A	N/A							VSS	Digital power ground
H2	VSS 37	G	N/A	N/A							VSS	Digital power ground
H3	VSS 38	G	N/A	N/A							VSS	Digital power ground
H4	VSS 39	G	N/A	N/A							VSS	Digital power ground
H5	VSS 40	G	N/A	N/A							VSS	Digital power ground
H7	VSS 41	G	N/A	N/A							VSS	Digital power ground
H8	VSS 42	G	N/A	N/A							VSS	Digital power ground
H9	VSS 43	G	N/A	N/A							VSS	Digital power ground
H10	VSS 44	G	N/A	N/A							VSS	Digital power ground
H11	VSS 45	G	N/A	N/A							VSS	Digital power ground
H12	VSS 46	G	N/A	N/A							VSS	Digital power ground
G16	VSS 47	G	N/A	N/A							VSS	Digital power ground
G15	VSS 48	G	N/A	N/A							VSS	Digital power ground
J5	VSS 49	G	N/A	N/A							VSS	Digital power ground
J8	VSS 50	G	N/A	N/A							VSS	Digital power ground
J9	VSS 51	G	N/A	N/A							VSS	Digital power ground
J10	VSS 52	G	N/A	N/A							VSS	Digital power ground
J11	VSS 53	G	N/A	N/A							VSS	Digital power ground
J12	VSS 54	G	N/A	N/A							VSS	Digital power ground
H13	VSS 55	G	N/A	N/A							VSS	Digital power ground
J17	VSS 56	G	N/A	N/A							VSS	Digital power ground
H17	VSS 57	G	N/A	N/A							VSS	Digital power ground
K5	VSS 58	G	N/A	N/A							VSS	Digital power ground
K8	VSS 59	G	N/A	N/A							VSS	Digital power ground
M13	VSS 60	G	N/A	N/A							VSS	Digital power ground
K11	VSS 61	G	N/A	N/A							VSS	Digital power ground
M12	VSS 62	G	N/A	N/A							VSS	Digital power ground
K12	VSS 63	G	N/A	N/A							VSS	Digital power ground
J13	VSS 64	G	N/A	N/A							VSS	Digital power ground
K13	VSS 65	G	N/A	N/A							VSS	Digital power ground
L14	VSS 66	G	N/A	N/A							VSS	Digital power ground
K17	VSS 67	G	N/A	N/A							VSS	Digital power ground
K18	VSS 68	G	N/A	N/A							VSS	Digital power ground
L13	VSS 69	G	N/A	N/A							VSS	Digital power ground
L3	VSS 70	G	N/A	N/A							VSS	Digital power ground
L4	VSS 71	G	N/A	N/A							VSS	Digital power ground
L5	VSS 72	G	N/A	N/A							VSS	Digital power ground
L8	VSS 73	G	N/A	N/A							VSS	Digital power ground
N14	VSS 74	G	N/A	N/A							VSS	Digital power ground
M14	VSS 75	G	N/A	N/A							VSS	Digital power ground
W18	VSS 76	G	N/A	N/A							VSS	Digital power ground
L12	VSS 77	G	N/A	N/A							VSS	Digital power ground
L17	VSS 78	G	N/A	N/A							VSS	Digital power ground
L18	VSS 79	G	N/A	N/A							VSS	Digital power ground
M5	VSS 80	G	N/A	N/A							VSS	Digital power ground
M8	VSS 81	G	N/A	N/A							VSS	Digital power ground
M15	VSS 82	G	N/A	N/A							VSS	Digital power ground
M16	VSS 83	G	N/A	N/A							VSS	Digital power ground
M9	VSS 84	G	N/A	N/A							VSS	Digital power ground
M10	VSS 85	G	N/A	N/A							VSS	Digital power ground
M17	VSS 86	G	N/A	N/A							VSS	Digital power ground



PX30 I/O LIST

版本	更新时间	修改内容										修改人
V1.0	2018.04.27	正式版										XHF
PX30 Pin No.	PX30 Pin Name	Pin Type	I/O Def	I/O Pull	Default Drive (mA)	Func 1	Func 2	Func 3	Func 4	Func5	Default function	Description
L15	VSS 87	G	N/A	N/A							VSS	Digital power ground
L16	VSS 88	G	N/A	N/A							VSS	Digital power ground
N3	VSS 89	G	N/A	N/A							VSS	Digital power ground
N5	VSS 90	G	N/A	N/A							VSS	Digital power ground
L6	VSS 91	G	N/A	N/A							VSS	Digital power ground
N7	VSS 92	G	N/A	N/A							VSS	Digital power ground
N8	VSS 93	G	N/A	N/A							VSS	Digital power ground
N15	VSS 94	G	N/A	N/A							VSS	Digital power ground
N16	VSS 95	G	N/A	N/A							VSS	Digital power ground
N9	VSS 96	G	N/A	N/A							VSS	Digital power ground
N10	VSS 97	G	N/A	N/A							VSS	Digital power ground
AA3	VSS 98	G	N/A	N/A							VSS	Digital power ground
W17	VSS 99	G	N/A	N/A							VSS	Digital power ground
P4	VSS 100	G	N/A	N/A							VSS	Digital power ground
P5	VSS 101	G	N/A	N/A							VSS	Digital power ground
P6	VSS 102	G	N/A	N/A							VSS	Digital power ground
P7	VSS 103	G	N/A	N/A							VSS	Digital power ground
P8	VSS 104	G	N/A	N/A							VSS	Digital power ground
P9	VSS 105	G	N/A	N/A							VSS	Digital power ground
P10	VSS 106	G	N/A	N/A							VSS	Digital power ground
P11	VSS 107	G	N/A	N/A							VSS	Digital power ground
P12	VSS 108	G	N/A	N/A							VSS	Digital power ground
P13	VSS 109	G	N/A	N/A							VSS	Digital power ground
P14	VSS 110	G	N/A	N/A							VSS	Digital power ground
P16	VSS 111	G	N/A	N/A							VSS	Digital power ground
P15	VSS 112	G	N/A	N/A							VSS	Digital power ground
R5	VSS 113	G	N/A	N/A							VSS	Digital power ground
R6	VSS 114	G	N/A	N/A							VSS	Digital power ground
R7	VSS 115	G	N/A	N/A							VSS	Digital power ground
R8	VSS 116	G	N/A	N/A							VSS	Digital power ground
R9	VSS 117	G	N/A	N/A							VSS	Digital power ground
R10	VSS 118	G	N/A	N/A							VSS	Digital power ground
R11	VSS 119	G	N/A	N/A							VSS	Digital power ground
R12	VSS 120	G	N/A	N/A							VSS	Digital power ground
R13	VSS 121	G	N/A	N/A							VSS	Digital power ground
R14	VSS 122	G	N/A	N/A							VSS	Digital power ground
R15	VSS 123	G	N/A	N/A							VSS	Digital power ground
R16	VSS 124	G	N/A	N/A							VSS	Digital power ground
R17	VSS 125	G	N/A	N/A							VSS	Digital power ground
T3	VSS 126	G	N/A	N/A							VSS	Digital power ground
T4	VSS 127	G	N/A	N/A							VSS	Digital power ground
T5	VSS 128	G	N/A	N/A							VSS	Digital power ground
T6	VSS 129	G	N/A	N/A							VSS	Digital power ground
T7	VSS 130	G	N/A	N/A							VSS	Digital power ground
T8	VSS 131	G	N/A	N/A							VSS	Digital power ground
T9	VSS 132	G	N/A	N/A							VSS	Digital power ground
T10	VSS 133	G	N/A	N/A							VSS	Digital power ground
T11	VSS 134	G	N/A	N/A							VSS	Digital power ground
W4	VSS 135	G	N/A	N/A							VSS	Digital power ground
T14	VSS 136	G	N/A	N/A							VSS	Digital power ground
T15	VSS 137	G	N/A	N/A							VSS	Digital power ground
T16	VSS 138	G	N/A	N/A							VSS	Digital power ground
T17	VSS 139	G	N/A	N/A							VSS	Digital power ground
T18	VSS 140	G	N/A	N/A							VSS	Digital power ground
U5	VSS 141	G	N/A	N/A							VSS	Digital power ground
U14	VSS 142	G	N/A	N/A							VSS	Digital power ground
U15	VSS 143	G	N/A	N/A							VSS	Digital power ground
U16	VSS 144	G	N/A	N/A							VSS	Digital power ground
U17	VSS 145	G	N/A	N/A							VSS	Digital power ground



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版本	更新时间	修改内容										修改人
V1.0	2018.04.27	正式版										XHF
PX30 Pin No.	PX30 Pin Name	Pin Type	I/O Def	I/O Pull	Default Drive (mA)	Func 1	Func 2	Func 3	Func 4	Func5	Default function	Description
U18	VSS 146	G	N/A	N/A							VSS	Digital power ground
V2	VSS 147	G	N/A	N/A							VSS	Digital power ground
V5	VSS 148	G	N/A	N/A							VSS	Digital power ground
V16	VSS 149	G	N/A	N/A							VSS	Digital power ground
V17	VSS 150	G	N/A	N/A							VSS	Digital power ground
V18	VSS 151	G	N/A	N/A							VSS	Digital power ground
V19	VSS 152	G	N/A	N/A							VSS	Digital power ground