Introduction to 8086 Assembly

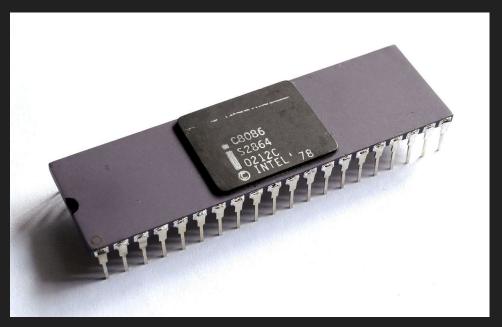
Lecture 2

80x86 architecture, registers and basic assembly

Intel 8086 microprocessor



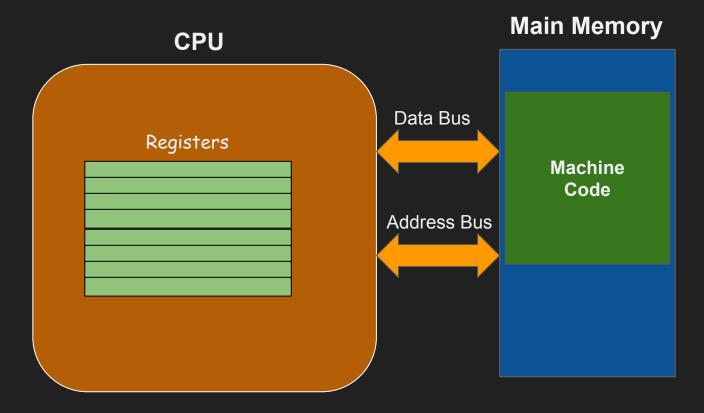
- released in 1978
- 16 bit
- the x86 family



https://en.wikipediB.org/wiki/Intel_8086

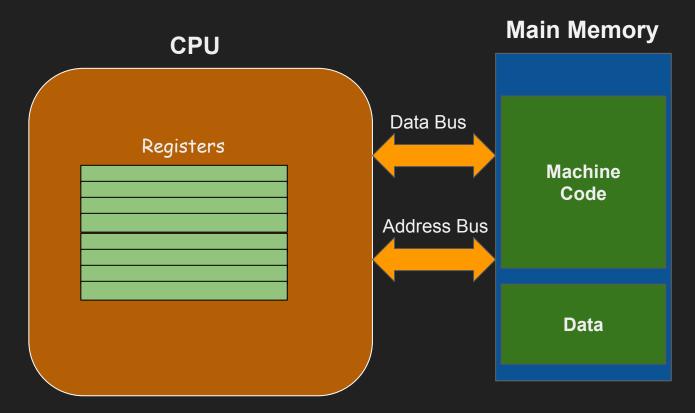
CPU, Memory, instructions and data





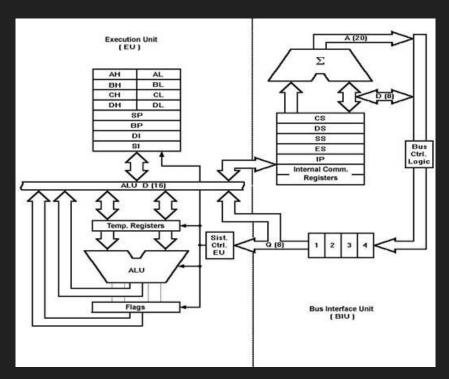
CPU, Memory, instructions and data





Intel 8086 architecture





http://www.cosc.brocku.ca/~bockusd/3p92/Local_Pages/8086_achitecture.htm

Intel 8086 registers

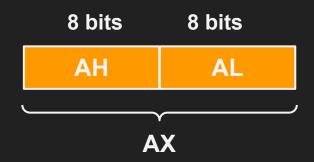
- 16 bit registers
- 8 bit access
 - o AX,BX,CX,DX
 - e.g. AX = (AH | AL)

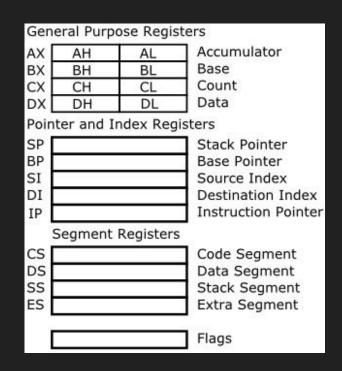


Gene	eral Purp	ose Regist	ters
AX [AH	AL	Accumulator
BX	ВН	BL	Base
cx [CH	CL	Count
DX [DH	DL	Data
Point	er and I	ndex Regi	sters
SP [Stack Pointer
BP			Base Pointer
SI			Source Index
DI			Destination Index
IP			Instruction Pointer
9	Segment	Registers	
cs [Code Segment
DS			Data Segment
ss			Stack Segment
ES			Extra Segment
			Flags

Intel 8086 registers

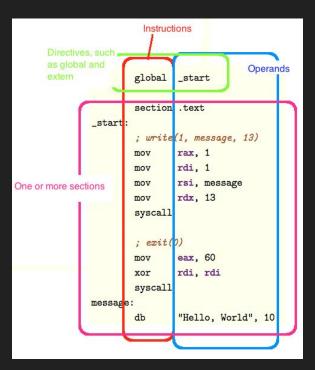






Intel 8086 assembly syntax





Gen	eral Purp	ose Regis	ters
AX [AH	AL	Accumulator
вх	ВН	BL	Base
CX [CH	CL	Count
DX [DH	DL	Data
Poin	ter and I	ndex Reg	isters
SP			Stack Pointer
BP			Base Pointer
SI			Source Index
DI			Destination Index
IP			Instruction Pointer
	Segment	Registers	
cs [(1)	Code Segment
DS			Data Segment
SS			Stack Segment
ES			Extra Segment
ī			Flags

http://cs.lmu.edu/~ray/notes/nasmtutorial/

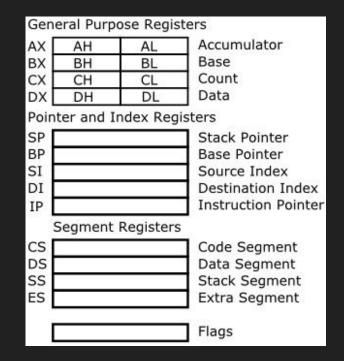
```
K. N. Toosi
University of Technology
```

```
mov x,y
xchg x,y
inc x
dec x
add x,y
sub x,y
neg x
; bitwise operators
and x,y
or x,y
xor x,y
not x
```

ral Purp	ose Regis	ters
AH	AL	Accumulator
ВН	BL	Base
CH	CL	Count
DH	DL	Data
er and I	ndex Reg	isters
		Stack Pointer
		Base Pointer
		Source Index
		Destination Index
		Instruction Pointer
egment	Registers	<u>-</u>
	100	Code Segment
		Data Segment
		Stack Segment
		Extra Segment
		T Flags
	AH BH CH DH er and I	AH AL BH BL CH CL

```
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University of Technology
```

```
mov x, y
inc x
dec x
add x,y
         ; x <- x+y
sub x,y ; x <- x-y
and x,y; x < -x & y
or x,y ; x \leftarrow x \mid y
xor x,y ; x <- x ^ y
x,y: register, constant, memory (address)
```

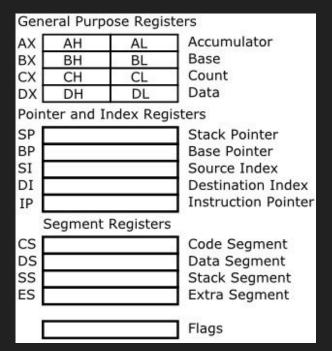






```
mov ax, bx
mov ax, 1
mov al, ah
mov cl, 123
mov ax, [u]
mov [v], bx
mov byte [v], 12

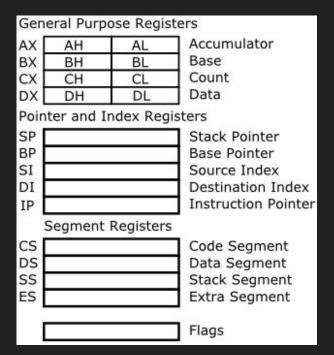
mov [v], [u]
```







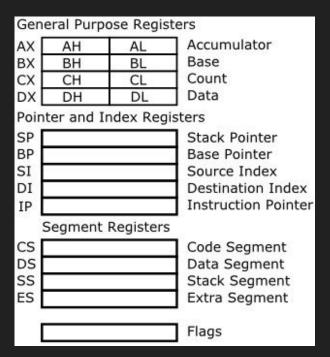
```
mov ax, bx
mov ax, 1
mov al, ah
mov cl, 123
mov ax, [u]
mov [v], bx
mov byte [v], 12
```







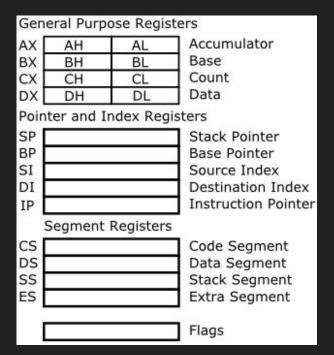
```
mov ax, bx
mov ax, 1
mov al, ah
mov cl, 123
mov ax, [u]
mov [v], bx
mov byte [v], 12
mov ax, [u]
mov [v], ax
```







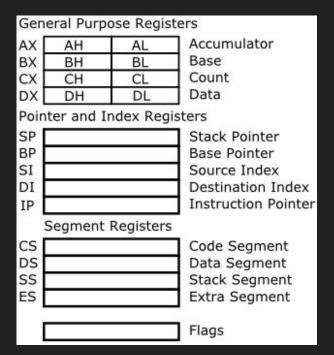
```
add ax, bx
add ax, 1
add al, ah
add cl, 123
add ax, [u]
add [v], bx
add byte [v], 12
```







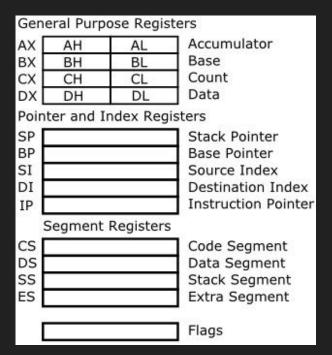
```
add ax, bx
add ax, 1
add al, ah
add cl, 123
add ax, [u]
add [v], bx
add byte [v], 12
```







```
add ax, bx
add ax, 1
add al, ah
add cl, 123
add ax, [u]
add [v], bx
add byte [v], 12
mov ax, [u]
add [v],ax
```



;
$$bx = (ax-10)*2$$

;
$$ax = (ax+1)*8$$

;
$$ax = (ax-1)*9$$

Gen	eral Purp	ose Regis	ters
AX [AH	AL	Accumulator
вх	ВН	BL	Base
CX	CH	CL	Count
DX [DH	DL	Data
Poin	ter and I	ndex Reg	isters
SP [Stack Pointer
BP			Base Pointer
SI			Source Index
DI			Destination Index
IP			Instruction Pointer
	Segment	Registers	
cs [110	Code Segment
DS			Data Segment
SS			Stack Segment
ES			Extra Segment
Г			Flags
[Flags

```
K. N. Toosi
University of Technology
```

```
; bx = (ax-10)*2
mov bx, ax
sub bx, 10
add bx, bx
; ax = (ax+1)*8
```

;
$$ax = (ax-1)*9$$

AH	AL	7 Assumulator
DLI		Accumulator
BH	BL	Base
CH	CL	Count
DH	DL	Data
er and I	ndex Reg	isters
		Stack Pointer
		Base Pointer
		Source Index
		Destination Index
		Instruction Pointer
egment	Registers	-
	110	Code Segment
		Data Segment
		Stack Segment
		Extra Segment
		Flags
	DH er and I	



```
; bx = (ax-10)*2
mov bx, ax
sub bx, 10
add bx, bx
; ax = (ax+1)*8
inc ax
add ax,ax
add ax,ax
add ax,ax
; ax = (ax-1)*9
```

Gene	eral Purp	ose Regis	ters
AX [AH	AL	Accumulator
вх	ВН	BL	Base
CX	CH	CL	Count
DX [DH	DL	Data
Point	ter and I	ndex Regi	sters
SP [Stack Pointer
BP			Base Pointer
SI			Source Index
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IP			Instruction Pointer
5	Segment	Registers	-
cs [Code Segment
DS			Data Segment
ss			Stack Segment
ES			Extra Segment
Г			Flags



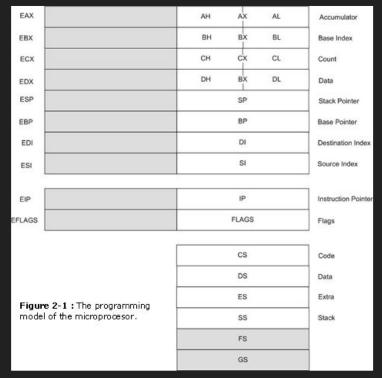


```
; bx = (ax-10)*2
mov bx, ax
sub bx, 10
add bx, bx
; ax = (ax+1)*8
inc ax
add ax,ax
add ax,ax
add ax,ax
; ax = (ax-1)*9
dec ax
mov si,ax
add ax,ax
add ax,ax
add ax,ax
add ax, si
```

Gene	eral Purp	ose Regis	ters
AX [AH	AL	Accumulator
BX [BH	BL	Base
CX [CH	CL	Count
DX [DH	DL	Data
Point	er and I	ndex Reg	isters
SP [Stack Pointer
BP			Base Pointer
SI			Source Index
DI			Destination Index
IP			Instruction Pointer
S	Segment	Registers	5
cs [10	Code Segment
DS			Data Segment
ss			Stack Segment
ES			Extra Segment
_			_ _
L			Flags



80386 (IA-32, i386)



http://www.byclb.com/TR/Tutorials/microprocessors/ch2 1.htm

8086, 80286

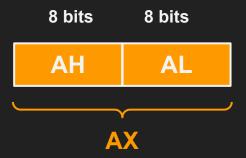


Gene	eral Purp	ose Regis	ters
AX [AH	AL	Accumulator
BX	ВН	BL	Base
cx [CH	CL	Count
DX [DH	DL	Data
Point	er and I	ndex Reg	isters
SP [Stack Pointer
BP			Base Pointer
SI			Source Index
DI			Destination Index
IP			Instruction Pointer
S	Segment	Registers	5
cs [Code Segment
DS			Data Segment
ss			Stack Segment
ES			Extra Segment
			Flags



80386 (IA-32, i386)

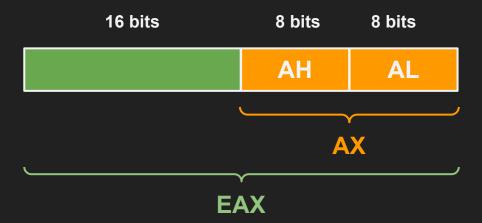






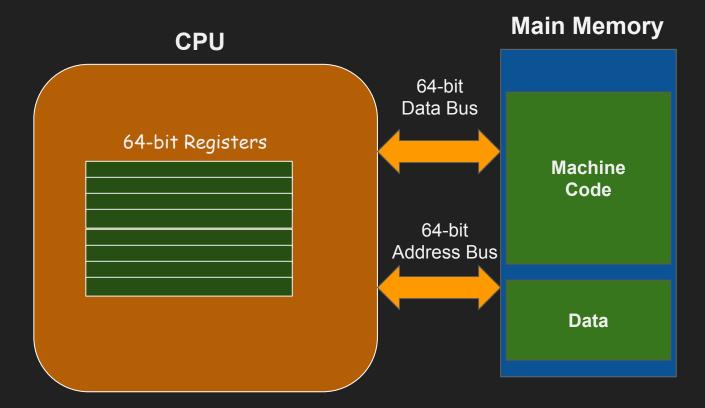
80386 (IA-32, i386)



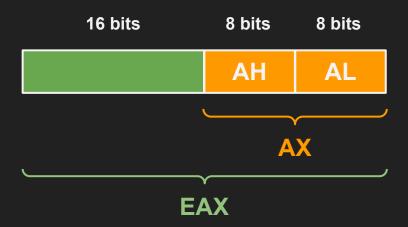


64 bit x86 systems (x86-64, x64, AMD64)

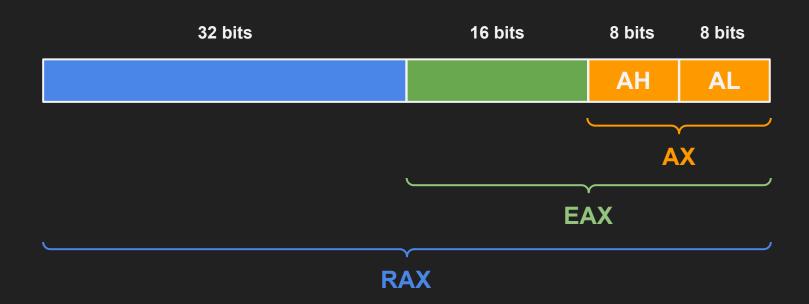




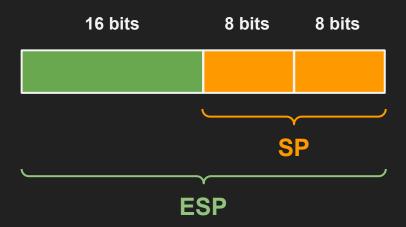




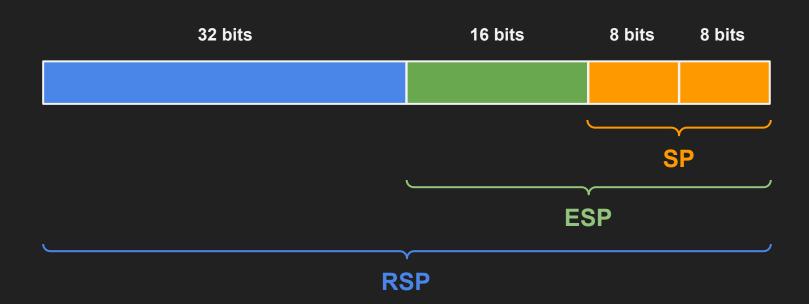




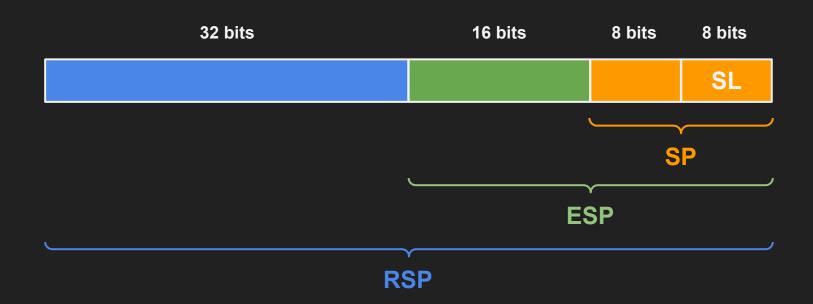






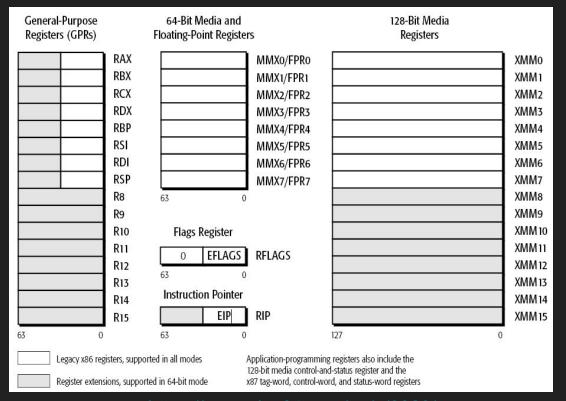






x86-64 bit registers (x86-64, AMD64, Intel64, x64)





https://www.viva64.com/en/a/0029/

x86-64 registers

- RAX
- RCX
- RDX
- RBX
- RBP
- RSI
- RDI
- RSP

- R8
- R9
- R10
- R11
- R12
- R13
- R14
- R15



x86-64 registers

- RAX = R0
- RCX = R1
- RDX = R2
- RBX = R3
- RBP = R4
- RSI = R5
- RDI = R6
- RSP = R7

- R8
- R9
- R10
- R11
- R12
- R13
- R14
- R15





