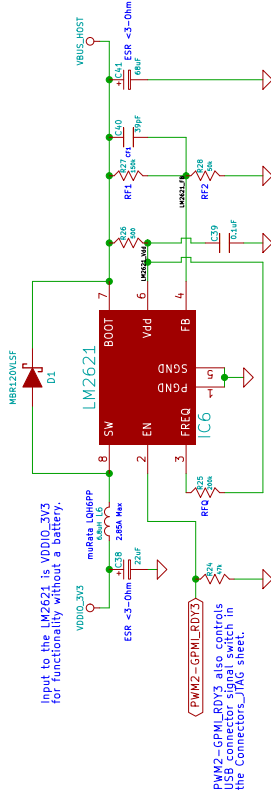


Notes:

- Capacitors are X7R, 6.3V, 20% or better unless otherwise noted
- Tantalum capacitors should have ESR < 300mOhm but to reduce costs only use them on the noisiest rails
- VDDIO, VDD, VDDA should have 33uF of decoupling and LPF >1MHz per IMX23RM 32.2.1.1
- Power Supply Designed to Freescale AppNote AN3883 recommendations

USB Host Mode VBUS Generator



The LQ486P meets the 2.5A inductor current limit of the LM2621. It also meets the typical (2.85A) internal current limit. However, it does not meet the maximum internal current limit of the LM2621 (4A), which can occur at high temperature during a short of USB-A VBUS+GND.

Debug/Host Mode Enabled LED



PWM2-GPMLRDY3 also controls USB Host enable to reduce the number of signals stuck on the board.

Power ON LED



Boot Mode

Boot Mode Selection

Boot Mode	LCD_Data
USB	3 2 1 0
I2C Master (AUART)	0 0 0 1
SPI SSP1 Flash	0 0 1 0
SPI SSP2 Flash	0 0 1 1
GPML (NAND)	0 1 0 0
DEBUG (JTAC_WAIT)	0 1 1 0
SPI SSP2 EEPROM	1 0 0 0
SD/MMC 1	1 0 0 1
SD/MMC 2 (Default)	1 0 1 0

Default Boot is SD/MMC2, the captive microSD card. Jumpers can be mounted to alter the default setting. LCD_RS High enables jumper boot. LCD_RS Low enables OTP boot.

