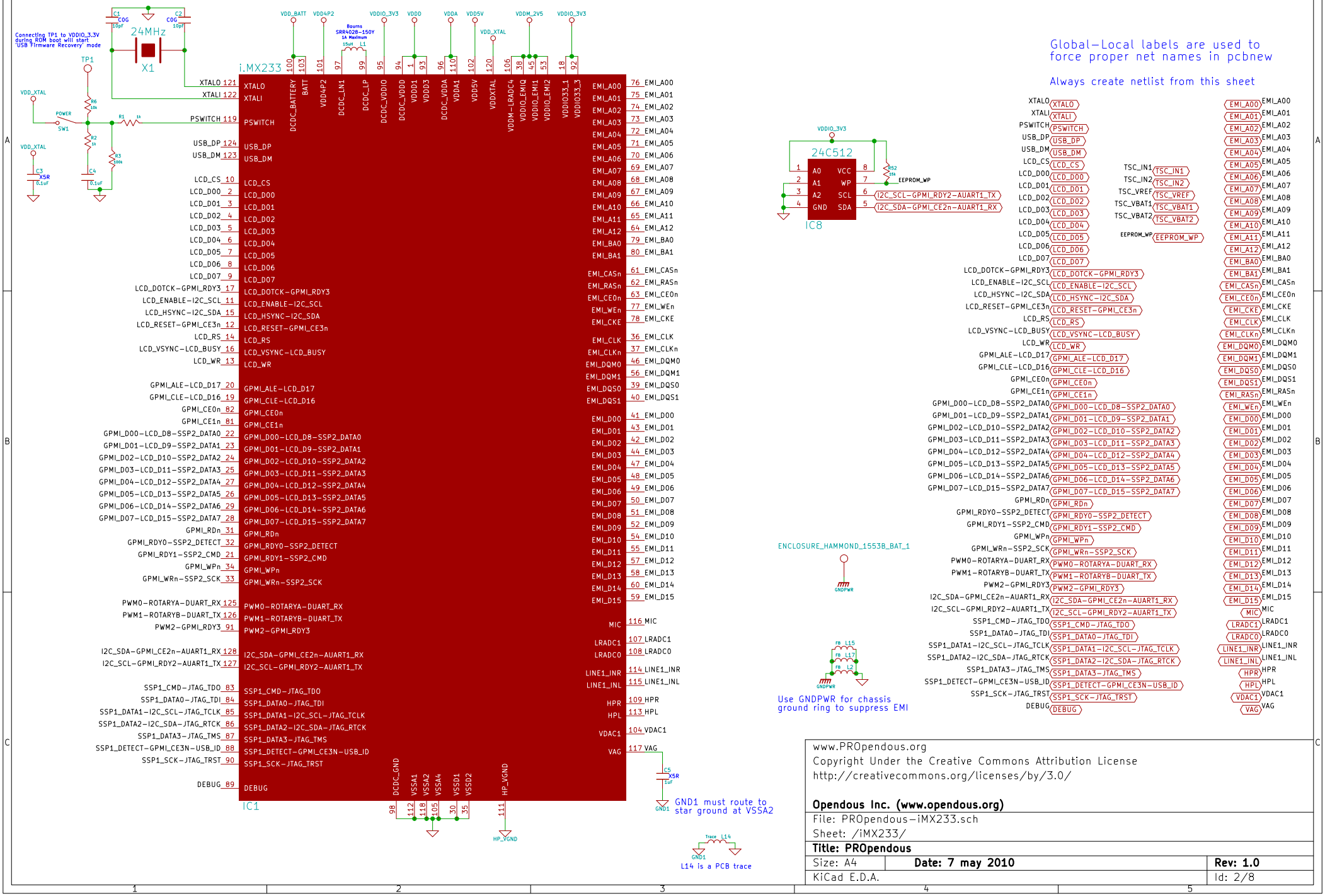
Connecting TP1 to VDDIO_3.3V during ROM boot will start USB Firmware Recovery mode
24MHz
X1
C1 C0G 10pF
C2 C0G 10pF
TP1
VDD_XTAL
POWER
SW1
R6 10k
R1 1k
R2 1k
R3 100k
C3 X5R 0.1uF
C4 0.1uF
i.MX233
IC1
VDD_BATT
VDD4P2
VDDIO_3V3
VDDD
VDDA
VDD5V
VDDM_2V5
VDD_XTAL
Bourns SRR4028-150Y 1A Maximum
15uH L1
XTALO 121
XTALI 122
PSWITCH 119
USB_DP 124
USB_DM 123
LCD_CS 10
LCD_D00 2
LCD_D01 3
LCD_D02 4
LCD_D03 5
LCD_D04 6
LCD_D05 7
LCD_D06 8
LCD_D07 9
LCD_DOTCK-GPMI_RDY3 17
LCD_ENABLE-I2C_SCL 11
LCD_HSYNC-I2C_SDA 15
LCD_RESET-GPMI_CE3n 12
LCD_RS 14
LCD_VSYNC-LCD_BUSY 16
LCD_WR 13
GPMI_ALE-LCD_D17 20
GPMI_CLE-LCD_D16 19
GPMI_CE0n 82
GPMI_CE1n 81
GPMI_D00-LCD_D8-SSP2_DATA0 22
GPMI_D01-LCD_D9-SSP2_DATA1 23
GPMI_D02-LCD_D10-SSP2_DATA2 24
GPMI_D03-LCD_D11-SSP2_DATA3 25
GPMI_D04-LCD_D12-SSP2_DATA4 27
GPMI_D05-LCD_D13-SSP2_DATA5 26
GPMI_D06-LCD_D14-SSP2_DATA6 29
GPMI_D07-LCD_D15-SSP2_DATA7 28
GPMI_RDn 31
GPMI_RDY0-SSP2_DETECT 32
GPMI_RDY1-SSP2_CMD 21
GPMI_WPn 34
GPMI_WRn-SSP2_SCK 33
PWM0-ROTARYA-DUART_RX 125
PWM1-ROTARYB-DUART_TX 126
PWM2-GPMI_RDY3 91
I2C_SDA-GPMI_CE2n-AUART1_RX 128
I2C_SCL-GPMI_RDY2-AUART1_TX 127
SSP1_CMD-JTAG_TDO 83
SSP1_DATA0-JTAG_TDI 84
SSP1_DATA1-I2C_SCL-JTAG_TCLK 85
SSP1_DATA2-I2C_SDA-JTAG_RTCK 86
SSP1_DATA3-JTAG_TMS 87
SSP1_DETECT-GPMI_CE3N-USB_ID 88
SSP1_SCK-JTAG_TRST 90
DEBUG 89
EMI_A00 76
EMI_A01 75
EMI_A02 74
EMI_A03 73
EMI_A04 72
EMI_A05 71
EMI_A06 70
EMI_A07 69
EMI_A08 68
EMI_A09 67
EMI_A10 66
EMI_A11 65
EMI_A12 64
EMI_BA0 79
EMI_BA1 80
EMI_CASn 61
EMI_RASn 62
EMI_CE0n 63
EMI_WEn 77
EMI_CKE 78
EMI_CLK 36
EMI_CLKn 37
EMI_DQM0 46
EMI_DQM1 56
EMI_DQS0 39
EMI_DQS1 40
EMI_D00 41
EMI_D01 43
EMI_D02 42
EMI_D03 44
EMI_D04 47
EMI_D05 48
EMI_D06 49
EMI_D07 50
EMI_D08 51
EMI_D09 52
EMI_D10 54
EMI_D11 55
EMI_D12 57
EMI_D13 58
EMI_D14 60
EMI_D15 59
MIC 116
LRADC1 107
LRADC0 108
LINE1_INR 114
LINE1_INL 115
HPR 109
HPL 113
VDAC1 104
VAG 117
C5 X5R 1uF
GND1
GND1 must route to star ground at VSSA2
DCDC_GND 98
VSSA1 112
VSSA2 118
VSSA4 105
VSSD1 30
VSSD2 35
HP_VGND 111
Trace L14
L14 is a PCB trace
24C512
IC8
VDDIO_3V3
R52 15k
EEPROM_WP
I2C_SCL-GPMI_RDY2-AUART1_TX
I2C_SDA-GPMI_CE2n-AUART1_RX
ENCLOSURE_HAMMOND_1553B_BAT_1
GNDPWR
FB L15
FB L17
FB L2
Use GNDPWR for chassis ground ring to suppress EMI
Global-Local labels are used to force proper net names in pcbnew
Always create netlist from this sheet
TSC_IN1
TSC_IN2
TSC_VREF
TSC_VBAT1
TSC_VBAT2
EEPROM_WP
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Opendous Inc. (www.opendous.org)
File: PROpendous-iMX233.sch
Sheet: /iMX233/
Title: PROpendous
Size: A4
Date: 7 may 2010
Rev: 1.0
KiCad E.D.A.
Id: 2/8