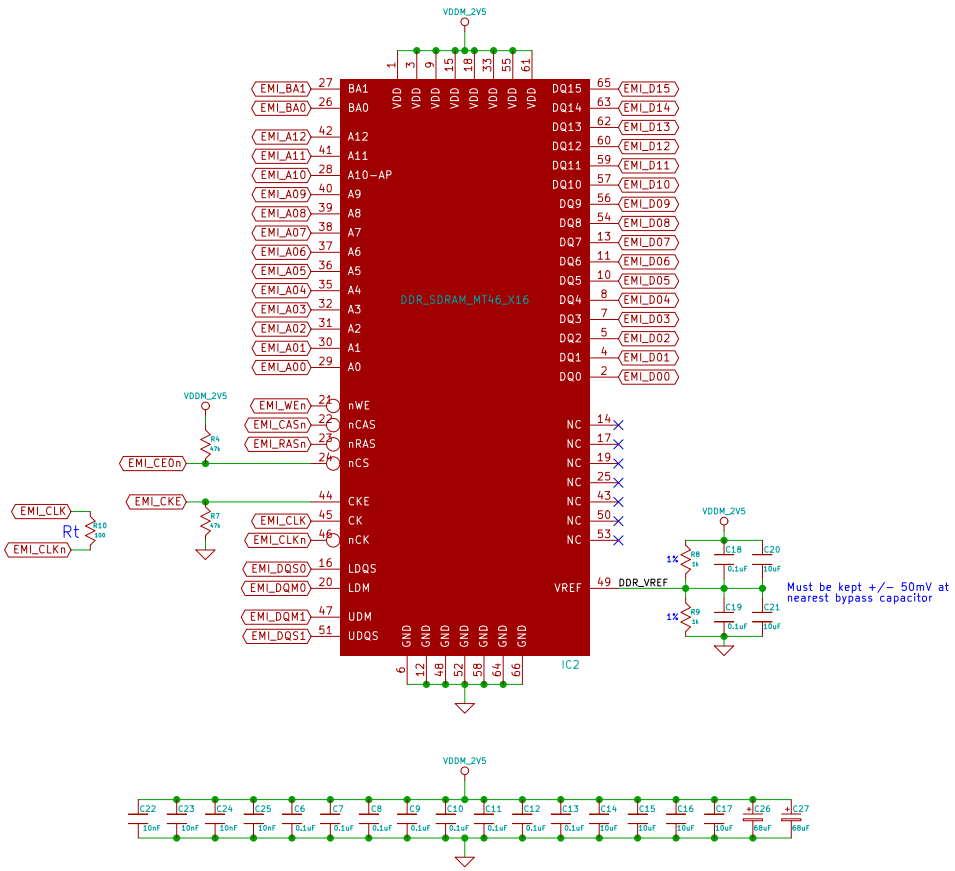
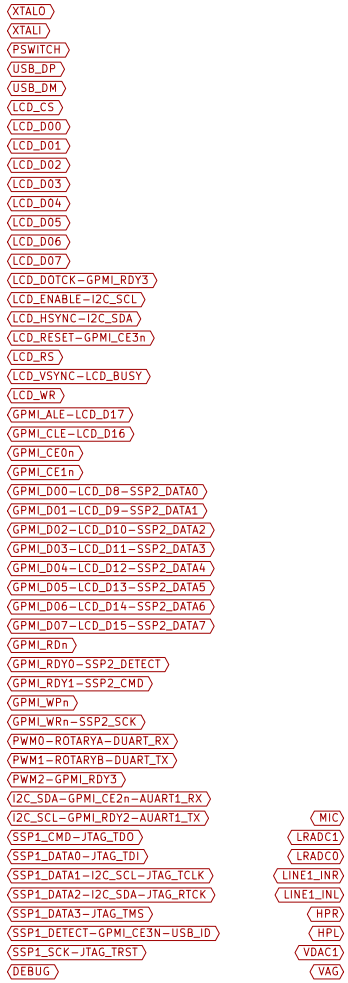
64Mbytes DDR SDRAM





Notes:
- All capacitors are X5R, 6.3V, 20%, ESR < 300mOhm or better unless otherwise noted
- All resistors are 5% unless otherwise noted
- Assuming 2-layer standard 1/16" FR4 (Dk=4.4, Df=0.02) and 7mil-7mil-15mil trace-space-via design
- Per Micron TN-46-02, need about 7 0.1uF bypass caps but to meet TN-46-14 layout guidelines use a lot more and consider external copper foil reference planes
- Cannot meet DDR routing guidelines (TN-46-14) on 2-layers so just keep all signals as short as possible to limit effects on Signal Integrity (SI) of incorrect inter/intra-pair spacing
- Only using one DDR device so should not require Rt

DDR_VREF DDR_VREF

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