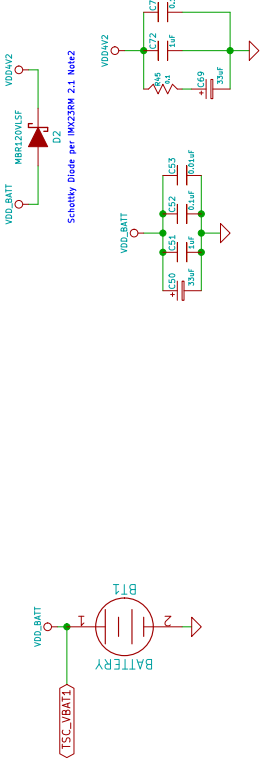
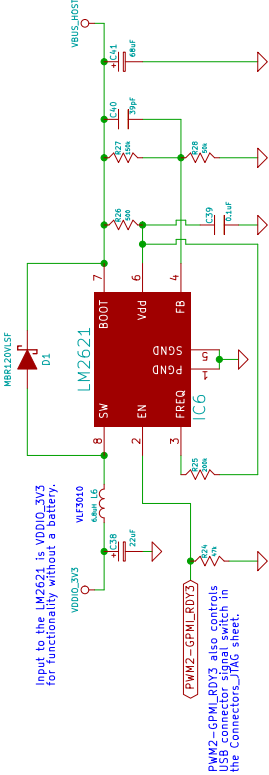


- Notes:
- Capacitors are X7R, 6.3V, 20% or better unless otherwise noted
 - Tantalum capacitors should have ESR < 300mOhm
 - VDDIO, VDDD, VDDA should have 33uF of decoupling and LPF >1MHz per IMX23RM 32.2.1.1
 - The Connectors_JTAG sheet contains power selection circuitry as it is relevant to the USB connector



USB Host Mode VBUS Generator



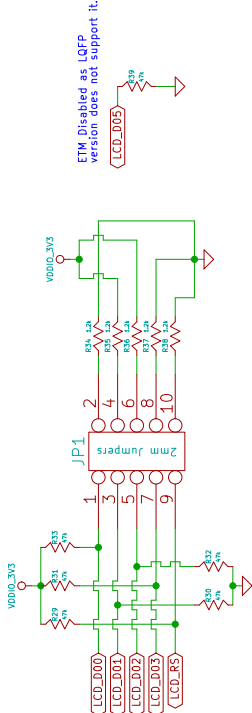
Input to the LM2621 is VDDIO_3V3 for functionality without a battery.

PMW2-GPM1-RV3 also controls USB connector JTAG in the Connectors_JTAG sheet.

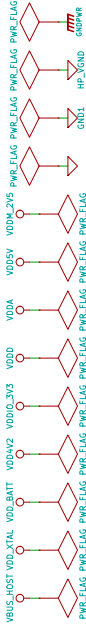
Boot Mode Selection

Boot Mode	LCD_Data
USB	3 2 1 0
I2C Master	0 0 0 0
SPI SSP1 Flash	0 0 0 1
SPI SSP2 Flash	0 0 1 0
GPMI (NAND)	0 1 0 0
DEBUG (JTAG_WAIT)	0 1 1 0
SPI SSP2 EEPROM	1 0 0 0
SD/MMC 1 (Default)	1 0 0 1
SD/MMC 2	1 0 1 0

Default Boot is SD/MMC1, the captive microSD card. Jumpers can be mounted to alter the default setting. LCD_RS High enables JTAG per boot. LCD_RS Low enables OIP boot.



ETM Disabled as LQFP version does not support it.



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File: PROpendous-Power_Boot.sch

Sheet: /Power_Boot/

Title: PROpendous

Size: A4 Date: 19 apr 2010

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Rev: 1.0

Id: 7/8