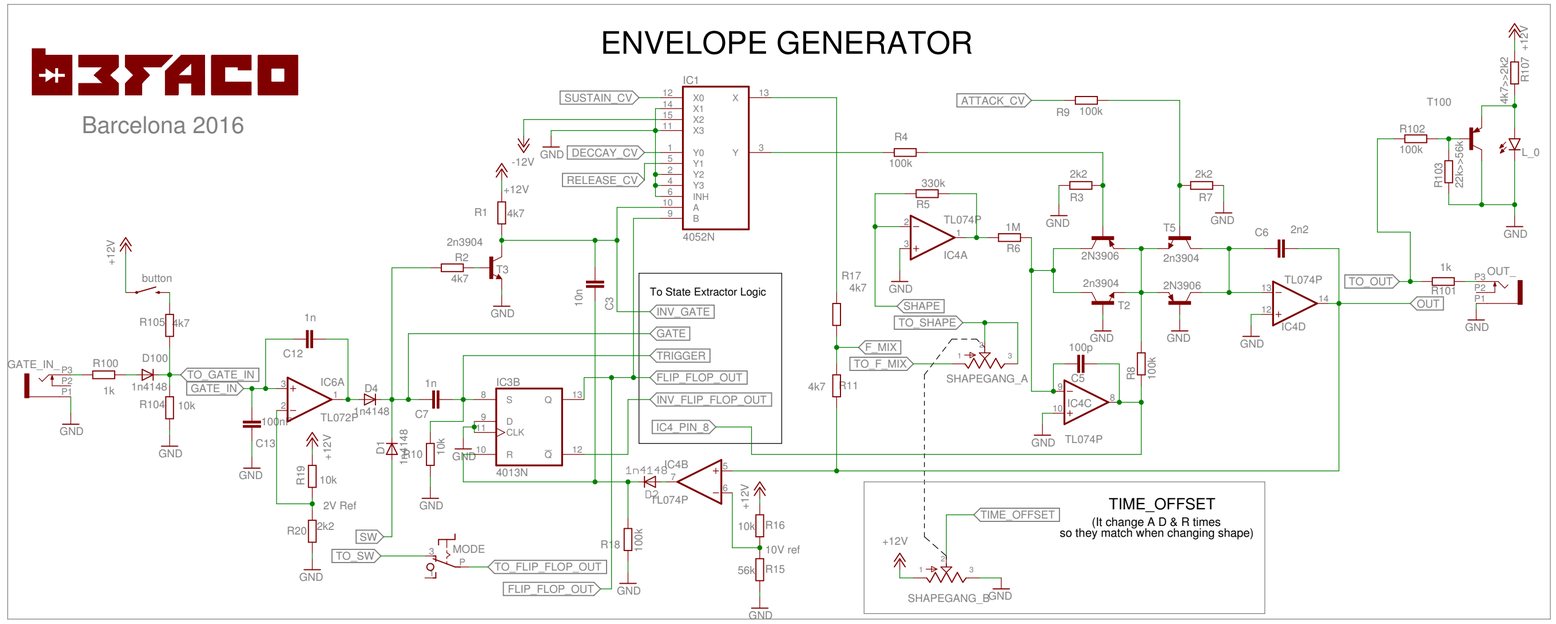


IC3 CD4052

Control: Pin 9 Pin10 — Active IN: — Outs: Pin 13 Pin 3

B	A	X&Y	X	Y
1	0	2	1	0
0	0	0	Sustain CV	Deccay CV
0	0	0	Sustain CV	Deccay CV
0	1	1	0	Release CV

IC7 CD4013

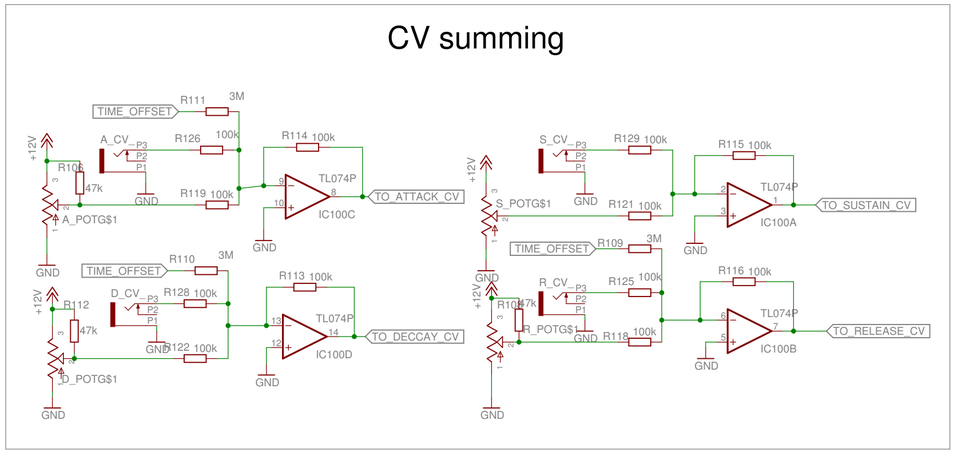
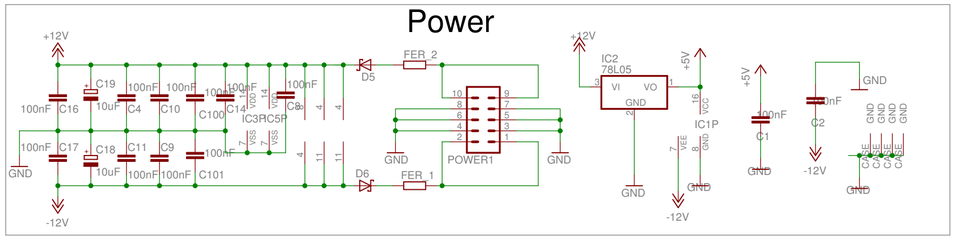
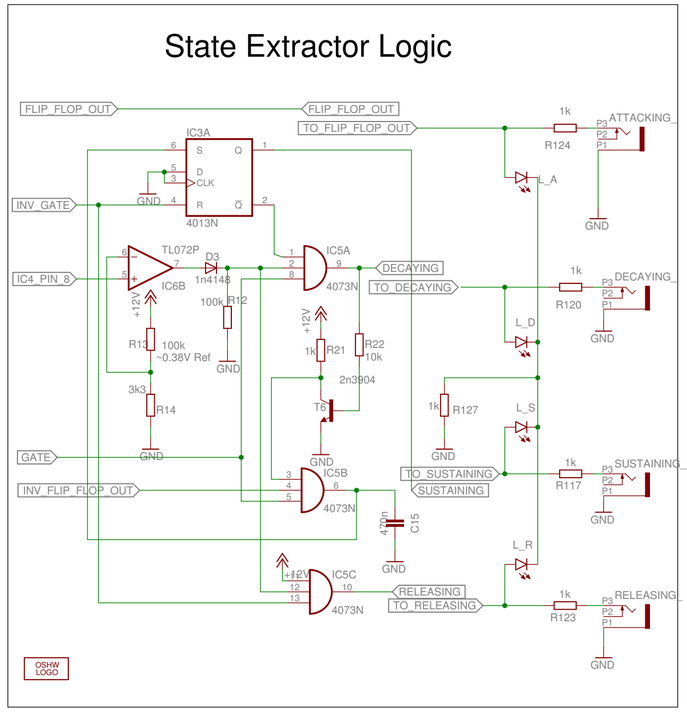
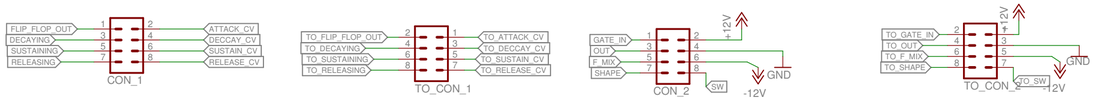
B Outs: Pin 13 Pin 12 — B INs: Pin 8 Pin 9 Pin 10 Pin 11 — A Outs: Pin 1 Pin 2 — A INs: Pin 6 Pin 5 Pin 4 Pin 3

Q	$\overline{Q}$	S	D	R	CLK	Q	$\overline{Q}$	S	D	R	CLK
1	0	Short Pulse at Start	0	0	0	1	0	0	0	0	0
0	1	0	0	Short Pulse at Start	0	0	1	Short Pulse at Start	0	Short Pulse at Start	0
0	1	0	0	Short Pulse at Start	0	0	1	0	0	Short Pulse at Start	0
0	1	0	0	0	0	0	1	0	0	0	0

IC8 CD4073

GATE A: Pin 1 Pin 2 Pin 8 Pin 9 — GATE B: Pin 3 Pin 4 Pin 5 Pin 6 — GATE C: Pin 11 Pin 12 Pin 13 Pin 10

IN	IN	IN	OUT	IN	IN	IN	OUT	IN	IN	IN	OUT
0	0	1	0	1	0	1	0	1	0	0	0
1	1	1	1	0	1	1	0	1	1	0	0
0	0	1	0	1	1	1	1	1	0	0	0
0	1	0	0	1	1	0	0	1	1	1	1

Attack
Decay
Sustain
Release

Attack
Decay
Sustain
Release