

Carlos Alberto da Silva Carvalho de Freitas

# **A Theory of Communicating Sequential Processes in Coq**

Recife

2020

Carlos Alberto da Silva Carvalho de Freitas

# **A Theory of Communicating Sequential Processes in Coq**

A B.Sc. Dissertation presented to the Centro de Informática of Universidade Federal de Pernambuco in partial fulfillment of the requirements for the degree of Bachelor in Computer Engineering.

Universidade Federal de Pernambuco

Centro de Informática

Bachelor in Computer Engineering

Supervisor: Gustavo Henrique Porto de Carvalho

Recife

2020

# Acknowledgements

# Abstract

Theories of concurrency such as Communicating Sequential Processes (CSP) allow system specifications to be expressed clearly and analyzed with precision. However, the state explosion problem, common to model checkers in general, is a real constraint when attempting to verify system properties for large systems. An alternative is to ensure these properties via proof development. This work will provide an approach on how we can develop a theory of CSP in the Coq proof assistant, and evaluate how this theory compares to other theorem prover-based frameworks for the process algebra CSP. We will implement an infrastructure for declaring syntactically and semantically correct CSP specifications in Coq, along with native support for process representation through Labelled Transition Systems (LTSs), in addition to traces refinement analysis.

**Keywords:** Process algebra. LTS. Traces refinement. CSP. Proof assistant. Coq. QuickChick.

# Resumo

Teorias de concorrência tais como *Communicating Sequential Processes* (CSP) permitem que especificações de sistemas sejam descritas com clareza e analisadas com precisão. No entanto, o problema da explosão de estados, comum aos verificadores de modelo em geral, é uma limitação real na tentativa de verificar propriedades de um sistema complexo. Uma alternativa é garantir essas propriedades através do desenvolvimento de provas. Este trabalho fornecerá uma abordagem sobre como se pode desenvolver uma teoria de CSP no assistente de provas Coq, além de compará-la com outros frameworks baseados em provadores de teoremas para a álgebra de processos CSP. Portanto, será implementada uma infraestrutura para declarar especificações sintática e semanticamente corretas de CSP em Coq, juntamente com um suporte nativo para a representação de processos por meio de Sistemas de Transições Rotuladas (LTSs), além de análise de refinamento no modelo de *traces*.

**Palavras-chave:** Álgebra de processos. LTS. Refinamento no modelo de *traces*. CSP. Assistente de provas. Coq. QuickChick.

# List of Figures

Figure 1 – The process LTS graph . . . . .	11
--	----

# List of Tables

Table 1 – The ASCII representation of CSP . . . . .	20
Table 2 – Proof of Example elem_not_in_list . . . . .	24
Table 2 – Proof of Example elem_not_in_list continued . . . . .	25
Table 2 – Proof of Example elem_not_in_list continued . . . . .	26
Table 3 – Proof of Example elem_not_in_list' . . . . .	27

# List of abbreviations and acronyms

CSP	Communicating Sequential Processes
FDR	Failures-Divergence Refinement
LTS	Labelled Transition System
SOS	Structured Operational Semantics



# Contents

<b>1</b>	<b>INTRODUCTION</b>	<b>9</b>
<b>1.1</b>	<b>Objectives</b>	<b>9</b>
<b>1.2</b>	<b>An overview of CSP<sub>Coq</sub></b>	<b>10</b>
<b>1.3</b>	<b>Main contributions</b>	<b>11</b>
<b>1.4</b>	<b>Document structure</b>	<b>12</b>
<b>2</b>	<b>BACKGROUND</b>	<b>13</b>
<b>2.1</b>	<b>Communicating sequential processes</b>	<b>13</b>
2.1.1	Structured operational semantics	16
2.1.2	Traces refinement	19
2.1.3	Machine-readable version of CSP	19
<b>2.2</b>	<b>The Coq proof assistant</b>	<b>20</b>
2.2.1	Building proofs	22
2.2.2	The tactics language	23
<b>2.3</b>	<b>QuickChick</b>	<b>27</b>
<b>3</b>	<b>A THEORY FOR CSP IN COQ</b>	<b>28</b>
<b>3.1</b>	<b>Syntax</b>	<b>28</b>
3.1.1	Abstract syntax	28
3.1.2	Concrete syntax	28
<b>3.2</b>	<b>Structured operational semantics</b>	<b>28</b>
<b>3.3</b>	<b>Labelled transition systems</b>	<b>28</b>
3.3.1	GraphViz integration	28
<b>3.4</b>	<b>Traces refinement</b>	<b>28</b>
3.4.1	QuickChick integration	28
<b>4</b>	<b>CONCLUSIONS</b>	<b>29</b>
<b>4.1</b>	<b>Related work</b>	<b>29</b>
<b>4.2</b>	<b>Future work</b>	<b>29</b>
	<b>BIBLIOGRAPHY</b>	<b>30</b>

# 1 Introduction

Concurrency is an attribute of any system that allows multiple components to perform operations at the same time. The understanding of this property is essential in modern programming because major areas, such as distributed and real-time systems, rely on this concept to work properly. As a result, the variety of applications enabled by the concurrency feature is broad: aircraft and industrial control systems, routing algorithms, peer-to-peer networks, client-server applications and parallel computation, to name a few.

Since concurrent systems may have parts that execute in parallel, the combination of ways in which these parts can interact raises the complexity in designing such systems. Phenomena like deadlock, livelock, nondeterminism and race condition can emerge from these interactions, so these issues must be addressed in order to avoid undesired behavior. Typically, testing cannot provide enough evidence to guarantee properties such as deadlock freedom, divergence freedom and determinism for a given system.

That being said, CSP (a theory for Communicating Sequential Processes) introduces a convenient notation that allows systems to be described in a clear and accurate way. More than that, it has an underlying theory that enables designs to be analysed and proven correct with respect to desired properties. The FDR (Failures-Divergence Refinement) tool is a model checker for CSP responsible for making this process algebra a practical tool for specification, analysis and verification of systems. System analysis is achieved by allowing the user to make assertions about processes and then exploring every possible behavior, if necessary, to check the truthfulness of the assertions made.

Although it is undeniable that FDR is a useful tool in the analysis of systems described in CSP, it has a limitation common to standard model checkers in general: the state explosion problem. An alternative way for deciding whether a system meets its specification is by proof development. Examples of this different approach are CSP-Prover and Isabelle/UTP, both frameworks based on the theorem prover Isabelle. Nevertheless, to the best of our knowledge, there is not a theory for CSP in the Coq proof assistant yet. Considering that, the main research question of this work is the following: how could we develop a theory of CSP in Coq, exploiting the main advantages of this proof assistant?

## 1.1 Objectives

The main objective (MO) of this work is to define in Coq a theory for concurrent systems, based on a limited scope of the process algebra CSP. This objective is unfolded into the following specific objectives (SO):

- SO1: study CSP and frameworks based on this process algebra.
- SO2: define a syntax for CSP in Coq, based on a restricted version of the  $CSP_M$  language (machine readable language for CSP).
- SO3: provide support for the LTS-based (Labelled Transition System) representation, considering the Structured Operational Semantics (SOS) of CSP.
- SO4: make use of the QuickChick tool to search for counterexamples of the traces refinement relation.

## 1.2 An overview of $CSP_{Coq}$

Consider the following CSP process adapted from [Schneider \(1999, p. 32, example 2.3\)](#). This process represents a cloakroom attendant that might help a costumer off or on with his coat, storing an retrieving coats as appropriate:

*channel coat\_off, coat\_on, store, retrieve, request\_coat, eat*

*SYSTEM = coat\_off -> store -> request\_coat -> retrieve -> coat\_on -> SKIP*  
*[[ {coat\_off, request\_coat, coat\_on} ]]*  
*coat\_off -> eat -> request\_coat -> coat\_on -> SKIP*

We can declare such system in  $CSP_{Coq}$  by defining a specification, which consists in lists of channels and processes. This specification must also abide by a set of contextual rules that will be discussed further in this work.

**Definition** *example : specification.*

**Proof.**

```
solve_spec_ctx_rules (
  Build_Spec
  [ Channel {{"coat_off", "coat_on", "request_coat", "retrieve", "store", "eat"}} ]
  [ "SYSTEM" ::=
    "coat_off" -> "store" -> "request_coat" -> "retrieve" -> "coat_on" -> SKIP
    [[ {{"coat_off", "request_coat", "coat_on"}} ] ]
    "coat_off" -> "eat" -> "request_coat" -> "coat_on" -> SKIP ]
).
```

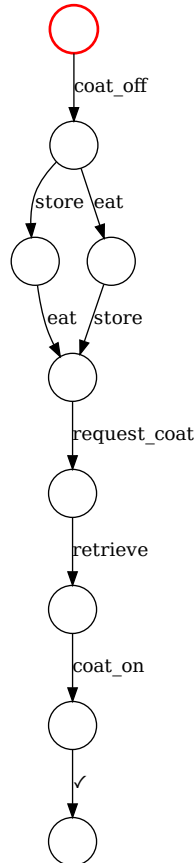
**Defined.**

Furthermore, we can execute the following command to compute the process LTS and output the graph in dot language:

**Compute** *generate\_dot (compute\_ltsR example "SYSTEM" 100).*

The [Figure 1](#) is a visual representation of the graph outputted by this command. The image is generated using GraphViz software.

Figure 1 – The process LTS graph.



### 1.3 Main contributions

The main contributions of this work are the following:

- Abstract and concrete syntax for a subset of CSP operators.
- Context rules for CSP specifications.
- Operational semantics via SOS approach.
- Inductive and functional definitions of a labelled transition system.
- Proof of correctness for the functional definition of the LTS.
- Inductive and functional definitions of traces.

- Proof of correctness for the functional definition of traces.
- Tactic macro that automates trace relation proofs.
- Formal definition of the traces refinement.
- Refinement verification using QuickChick.

## 1.4 Document structure

Apart from this introductory chapter, in which we discuss about the motivation behind this work and its main objective, and also take a quick look at an example that illustrates what can be done using the framework developed, this monograph contains three more chapters. The content of these chapters are detailed bellow:

**Chapter 2** Discusses fundamental concepts such as CSP theory, SOS approach, trace refinement and LTS representation. Moreover, this chapter introduces the Coq proof assistant and its functional language Gallina, along with an introduction to proof development (tactics) and the Ltac language inside this tool, which gives support for developing tactic macros.

**Chapter 3** Provides an in-depth look at the implementation of  $\text{CSP}_{\text{Coq}}$ , including its abstract and concrete syntax, and language semantics. Furthermore, the LTS process representation support, using the GraphViz software, is also detailed in this chapter.

**Chapter 4** Concludes this monograph by presenting a comparison between the infrastructure described in this work and other interactive theorem provers based on CSP. It also addresses possible topics for future work.

## 2 Background

Before jumping into the specifics of the implementation of  $\text{CSP}_{\text{Coq}}$ , we need to understand some elements of the CSP language itself, such as the concrete syntax and the semantics defined in both denotational and operational models (section 2.1). Beyond that, it is also important to provide an overview of what an interactive theorem prover is: the Coq proof assistant fundamentals such as tactics, as well as the embedded Ltac language (section 2.2). We must also address the QuickChick property-based testing tool (section 2.3), which is the Coq implementation of QuickCheck (CLAESSEN; HUGHES, 2000). This chapter gives an introduction to each one of these concepts.

### 2.1 Communicating sequential processes

In 1978, Tony Hoare’s *Communicating Sequential Processes* (HOARE, 1978) described a theory to help us understand concurrent systems, parallel programming and multiprocessing. More than that, it has introduced a way to decide whether a program meets its specification. This theory quickly evolved into what is known today as the CSP programming language. This language belongs to a class of notations known as process algebras, where concepts of communication and interaction are presented in an algebraic style.

Since the main goal of CSP is to provide a theory-driven framework for designing systems of interacting components and reasoning about them, we must introduce the concept of a component, or as we will be referencing it from now on, a *process*. Processes are self-contained entities that once combined they can describe a system, which is yet another larger process that may itself be combined as well with other processes. The way a process communicates with the environment is through its *interface*. The interface of a process is the set of all the events that the process has the potential to engage in. At last, an *event* represents the atomic part of the communication itself. It is the piece of information the processes rely on to interact with one another. A process can either participate actively or passively in a communication, depending on whether it performed or suffered the action. Events may be external, meaning they appear in the process interface; indicate termination, represented by the event  $\checkmark$ ; or be internal, and therefore unknown for the environment, denoted by the event  $\tau$ .

The most basic process one can define is *STOP*. Essentially, this process never interacts with the environment and its only purpose is to declare the end of an execution. In other words, it illustrates a deadlock: a state in which the process can not engage in any event or make any progress whatsoever. It could be used to describe a computer that

failed booting because one of its components is damaged, or a camera that can no longer take pictures due to storage space shortage.

Another simple process is *SKIP*. It indicates that the process has reached a successful termination state, which also means that it has finished executing. We can use *SKIP* to illustrate an athlete that has crossed the finish line, or a build for a project that has passed.

Provided these two trivial processes, *STOP* and *SKIP*, and the knowledge of what a process interface is, we can apply a handful of CSP operators to define more descriptive processes. For example, let  $a$  be an event in the process  $P$  interface. One can write the new process  $P$  as  $a \rightarrow STOP$ , meaning that this process behaves as *STOP* after performing  $a$ . This operator is known as the *event prefix*, and it is pronounced as “then”.

The choice between processes can be constructed in two different ways in CSP: externally and internally. An *external choice* between two processes implies the ability to perform any event that either process can engage in. Therefore, the environment has control over the outcome of such decision. On the other hand, if the process itself is the only responsible for deciding which event from its interface will be communicated, thus which process it will resolve to, then we call it an *internal choice*. Note that this operator is essentially a source of non-deterministic behavior.

To illustrate the difference between these choice operators, consider the following scenario: a cafeteria may operate by either letting the costumers choose between ice cream and cake for desert, or by making this choice itself (employees decide), having the clients no take on what deserts they will get. In the first specification, the choice is external to the business and it might be described as  $ice\_cream \rightarrow SKIP \sqcap cake \rightarrow SKIP$ , whereas it is internal in the latter, thus  $ice\_cream \rightarrow SKIP \sqcap cake \rightarrow SKIP$  would capture such business rule.

CSP introduces two approaches for describing a parallel execution between processes: the *alphabetized parallel* and the *generalized parallel*. Let  $A$  be the interface of process  $P$ , and  $B$  the interface of process  $Q$ . An alphabetized parallel combination of these processes is described as  $P \parallel_B Q$ . Events in the intersection of  $A$  and  $B$  must be simultaneously engaged in by the processes  $P$  and  $Q$ . In other words, an event that appears in both process interfaces can only be communicated if the two processes are ready to perform this event. Any other event that does not match this criteria can be engaged in by its corresponding process independently. The semantics are similar for the generalized version of the parallel operator. The only change being its constructor, that takes the synchronization alphabet alone as the interface argument the processes must agree upon. Let  $C$  be the intersection of previously defined interfaces  $A$  and  $B$ . The generalized parallel between process  $P$  and  $Q$  is written as  $P \parallel_C Q$ .

Both versions of the parallel operator may be used to describe a marathon where every participant is a process that runs in parallel with each other. They must all start the race at the same time, but they are not expected to cross the finish line all together. We can use the alphabetized parallel to specify the combination between two participants as  $RUNNER1 \text{ }_{\{start, finish1\}} \parallel_{\{start, finish2\}} RUNNER2$ , or use the generalized version of the operator instead:  $RUNNER1 \parallel_{\{start\}} RUNNER2$ .

Another CSP operator that provides a concurrent execution of processes is the *interleaving* operator. Different from the parallel operators, the interleaving represents a combination of processes that do not require any synchronization at all. The processes applied to this operation execute totally independent of each other. This might be the case of two vending machines at a supermarket. They operate completely separate from each other, receiving payments, processing changes and releasing snacks. In other words, there is no dependency regarding the communication of events between the vending machines. That being said consider the process *VENDING\_MACHINE* as  $pay \rightarrow select\_snack \rightarrow return\_change \rightarrow release\_snack$ . Then, the process that specifies both machines operating together is described as  $VENDING\_MACHINE \parallel VENDING\_MACHINE$ .

The last two operators we will be discussing are the *sequential composition* and *event hiding*. Before we continue, the reader must be aware that there are others CSP operators for combining processes apart from the ones presented in this chapter, but they will not be supported by the framework implemented in this project.

Sometimes it is necessary to pass the control over execution from one process to another, and for that we use sequential composition. It means that the first process has reached a successful termination state and now the system is ready to behave as the second process in the composition. Parents can choose to let their children play only after completing their homework. That being the case, the process *CHILD* could be modeled as  $HOMWORK; FUN$ , where

$$\begin{aligned} HOMWORK &= choose\_subject \rightarrow study \rightarrow answer\_exercises \rightarrow SKIP \\ FUN &= build\_lego \rightarrow watch\_cartoons \rightarrow play\_videogame \rightarrow SKIP \end{aligned}$$

In this example, the process *FUN* can only be executed after the process *HOMWORK* has successfully terminated.

Last but not least, we have the event hiding operator. A system designer may choose to hide events from a process interface to prevent them from being recognized by other processes. That way, the environment can not distinguish this particular event, thus no process can engage in it. Event hiding proves to be useful when processes placed in parallel should not be allowed to synchronize on certain events. Consider, for example, that a school teacher is communicating each student individually his or her test grade. It has to be done in such way that no student gets to know other test grades besides his or her own. The process



*TEACHER* may be modeled as  $show\_grade \rightarrow discuss\_questions \rightarrow SKIP$ , so a teacher concerned with the students privacy can be described as  $TEACHER \setminus \{show\_grade\}$ .

### 2.1.1 Structured operational semantics

There are three major complementary approaches for describing and reasoning about the semantics of CSP programs. These are through *algebraic*, *denotational* (also called *behavioral*), and *operational semantics*. We will be focusing in the last one, which tries to understand all the actions and decisions that process implementations can make as they proceed.

The operational semantics for CSP language describes how a valid program is interpreted as sequences of computational steps. By evaluating the initial events of a process and finding out how it will behave immediately after performing them, this approach enables us to explore the state space of any process. All we need to do is repeat this step until we have covered the transition system picture of the process we are interested in.

It is traditional to present operational semantics as a logical inference system: Plotkin's SOS, or *Structured Operational Semantics* style. A process has a given action if, and only if, that is deducible from the rules given.

We start by analyzing the process *STOP*. Since it is unable to engage in any event whatsoever, there are no inference rules for it. Then, we move forward to the next primitive process: *SKIP*. While *STOP* has no actions of itself, *SKIP* is able to perform a single event, which is the termination event  $\checkmark$ . The lack of antecedents in the following rule means it is always the case that *SKIP* may perform  $\checkmark$  and behave as *STOP*.

$$\frac{}{SKIP \xrightarrow{\checkmark} STOP}$$

The event prefix operation also spares the antecedents in its inference rule, so the conclusion is immediately deduced: if the process is initially able to perform  $a$ , then after performing  $a$  it behaves like  $P$ .

$$\frac{}{(a \rightarrow P) \xrightarrow{a} P}$$

The transition rules for external choice reflect the fact that the first external event resolves the choice in favor of the process performing the event. In addition, as we can see in the first two rules, the choice is not resolved on the occurrence of internal events. Control over resolution of the choice is external because the events of both choices are initially available.

$$\frac{P \xrightarrow{\tau} P'}{P \sqcap Q \xrightarrow{\tau} P' \sqcap Q}$$

$$\frac{Q \xrightarrow{\tau} Q'}{P \sqcap Q \xrightarrow{\tau} P \sqcap Q'}$$

$$\frac{P \xrightarrow{a} P'}{P \sqcap Q \xrightarrow{a} P'} \quad (a \neq \tau)$$

$$\frac{Q \xrightarrow{a} Q'}{P \sqcap Q \xrightarrow{a} Q'} \quad (a \neq \tau)$$

The internal choice is an operation that guarantees the process to behave as either of its components on any execution. This state change happens “silently”, thus this transition is followed by the communication of internal event  $\tau$ , as we can see in the inference rules for this operation.

$$\frac{}{P \sqcap Q \xrightarrow{\tau} P}$$

$$\frac{}{P \sqcap Q \xrightarrow{\tau} Q}$$

We can separate the rules for the alphabetized parallel into two categories: one that describes the independent execution of each process, and other defining the synchronized step performed at once by the components. The first two inference rules capture the ability of both sides performing events that are not in the common interface, thus executing them independently. The third rule dictates the joint step, where both processes are able to perform the event, so they communicate it at the same time.

$$\frac{P \xrightarrow{\mu} P'}{P \sqcap_A B \xrightarrow{\mu} P' \sqcap_A B} \quad (\mu \in (A \cup \{\tau\} \setminus B))$$

$$\frac{Q \xrightarrow{\mu} Q'}{P \sqcap_A B \xrightarrow{\mu} P \sqcap_A B \xrightarrow{\mu} Q'} \quad (\mu \in (B \cup \{\tau\} \setminus A))$$

$$\frac{P \xrightarrow{a} P' \quad Q \xrightarrow{a} Q'}{P \sqcap_A B \xrightarrow{a} P' \sqcap_A B \xrightarrow{a} Q'} \quad (a \in A^\vee \cap B^\vee)$$

The transition rules for the generalized parallel are very similar to the ones for the previous operation. The main difference lies in the side condition, since this version of parallelism is only interested in the interface alphabet. The same rule categories for the alphabetized parallel apply to this operation.

$$\frac{P \xrightarrow{\mu} P'}{P \parallel_A Q \xrightarrow{\mu} P' \parallel_A Q} \quad (\mu \notin A^\vee)$$

$$\frac{Q \xrightarrow{\mu} Q'}{P \underset{A}{\parallel} Q \xrightarrow{\mu} P \underset{A}{\parallel} Q'} \quad (\mu \notin A^\vee)$$

$$\frac{P \xrightarrow{a} P' \quad Q \xrightarrow{a} Q'}{P \underset{A}{\parallel} Q \xrightarrow{a} P' \underset{A}{\parallel} Q'} \quad (a \in A^\vee)$$

The interleave operation describes a parallel execution between processes that do not synchronize in any event except termination  $\checkmark$ . In other words, this operation is a particular case of the generalized parallelism, where the interface alphabet is empty, thus the event  $\checkmark$  being the only event that can be performed simultaneously by the components.

$$\frac{P \xrightarrow{\mu} P'}{P \parallel\!\!\parallel Q \xrightarrow{\mu} P' \parallel\!\!\parallel Q} \quad (\mu \neq \checkmark)$$

$$\frac{Q \xrightarrow{\mu} Q'}{P \parallel\!\!\parallel Q \xrightarrow{\mu} P \parallel\!\!\parallel Q'} \quad (\mu \neq \checkmark)$$

$$\frac{P \xrightarrow{\checkmark} P' \quad Q \xrightarrow{\checkmark} Q'}{P \parallel\!\!\parallel Q \xrightarrow{\checkmark} P' \parallel\!\!\parallel Q'}$$

As we already know, the hiding operator removes all events in a given alphabet from the process interface, preventing other processes to engage in them. The process to which the event hiding is applied can then behave just like it would without the operator, except the events in the given alphabet are made internal and then renamed to  $\tau$ . Such behavior is capture by the inference rules:

$$\frac{P \xrightarrow{a} P'}{P \setminus A \xrightarrow{\tau} P' \setminus A} \quad (a \in A)$$

$$\frac{P \xrightarrow{\mu} P'}{P \setminus A \xrightarrow{\mu} P' \setminus A} \quad (\mu \notin A)$$

The last operational rules we need to discuss are for the sequential composition operator. Initially, this combination behaves as the process to the left of the operator until it terminates. Then, the execution control is granted to the other process in the composition. The control handover is represented by the communication of the internal event  $\tau$ , as we can see in the second rule:

$$\frac{P \xrightarrow{a} P'}{P; Q \xrightarrow{a} P'; Q} \quad (a \neq \checkmark)$$

$$\frac{P \xrightarrow{\checkmark} P'}{P; Q \xrightarrow{\tau} Q}$$

### 2.1.2 Traces refinement

A pretty reasonable way for gathering information from a process interacting with the environment is by keeping track of the events this process engages in. This sequence of communication between process and environment, presented in a chronological order, is what we call a *trace*. Traces can either be finite or infinite, and it depends on the observation span and the nature of the process itself.

Because this record is easily observed by the environment and it represents a single interaction, it is often used to build models of CSP processes. As a matter of fact, there is one named after it: the *traces* model, represented by the symbol  $\mathcal{T}$ . It defines the meaning of a process expression as the set of sequences of events (traces) that the process can be observed to perform. This model is one of the three major denotational models of CSP, the other ones being the *stable failures*  $\mathcal{F}$  and the *failures-divergences* model  $\mathcal{N}$ .

The notion of refinement is a particularly useful concept for specifying the correctness of a CSP process. If we can establish a relation between components of a system which captures the fact that one satisfies at least the same conditions as another, then we may replace a worse component by a better one without degrading the properties of the system.

**Definition. (Traces Refinement)** Let  $P$  and  $Q$  be two CSP processes, and *traces* be a function that yields the set of all possible traces of a given CSP process, we say that  $Q$  trace-refines  $P$  if, and only if, every trace of  $Q$  is also a trace of  $P$ :

$$P \sqsubseteq_{\mathcal{T}} Q \iff \text{traces}(Q) \subseteq \text{traces}(P)$$

If we consider  $P$  to be a specification which determines possible safe states of a system, then we can think of  $P \sqsubseteq_{\mathcal{T}} Q$  as saying that  $Q$  is a safe implementation: no wrong events will be allowed.

### 2.1.3 Machine-readable version of CSP

In the beginning, CSP was typically used as a blackboard language. In other words, it was conceived to describe communicating and interacting processes for a human audience. Theories such as CSP have a higher chance of acceptance among the industry and academy (e.g. for teaching purposes) when they have tool support available. For that reason, the need of a notation that could actually be used with tools emerged.

The machine-readable CSP, usually denoted as  $\text{CSP}_M$ , not only provides a notation for tools such as FDR model-checker to be build upon but also extends the existing theory by using a functional programming language to describe and manipulate things like events and process parameters.

The Table 1 shows for every CSP process constructor discussed in section 2.1 the corresponding ASCII representation according to  $\text{CSP}_M$  language.

Table 1 – The ASCII representation of CSP.

Constructor	Syntax	ASCII form
Stop	$STOP$	STOP
Skip	$SKIP$	SKIP
Event prefix	$e \rightarrow P$	e -> P
External choice	$P \sqcap Q$	P    Q
Internal choice	$P \sqcap Q$	P  ~  Q
Alphabetized parallel	$P \sqcap_A B Q$	P [A    B] Q
Generalized parallel	$P \parallel_A Q$	P    A    Q
Interleave	$P \parallel\parallel Q$	P     Q
Sequential composition	$P ; Q$	P ; Q
Event hiding	$P \setminus A$	P \ A

## 2.2 The Coq proof assistant

A proof assistant is a software for helping construct proofs of logical propositions. Essentially, it is a hybrid tool that automates the more routine aspects of building proofs while relying on human intervention for more complex steps. There is a variety of proof assistants including Isabelle, Agda, ATS, Idris and Coq, among others. This work is based around the Coq proof assistant.

Coq can be viewed as a combination of a functional programming language plus a set of tools for stating and proving logical assertions. Moreover, the Coq environment provides high-level facilities for proof development, including a large library of common definitions and lemmas, powerful tactics for constructing complex proofs semi-automatically, and a special-purpose programming language for defining new proof-automation tactics for specific situations.

Coq’s native functional programming language is called *Gallina*. Before we discuss about the proof development aspect of this interactive theorem prover, we need to introduce the most essential elements we may find in a Gallina program. Consider the following definition of natural numbers in Coq:

```
Inductive nat : Type :=
| O
```

|  $S (n : nat)$ .

This declaration tells Coq that we are defining a *type*. The capital-letter  $O$  constructor represents zero. When the  $S$  constructor is applied to the representation of the natural number  $n$ , the result is the representation of  $n + 1$ , where  $S$  stands for "successor". An **Inductive** definition carves out a subset of the whole space of constructor expressions and gives it a name, in this case,  $nat$ .

Having defined  $nat$ , we can write functions that operate on natural numbers, such as the predecessor function:

```
Definition  $pred (n : nat) : nat :=$ 
  match  $n$  with
    |  $O \Rightarrow O$ 
    |  $S n' \Rightarrow n'$ 
  end.
```

Note that we do not need recursion to define the predecessor function, but simple pattern matching is not enough for more interesting computations involving natural numbers. For example, to check that a number  $n$  is even, we may need to recursively check whether  $n - 2$  is even. In order to do that, we use the keyword **Fixpoint** instead of **Definition**:

```
Fixpoint  $evenb (n:nat) : bool :=$ 
  match  $n$  with
    |  $O \Rightarrow true$ 
    |  $S O \Rightarrow false$ 
    |  $S (S n') \Rightarrow evenb n'$ 
  end.
```

Yet another way for defining evenness is through *inductive declaration*. Consider the following two rules: *the number 0 is even*, and *if  $n$  is even, then  $S (S n)$  is even*. Lets call the first rule  $ev\_0$  and then the second  $ev\_SS$ . Using  $ev$  for the name of evenness property, we can write the following inference rules:

$$\frac{}{ev\ 0} \quad (ev\_0)$$

$$\frac{ev\ n}{ev\ (S\ (S\ n))} \quad (ev\_SS)$$

Now, we can translate these rules into a formal Coq definition. Each constructor in this definition corresponds to an inference rule:

```
Inductive  $ev : nat \rightarrow Prop :=$ 
  |  $ev\_0 : ev\ 0$ 
```

|  $ev\_SS\ (n : nat)\ (H : ev\ n) : ev\ (S\ (S\ n))$ .

This definition is different from previous use of **Inductive**. We are defining a function from *nat* to *Prop*, in other words, a property of numbers. The type of each constructor must be specified explicitly (after a colon), and each constructor's type must have the form *ev n* for some natural number *n*.

### 2.2.1 Building proofs

As a proof development system, Coq provides interactive proof methods, decision and semi-decision algorithms, and a tactic language for letting the user define its own proof methods. Proof development in Coq is done through a language of tactics that allows a user-guided proof process.

For instance, consider the following theorem on natural numbers: for all natural *n*, if *n* is even, then the predecessor of the predecessor of *n* is also even. This theorem can be proved in Coq using the following commands:

**Theorem** *ev\_minus2* :  $\forall\ n:nat,$   
 $ev\ n \rightarrow ev\ (pred\ (pred\ n))$ .

**Proof.**

```
intros.
destruct H.
- simpl. apply ev_0.
- simpl. apply H.
```

**Qed.**

The proof editing mode in Coq is entered whenever asserting a statement. The command **Theorem** does so by allowing us to give the theorem a name and providing it with the proposition we want to prove. Additionally, the commands **Proof** and **Qed** delimit, respectively, the beginning and end of the sequence of tactic commands.

The keywords **intros**, **simpl**, **destruct** and **apply** are examples of tactics. A tactic is a command that is used to guide the process of checking some claim we are making. Note that we have added the quantifier  $\forall\ n:nat$ , so that our theorem talks about all natural numbers *n*. Therefore, the tactic **intros** moves *n* from the quantifier in the goal to a context of current assumptions, along with a new *hypothesis* that consists of the antecedent of the implication (*modus ponens*).

Proving a statement by case analysis is possible via the tactic **destruct**. In our example, this tactic is responsible for generating, from the hypothesis, two sub-goals based on the inductive definition of evenness we have provided: one where  $n = 0$  and the other where  $n = S\ (S\ n)$ . Thus, we must prove them separately in order to get Coq to accept the theorem.

The tactic `simpl` is used in situations where we want to evaluate a compound expression, eventually reducing it to a simplified, easier-to-understand term. It facilitates our decisions in a proof development by resolving all the computations that can be done in a given state of the goal.

We then proceed to use the parameterized tactic `apply`, passing the term we find useful for proving each sub-goal. In the first branch, where our goal is to prove `ev 0`, we apply the first rule of our inductive definition, `ev_0`, which concludes this sub-proof. In the second branch, we have `ev n` as goal, which is already an assumption of ours (introduced to the proof context by the command `intros`). This also concludes the second sub-proof, thus proving the entire statement ([Theorem `ev\_minus2`](#)).

There are many other tactics and variations of them that can be used when proving a statement. Apart from the ones we have already discussed in this subsection, other commonly used tactic commands are `reflexivity`, `rewrite`, `unfold`, `induction`, `inversion` and `contradiction`. Further explanation on these tactics will be provided as needed throughout the [chapter 3](#).

### 2.2.2 The tactics language

Ltac is the tactic language for Coq. It provides the user with a high-level “toolbox” for tactic creation, allowing one to build complex tactics by combining existing ones with constructs such as conditionals, looping, backtracking, and error catching.

Imagine we want to prove that the number 4 does not appear in the list of consecutive natural numbers ranging from 0 to 3. By using the keyword [Example](#), we can assert this statement in Coq and develop our proof:

[Example](#) `elem_not_in_list` :  $\neg (In\ 4\ [0 ; 1 ; 2 ; 3])$ .

[Proof](#).

```
unfold not. simpl. intros.
destruct H.
- inversion H.
- destruct H.
  × inversion H.
  × destruct H.
  + inversion H.
  + destruct H.
    { inversion H. }
    { contradiction. }
```

[Qed](#).

See the proof for Example `elem_not_in_list` in [table 2](#)



Table 2 – Proof of Example elem\_not\_in\_list

Next step in Coq	Proof situation
<i>Proof.</i>	<hr/> $\neg In\ 4\ [0; 1; 2; 3]$
<i>unfold not.</i>	<hr/> $In\ 4\ [0; 1; 2; 3] \rightarrow False$
<i>simpl.</i>	<hr/> $0 = 4 \vee 1 = 4 \vee 2 = 4 \vee 3 = 4 \vee False \rightarrow False$
<i>intros.</i>	<hr/> $H : 0 = 4 \vee 1 = 4 \vee 2 = 4 \vee 3 = 4 \vee False$ <hr/> $False$
<i>destruct H.</i>	$H : 0 = 4$ <hr/> $False$
	$H : 1 = 4 \vee 2 = 4 \vee 3 = 4 \vee False$ <hr/> $False$
$\neg_{1/2}$	$H : 0 = 4$ <hr/> $False$
<i>inversion H.</i>	$\neg_{1/2}$ completed
$\neg_{2/2}$	$H : 1 = 4 \vee 2 = 4 \vee 3 = 4 \vee False$ <hr/> $False$
<i>destruct H.</i>	$H : 1 = 4$ <hr/> $False$
	$H : 2 = 4 \vee 3 = 4 \vee False$ <hr/> $False$
$*_{1/2}$	$H : 1 = 4$ <hr/> $False$

Continuing proof of Example elem\_not\_in\_list on the next page

Table 2 – Proof of Example `elem_not_in_list` continued

Next step in Coq	Proof situation
<i>inversion H.</i>	$*_{1/2}$ completed
$*_{2/2}$	$H : 2 = 4 \vee 3 = 4 \vee False$
	$False$
<i>destruct H.</i>	$H : 2 = 4$
	$False$
	$H : 3 = 4 \vee False$
	$False$
$+_{1/2}$	$H : 2 = 4$
	$False$
<i>inversion H.</i>	$+_{1/2}$ completed
$+_{2/2}$	$H : 3 = 4 \vee False$
	$False$
<i>destruct H.</i>	$H : 3 = 4$
	$False$
	$H : False$
	$False$
{	$H : 3 = 4$
	$False$
<i>inversion H.</i>	<i>Subproof</i> completed
}	$H : False$
	$False$

Continuing proof of Example `elem_not_in_list` on the next page

Table 2 – Proof of Example `elem_not_in_list` continued

Next step in Coq	Proof situation
{	$H : False$
	$False$
<i>contradiction.</i>	<i>Subproof</i> completed, proof completed by <code>Qed</code>
}	$+_{2/2}$ completed, proof completed by <code>Qed</code>

End of proof of Example `elem_not_in_list`

The first tactic unfolds the definition of  $\neg$  (not) in the goal, replacing our initial statement by `In 4 [0 ; 1 ; 2 ; 3]  $\rightarrow$  False`. The second tactic reduces the new goal by computing the function `In`, which leaves us with the disjunctions  $0 = 4 \vee 1 = 4 \vee 2 = 4 \vee 3 = 4 \vee False$  in the antecedent of the implication. Then, the tactic `intros` moves this antecedent to the proof context, introducing a new hypothesis `H` and leaving the literal `False` as goal.

From this point on, the proof develops a pattern: we perform a destruction followed by an inversion of the hypothesis until we can end the proof by contradiction. The recurrence of the tactic `destruct` lets us focus in one equality from the disjunction at a time: first the hypothesis becomes  $0 = 4$ , then  $1 = 4$  and so on. The tactic `inversion` finishes each sub proof created by the previous command by deriving all the necessary conditions that should hold for the assumption to be proved. In this case, since none of these equalities is true, there is no condition that satisfies the proposition, thus proving our goal.

Since this pattern in the sequence of tactics is now exposed, we can define a tactic macro for proving propositions of the format “not in” using the keyword `Ltac`:

```
Ltac solve_not_in := unfold not;
  let H := fresh "H" in (
    intros H; repeat (contradiction + (destruct H; [> inversion H | ]))
  ).
```

**Example** `elem_not_in_list'` :  $\neg$  (`In 4 [0 ; 1 ; 2 ; 3]`).

**Proof.** `solve_not_in. Qed.`

The new tactic `solve_not_in` works by first unfolding the function `not`, therefore deriving an implication, then moving the premise to the context of assumption, and finally repeating the following steps until the proof is finished: try to finish the proof by searching for a *contradiction* in the assumptions, if it fails, *destruct* the hypothesis and apply *inversion* to it in order to prove the first sub-goal yielded by the previous tactic.

See the proof for Example `elem_not_in_list'` in table 3

Table 3 – Proof of Example `elem_not_in_list'`

Next step in Coq	Proof situation
<i>Proof.</i>	<hr/> $\sim In\ 4\ [0; 1; 2; 3]$
<i>solve_not_in.</i>	Proof finished by Qed

End of proof of Example `elem_not_in_list'`

## 2.3 QuickChick

QuickChick is a set of tools and techniques for combining randomized property-based testing with formal specification and proof in the Coq ecosystem. It is the equivalent of Haskell’s QuickCheck for Coq proof assistant.

There are four basic elements in property-based random testing: an *executable property* such as for deciding whether a number is even, *generators* for random inputs to the property, *printers* for converting data structures like numbers to strings when reporting counterexamples, and *shrinkers*, which are used to search for minimal counterexamples when errors occur.

## 3 A theory for CSP in Coq

### 3.1 Syntax

#### 3.1.1 Abstract syntax

#### 3.1.2 Concrete syntax

### 3.2 Structured operational semantics

### 3.3 Labelled transition systems

#### 3.3.1 GraphViz integration

### 3.4 Traces refinement

#### 3.4.1 QuickChick integration

## 4 Conclusions

### 4.1 Related work

### 4.2 Future work

# Bibliography

CLAESSEN, K.; HUGHES, J. Quickcheck: A lightweight tool for random testing of haskell programs. In: *Proceedings of the Fifth ACM SIGPLAN International Conference on Functional Programming*. New York, NY, USA: Association for Computing Machinery, 2000. (ICFP '00), p. 268–279. ISBN 1581132026. Disponível em: <https://doi.org/10.1145/351240.351266>. Citado na página 13.

HOARE, C. A. R. Communicating sequential processes. *Communications of the ACM*, ACM New York, NY, USA, v. 21, n. 8, p. 666–677, 1978. Citado na página 13.

SCHNEIDER, S. *Concurrent and Real Time Systems: The CSP Approach*. 1st. ed. USA: John Wiley & Sons, Inc., 1999. ISBN 0471623733. Citado na página 10.