# PRÀCTICA 1 ARQUITECTURA DE COMPUTADORS: SIMULACIÓN PROCESADOR SUPERESCALAR

Alumnes: Carlos Martínez García-Villarrubia

Joel Teodoro Gómez

Curs 2022-2023

## Paràmetres

### Intel Core i9-12900KS

### Amd Ryzen 7 5800

3.

[Cache L1(instruction)](https://www.cpu-world.com/CPUs/Zen/AMD-Ryzen%207%205800X3D.html#:~:text=8%20x%2032%20KB%208%2Dway%20set%20associative%20instruction%20caches): 8x32KB 8-way set associative

[Cache L1(data)](https://www.cpu-world.com/CPUs/Zen/AMD-Ryzen%207%205800X3D.html#:~:text=8%20x%2032%20KB%208%2Dway%20set%20associative%20data%20caches):8x32KB 8-way set associative.

[Cache L2](https://www.cpu-world.com/CPUs/Zen/AMD-Ryzen%207%205800X3D.html#:~:text=8%20x%20512%20KB%208%2Dway%20set%20associative%20unified%20caches): 8x512KB 8-way set associative.

Algoritmo de reemplazo: LRU

4.

[Data width](https://www.cpu-world.com/CPUs/Zen/AMD-Ryzen%207%205800X3D.html#specs:~:text=Data%20width-,64%20bit,-The%20number%20of): 64bit

[Latency](https://www.tomshardware.com/reviews/amd-ryzen-7-5800x3d-review/2#:~:text=AIDA%20%2D%20L3%20Latency-,13.84%20ns,-11.49%20ns): 13,84ns

5.

[Texto

Descripción generada automáticamente](https://www.cpu-world.com/CPUs/Zen/AMD-Ryzen%207%205800X3D.html#specs:~:text=MMX%20instructions,Precision%20Boost%202)